# LTO 30

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Vishay Sfernice

### **30 W Power Resistor Thick Film Technology**

### FEATURES

• 30 W at 25 °C case temperature heatsink mounted



- Direct mounting ceramic on heatsink
- Broad resistance range: 0.010  $\Omega$  to 550 k $\Omega$
- Non inductive
- TO-220 package: Compact and easy to mount
- Material categorization: For definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DIMENSIONS in millimeters

Note

• Tolerances unless stated: ± 0.3 mm

STANDA	STANDARD ELECTRICAL SPECIFICATIONS								
MODEL	SIZE	RESISTANCE RANGE Ω	RATED POWER P <sub>25 °C</sub> W	LIMITING ELEMENT VOLTAGE U <sub>L</sub> V	TOLERANCE ± %	TEMPERATURE COEFFICIENT ± ppm/°C	$\begin{array}{c} CRITICAL\\ RESISTANCE\\ \Omega \end{array}$		
LTO 30	TO-220	0.010 to 550K	30	500	1, 2, 5, 10	150, 250, 700, 900	8.33K		

Flammability

MECHANICAL SPECIFICATIONS					
Mechanical Protection Molded					
Resistive Element	Thick film				
Substrate	Alumina				
Connections	Tinned copper				
Weight	2 g max.				
Mounting Torqure	1 Nm				

ENVIRONMENTAL SPECIFICATIONS				
Temperature Range- 55 °C to + 155 °C				
Climatic Category	55/155/56			

### ENVIRONMENTAL SPECIFICATIONS

IEC 60695-1	1-5
2 applications	30 s
separated by	60 s

TECHNICAL SPECIFICATIONS				
Dissipation and Associated	Onto a heatsink			
Power Rating and Thermal Resistance of the Component	30 W at + 25 °C (case temp.) R <sub>TH (j - c)</sub> : 4.2 °C/W Free air: 2.25 W at + 25 °C			
Temperature Coefficient	See Performance table			
Standard	± 150 ppm/°C			
Dielectric Strength MIL STD 202	1500 V <sub>RMS</sub> - 1 min 10 mA max.			

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1 For technical questions, contact: <u>sferfixedresistors@vishay.com</u> Document Number: 50049



LTO series are the extension of RTO types. We used the direct ceramic mounting design (no metal tab) of our RCH power resistors applied to semiconductor packages.

Load Life

Vibration

Humidity (Steady State)

P: Expressed in W
$\Delta T:$ Difference between maximum working temperature and room
temperature

R<sub>TH (j - c)</sub>: Thermal resistance value measured between resistive layer and outer side of the resistor. It is the thermal resistance of the component.

#### Example:

R<sub>TH (c - a)</sub> for LTO 30 power rating 10 W at ambient temperature + 25 °C Thermal resistance R<sub>TH (j - c)</sub>: 4.2 °C/W

Considering equation <sup>(1)</sup> we have:

$$\begin{split} &\Delta T = 150 \ ^{\circ}C \ - \ 25 \ ^{\circ}C = 125 \ ^{\circ}C \\ &R_{TH \ (j \ - \ c)} \ + \ R_{TH \ (c \ - \ h)} \ + \ R_{TH \ (h \ - \ a)} = \frac{\Delta T}{P} \ = \frac{125}{10} \ = 12.5 \ ^{\circ}C/W \\ &R_{TH \ (c \ - \ h)} \ \ R_{TH \ (h \ - \ a)} = 12.5 \ ^{\circ}C/W \ - \ 4.2 \ ^{\circ}C/W \ = 8.3 \ ^{\circ}C/W \end{split}$$

For te

with a thermal grease  $R_{TH (c-h)} = 1 \text{ °C/W}$ , we need a heatsink with  $R_{TH (h-a)} = 7.3 \text{ °C/W}$ .

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50049

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Terminal Strength	=	-STD-202 211 cond. A1	± (0.2 % + 0.005 Ω)		
Shock		/IL-STD-202 I 213 cond. I	± (0.5 % + 0.005 Ω)		
SPECIAL FEATURES					
Resistance Values	≥ 0.010	≥ 0.015	≥ 0.1	≥ 0.5	
Tolerances	± 1 % at ± 10 %				
Typical Temperature Coefficient (- 55 ° to + 155 °C)	± 900 ppm/°C	± 700 ppm/°C	± 250 ppm/°C	± 150 ppm/°C	

CONDITIONS

EN 60115-1

1.5 Pr/5 s

 $U_{\rm S} < 1.5 U_{\rm L}$ EN 60115-1 IEC 60068-2-14 Test Na

> 5 cycles - 55 °C to + 155 °C EN 60115-1

1000 h Pr at + 25 °C MIL-STD-202

method 103 B cond. D MIL-STD-202

method 204 cond. D MIL-STD-202

### CHOICE OF THE HEATSINK

The user must choose according to the working conditions of the component (power, room temperature). Maximum working temperature must not exceed 155 °C. The dissipated power is simply calculated by the following ratio:

$$\mathsf{P} = \frac{\Delta \mathsf{T}}{\mathsf{R}_{\mathsf{TH} (j-c)} + \mathsf{R}_{\mathsf{TH} (c-h)} + \mathsf{R}_{\mathsf{TH} (h-a)}}^{(1)}$$

R<sub>TH (c - h)</sub>: Thermal resistance value measured between outer side of the resistor and upper side of the heatsink. This is the thermal resistance of the interface (grease, thermal pad), and the quality of the fastening device.

 $R_{TH (h - a)}$ : Thermal resistance of the heatsink.

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 $\geq 10^4 \text{ M}\Omega$ 

≤ 0.1 µH

**TECHNICAL SPECIFICATIONS** 

REQUIREMENTS

± (0.5 % + 0.005 Ω)

± (0.5 % + 0.005 Ω)

 $\pm (1 \% + 0.005 \Omega)$ 

± (0.5 % + 0.005 Ω)

 $\pm (0.2 \% + 0.005 \Omega)$ 



Inductance

TESTS

Insulation Resistance

PERFORMANCE

**Momentary Overload** 

**Rapid Temperature Change** 

### LTO 30



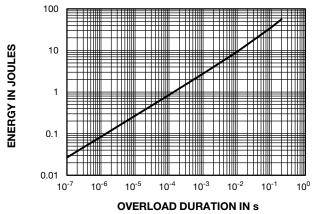
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#### **OVERLOADS**

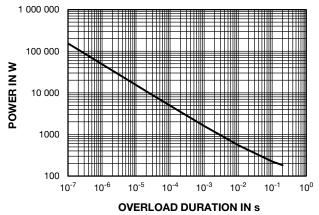
In any case the applied voltage must be lower than the maximum overload voltage of 750 V.

The values indicated on the graph below are applicable to resistors in air or mounted onto a heatsink.

#### ENERGY CURVE



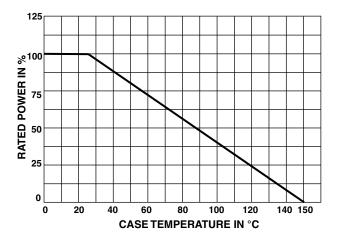
#### **POWER CURVE**



#### POWER RATING

The temperature of the case should be maintained within the limits specified.

To improve the thermal conductivity, surfaces in contact should be coated with a silicone grease and the torque applied on the screw for tightening should be around 1 Nm.



PACKAGING	
Tube of 50 units	

#### MARKING

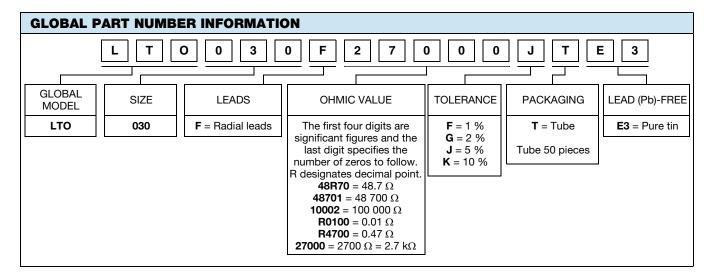
Model, style, resistance value (in  $\Omega$ ), tolerance (in %), manufacturing date, Vishay Sfernice trademark.

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### Vishay Sfernice

ORDERI	ORDERING INFORMATION									
LTO	30	F	2.7 kΩ	±1%	XXX	TU50	e3			
MODEL	STYLE	CONNECTIONS	RESISTANCE VALUE	TOLERANCE ± 1 % ± 2 % ± 5 % ± 10 %	CUSTOM DESIGN Optional on request: Special TCR, shape etc.	PACKAGING	LEAD (Pb)-FREE			





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