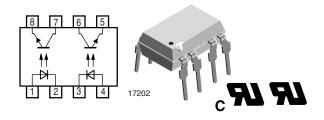
Vishay Semiconductors



Optocoupler, Phototransistor Output, Dual Channel



DESCRIPTION

The MCT6H and MCT62H consist of a phototransistor optically coupled to a gallium arsenide infrared emitting diode in a 6-lead plastic dual inline package.

The elements are mounted on one leadframe, providing a fixed distance between input and output for highest safety requirements.

FEATURES

- Current transfer ratio (CTR) of typical 100 %
- Isolation test voltage V_{ISO} = 5300 V_{RMS}
- · Low temperature coefficient of CTR
- Low coupling capacitance of typical 0.3 pF
- · Wide ambient temperature range
- · Lead (Pb)-free component
- · Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC



APPLICATIONS

- · Galvanically separated circuits
- · Non-interacting switches

AGENCY APPROVALS

• UL1577, file no. E76222 system code U, double protection

ORDER INFORMATION	
PART	REMARKS
MCT6H	CTR > 50 %, DIP-8
MCT62H	CTR > 100 %, DIP-8

Note

MCT6H and MCT62H are marked as MCT6 and MCT62 respectively.

ABSOLUTE MAXIMUM RATINGS (1)							
PARAMETER	TEST CONDITION	ST CONDITION PART SYMBOL		VALUE	UNIT		
INPUT							
Reverse voltage			V _R	6	V		
Forward current			I _F	60	mA		
Forward surge current	t _p ≤ 10 μs		I _{FSM}	1.5	Α		
Power dissipation			P _{diss}	100	mW		
Junction temperature			Tj	125	°C		
OUTPUT							
Collector emitter voltage			V _{CEO}	70	V		
Emitter collector voltage			V _{ECO}	7	V		
Collector current			Ic	50	mA		
Collector peak current	$t_p/T = 0.5, t_p \le 10 \text{ ms}$		I _{CM}	100	mA		
Power dissipation			P _{diss}	150	mW		
Junction temperature			T _j	125	°C		
COUPLER							
AC isolation test voltage (RMS)	t = 1.0 min		V _{ISO}	5000	V_{RMS}		
Total power dissipation			P _{tot}	250	mW		
Ambient temperature range			T _{amb}	- 55 to + 100	°C		
Storage temperature range			T _{stg}	- 55 to + 125	°C		
Soldering temperature (2)	2 mm from case, t ≤ 10 s		T _{sld}	260	°C		

T_{amb} = 25 °C, unless otherwise specified. Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

(2) Refer to wave profile for soldering conditions for through hole devices.





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ELECTRICAL CHARACTERISTICS (1)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT								
Forward voltage	I _F = 50 mA	V _F		1.25	1.6	V		
OUTPUT								
Collector emitter voltage	I _C = 1 mA	V _{CEO}	70			V		
Emitter collector voltage	I _E = 100 μA	V _{ECO}	7			V		
Collector dark current	$V_{CE} = 20 \text{ V}, I_F = 0, E = 0$	I _{CEO}			100	nA		
COUPLER	•			•				
DC isolation test voltage	t = 2 s	V_{ISO}	5000			V_{RMS}		
Isolation resistance	V _{IO} = 1000 V, 40 % relative humidity	R _{IO}		10 ¹²		Ω		
Collector emitter saturation voltage	I _F = 10 mA, I _C = 1 mA	V _{CEsat}			0.3	V		
Cut off frequency	I_F = 10 mA, V_{CE} = 5 V, R_L = 100 Ω	f _C		100		kHz		
Coupling capacitance	f = 1 MHz	C _k		0.3		pF		

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I _C /I _F	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	МСТ6Н	CTR	50	100		%
	$V_{CE} = 5 \text{ V}, I_F = 10 \text{ mA}$	МСТ6Н	CTR	60	120		%
	$V_{CE} = 5 \text{ V}, I_F = 5 \text{ mA}$	MCT62H	CTR	100	200		%

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Delay time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _d		3		μs	
Rise time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _r		3		μs	
Fall time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _f		4.7		μs	
Storage time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _s		0.3		μs	
Turn-on time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _{on}		6		μs	
Turn-off time	$V_S = 5 \text{ V}, I_C = 2 \text{ mA}, R_L = 100 \Omega$ (see figure 1)	t _{off}		5		μѕ	

 $^{^{(1)}}$ T_{amb} = 25 $^{\circ}$ C, unless otherwise specified.

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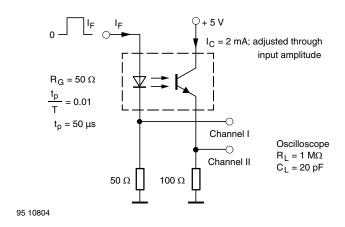


Fig. 1 - Test Circuit, non Saturated Operation

TYPICAL CHARACTERISTICS

T_{amb} = 25 °C, unless otherwise specified

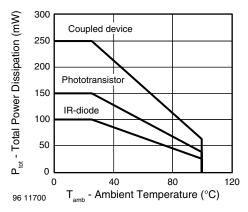


Fig. 3 - Total Power Dissipation vs. Ambient Temperature

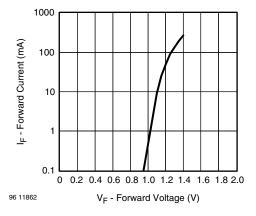


Fig. 4 - Forward Current vs. Forward Voltage

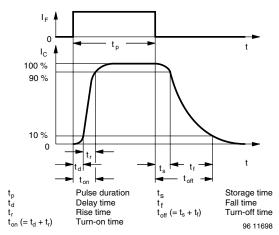


Fig. 2 - Switching Times

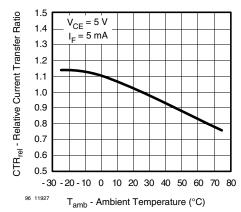


Fig. 5 - Relative Current Transfer Ratio vs. Ambient Temperature

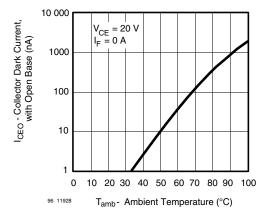


Fig. 6 - Collector Dark Current vs. Ambient Temperature



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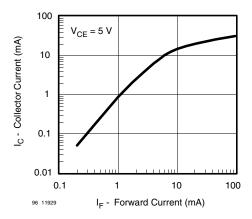


Fig. 7 - Collector Current vs. Forward Current

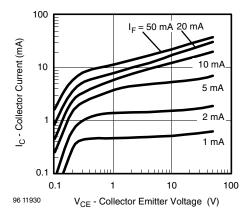


Fig. 8 - Collector Current vs. Collector Emitter Voltage

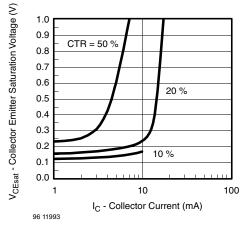


Fig. 9 - Collector Emitter Saturation Voltage vs. Collector Current

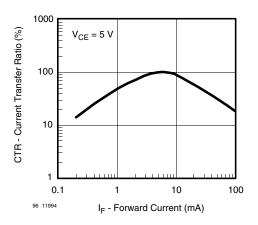


Fig. 10 - Current Transfer Ratio vs. Forward Current

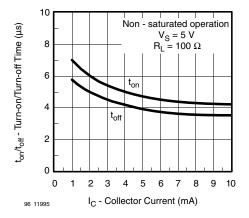


Fig. 11 - Turn-on/Turn-off Time vs. Collector Current

МСТ6Н, МСТ62Н

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Drawing-No.: 6.544-5302.02-4

Issue: 4; 02.06.99

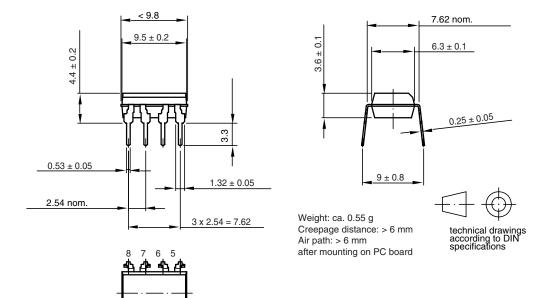
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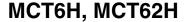
3



14784

PACKAGE DIMENSIONS in inches (millimeters)







Optocoupler, Phototransistor Output, Dual Channel

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OZONE DEPLETING SUBSTANCES POLICY STATEMENT

It is the policy of Vishay Semiconductor GmbH to

- 1. Meet all present and future national and international statutory requirements.
- 2. Regularly and continuously improve the performance of our products, processes, distribution and operating systems with respect to their impact on the health and safety of our employees and the public, as well as their impact on the environment.

It is particular concern to control or eliminate releases of those substances into the atmosphere which are known as ozone depleting substances (ODSs).

The Montreal Protocol (1987) and its London Amendments (1990) intend to severely restrict the use of ODSs and forbid their use within the next ten years. Various national and international initiatives are pressing for an earlier ban on these substances.

Vishay Semiconductor GmbH has been able to use its policy of continuous improvements to eliminate the use of ODSs listed in the following documents.

- 1. Annex A, B and list of transitional substances of the Montreal Protocol and the London Amendments respectively.
- Class I and II ozone depleting substances in the Clean Air Act Amendments of 1990 by the Environmental Protection Agency (EPA) in the USA.
- 3. Council Decision 88/540/EEC and 91/690/EEC Annex A, B and C (transitional substances) respectively.

Vishay Semiconductor GmbH can certify that our semiconductors are not manufactured with ozone depleting substances and do not contain such substances.

We reserve the right to make changes to improve technical design and may do so without further notice.

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Vishay Semiconductor GmbH, P.O.B. 3535, D-74025 Heilbronn, Germany



Vishay

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