RoHS

COMPLIANT

HALOGEN

FREE





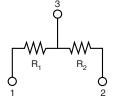
Molded, SOT-23 Thin Film Resistor, Surface Mount Network





Vishay Dale Thin Film MPM Series Dividers provide $\pm\,2$ ppm/°C tracking and a ratio tolerance as tight as 0.01 %, small size, and exceptional stability for all surface mount applications. The standard SOT-23 package format with unity and common standard resistance divider ratios provide easy selection for most applications requiring matched pair resistor elements. The ratios listed are available for off the shelf delivery. If you require a non-standard ratio, consult the applications engineering group as we may be able to meet your requirements.

SCHEMATIC



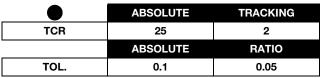
FEATURES

- Excellent long term ratio stability ($\Delta R \pm 0.015$ %, 2000 h, + 70 °C)
- Ratio tolerances to ± 0.01 %
- Low TCR tracking ± 2 ppm
- Standard JEDEC TO-236 package variation AB
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition

Note

Pb containing terminations are not RoHS compliant, exemptions may apply

TYPICAL PERFORMANCE



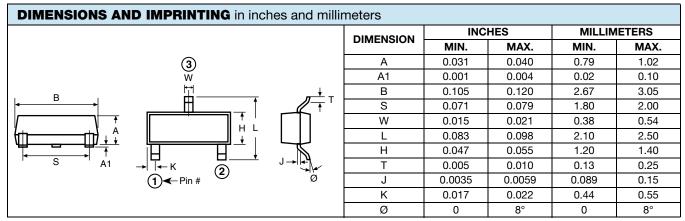
STANDARD DIVIDER RATIO (R ₂ /R ₁)			
RATIO	R ₂ (Ω)	R ₁ (Ω)	
100:1	100K	1K	
50:1	50K	1K	
25:1	25K	1K	
20:1	20K	1K	
10:1	10K	1K	
9:1	9K	1K	
6:1	6K	1K	
5:1	10K	2K	
5:1	5K	1K	
4:1	8K	2K	
4:1	4K	1K	
2:1	10K	5K	
2:1	2K	1K	
1:1	50K	50K	
1:1	25K	25K	
1:1	10K	10K	
1:1	5K	5K	
1:1	2.5K	2.5K	
1:1	1K	1K	
1:1	500	500	
1:1	250	250	

STANDARD ELECTRICAL SPECIFICATIONS			
TEST	SPECIFICATIONS	CONDITIONS	
Material	Passivated nichrome	-	
Pin/Lead Number	3	-	
Resistance Range	250 Ω to 100 k Ω per resistor	-	
TCR: Absolute	± 25 ppm/°C	- 55 °C to + 125 °C	
TCR: Tracking	± 2 ppm/°C (typical)	- 55 °C to + 125 °C	
Tolerance: Absolute	± 0.05 % to ± 1.0 %	+ 25 °C	
Tolerance: Ratio	± 0.01 % to 0.5 %	+ 25 °C	
Power Rating: Resistor	100 mW	Maximum at + 70 °C	
Power Rating: Package	200 mW	Maximum at + 70 °C	
Stability: Absolute	ΔR ± 0.05 %	2000 h at + 70 °C	
Stability: Ratio	ΔR ± 0.015 %	2000 h at + 70 °C	
Voltage Coefficient	0.1 ppm/V	=	
Working Voltage	100 V max. not to exceed √P x R	-	
Operating Temperature Range	- 55 °C to + 125 °C	-	
Storage Temperature Range	- 55 °C to + 150 °C	-	
Noise	< - 30 dB	-	
Thermal EMF	0.2 μV/°C	-	
Shelf Life Stability: Absolute	ΔR ± 0.01 %	1 year at + 25 °C	
Shelf Life Stability: Ratio	$\Delta R \pm 0.002 \%$	1 year at + 25 °C	

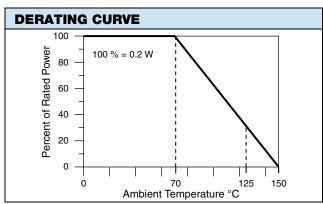
Revision: 20-Oct-11 Document Number: 60001

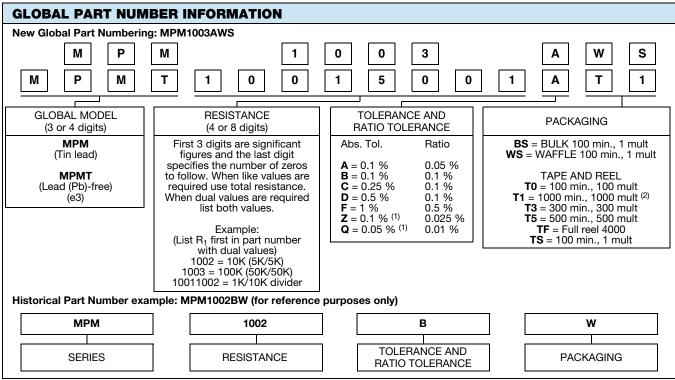


Vishay Dale Thin Film



MECHANICAL SPECIFICATIONS			
Resistive Element	Passivated nichrome		
Substrate Material	Silicon		
Body	Molded epoxy		
Terminals	Copper alloy		
Lead (Pb)-free Option	100 % matte tin		
Tin Lead Option	Sn85		
Tin Lead and Lead (Pb)-free Finish	Plated		





Notes

(1) Tol. available 1K and up equal values only

(2) Preferred packaging code



Legal Disclaimer Notice

Vishay

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Please note that some Vishay documentation may still make reference to RoHS Directive 2002/95/EC. We confirm that all the products identified as being compliant to Directive 2002/95/EC conform to Directive 2011/65/EU.

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