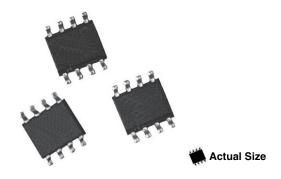
ORN (Divider)



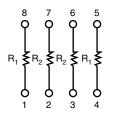
Vishay Dale Thin Film

Molded, 50 mil Pitch, Dual-In-Line Thin Film Divider, Surface Mount Resistor Network



Vishay Dale Thin Film ORN series Dividers provide optimum ratio precision, small size and exceptional stability for most applications. They offer a wide ratio range that is listed in the selection guide and are available for immediate delivery. The tight ratio tolerance offered on the standard ratios will provide exceptional performance throughout life.

SCHEMATIC



FEATURES

- 0.068" (1.73 mm) maximum seated height
- Rugged molded case construction with no internal solder (JEDEC MS-012 variation AA package)
- Low TCR tracking ± 5 ppm/°C
- Compliant to RoHS Directive 2002/95/EC
- Halogen-free according to IEC 61249-2-21 definition

Note

* Pb containing terminations are not RoHS compliant, exemptions may apply

TYPICAL PERFORMANCE

\bullet	ABSOLUTE	TRACKING
TCR	25	5
	ABSOLUTE	RATIO
TOL.	0.1	0.05

STANDARD RESISTANCE OFFERING (R1/R2)				
RATIO	R ₁	R ₂		
100:1	100K	1K		
50:1	50K	1K		
25:1	25K	1K		
20:1	20K	1K		
10:1	10K	1K		
5:1	10K	2K		
2:1	10K	5K		

TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome	-
Pin/Lead Number	8	-
Resistance Range	1000 Ω to 100 k Ω per resistor	-
TCR: Absolute	± 25 ppm/°C	- 55 °C to + 125 °C
TCR: Tracking	± 5 ppm/°C	- 55 °C to + 125 °C
Tolerance: Absolute	± 0.1 %	+ 25 °C
Tolerance: Ratio	± 0.05 %	+ 25 °C
Power Rating: Resistor	100 mW	Maximum at + 70 °C
Power Rating: Package	400 mW	Maximum at + 70 °C
Stability: Absolute	$\Delta R \pm 0.05 \%$	2000 h at + 70 °C
Stability: Ratio	∆ <i>R</i> ± 0.015 %	2000 h at + 70 °C
Voltage Coefficient	< 0.1 ppm/V	-
Working Voltage	100 V max. not to exceed $\sqrt{P \times R}$	-
Operating Temperature Range	- 55 °C to + 125 °C	-
Storage Temperature Range	- 55 °C to + 150 °C	-
Noise	< - 30 dB	-
Thermal EMF	0.08 µV/°C	-
Shelf Life Stability: Absolute	∆ <i>R</i> ± 0.01 %	1 year at + 25 °C
Shelf Life Stability: Ratio	$\Delta R \pm 0.002 \%$	1 year at + 25 °C

Note

Tantalum nitride film is custom, consult factory

Revision: 20-Oct-11

Document Number: 60006



ORN (Divider)



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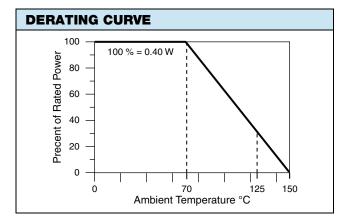
Vishay Dale Thin Film

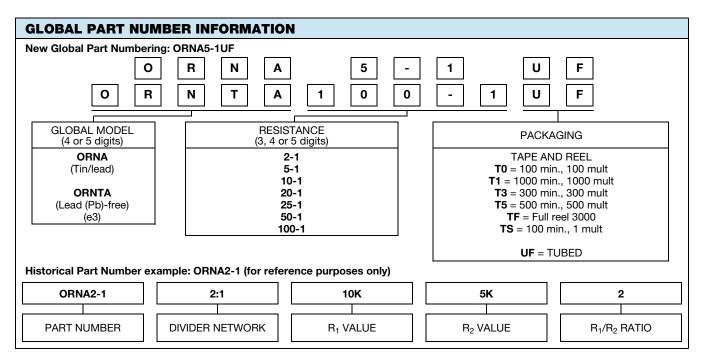
DIMENSIONS AND IMPRINTING in inches and millimeters				
B-► ━- [►] ━- E	DIMENSION	INCHES	MILLIMETERS	
C ← Part	A	0.157	3.99	
	В	0.0165 ± 0.005	0.4 ± 0.06	
	С	0.050	1.27	
	D	0.195 max.	4.93	
	E	0.008 ± 0.001	0.20 ± 0.03	
	F	0.028 ± 0.001	0.71 ± 0.02	
Ø	G	0.239 ± 0.005	6.07 ± 0.13	
	Н	0.068 max.	1.73	
	I	0.008 ± 0.002	0.22 ± 0.06	
	Ø	2° to 6°	2° to 6°	

Note

Marking - Vishay symbol, part number from ordering information

MECHANICAL SPECIFICATIONS			
Resistive Element	Passivated nichrome		
Substrate Material	Silicon		
Body	Molded epoxy		
Terminals	Copper alloy		
Lead (Pb)-free Option	100 % matte tin		
Tin Lead Option	Sn90		
Tin Lead and Lead (Pb)-free Finish	Plated		





For technical questions, contact: thinfilm@vishay.com

Document Number: 60006

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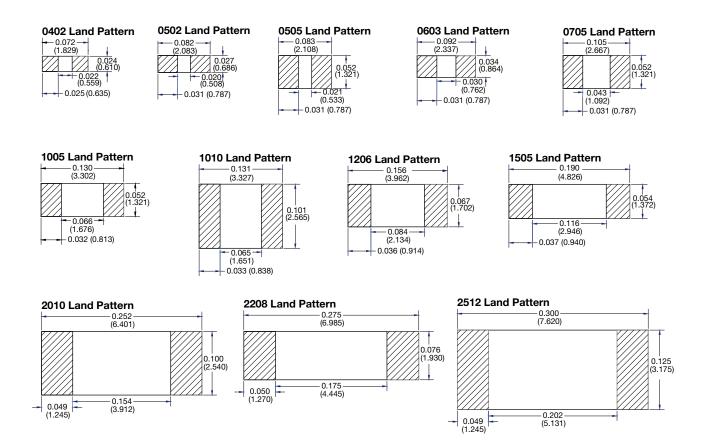
Vishay Dale Thin Film Land Patterns

1. Scope

This technical note provides sample land patterns for Vishay Dale Thin Film SMT resistive products. The following drawings are based on IPC-SM-782 Surface Mount Design and Land Pattern Standard. These drawings are for reference only Vishay Thin Film recommends that the user contacts their PC board supplier for actual land patterns required. The pads are intended for lead (Pb)-free and tin / lead solder types.

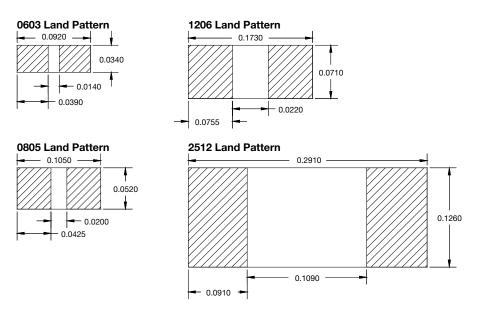
2. Product Series

Thin Film Surface Mount Chip Resistors (FC, L, P, PTN, PLT, PLTT. PLTU, PAT, PATT, PNM, M/D55342 QPL Series)

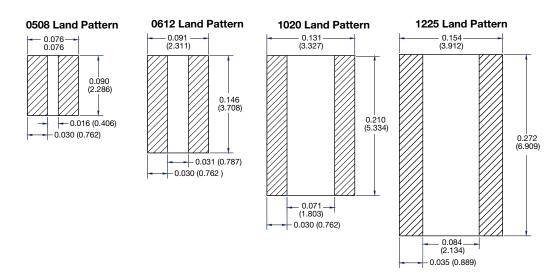




Thin Film Surface Mount Chip Resistors (PHP, PCAN Series)



Thin Film Surface Mount Chip Resistors Long Axis Termination (L Series)



SC70-4 (MP4)

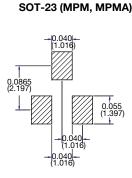
0.038

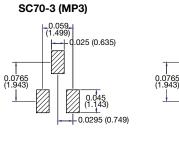
-0.025 (0.635)

045 143

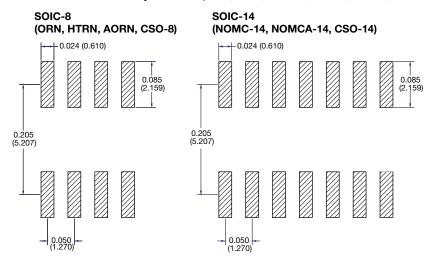


Surface Mount Networks (MPM, MP3, MP4 Series)

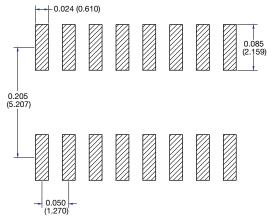




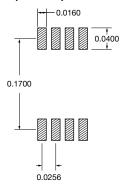
Surface Mount Networks SOIC Narrow Body 150 mils (ORN, CSO, MOMC, HTRN, AORN, MORN Series)



SOIC-16 (NOMC-16, NOMCA-16, CSO-16, VSOR-16)



MORN MSOP MO-187AA (MORN-8)

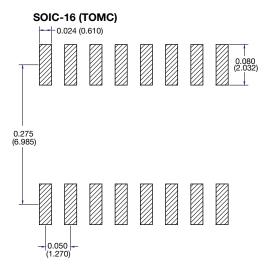


3 For technical questions, contact: <u>thinfilm@vishay.com</u> Document Number: 60119

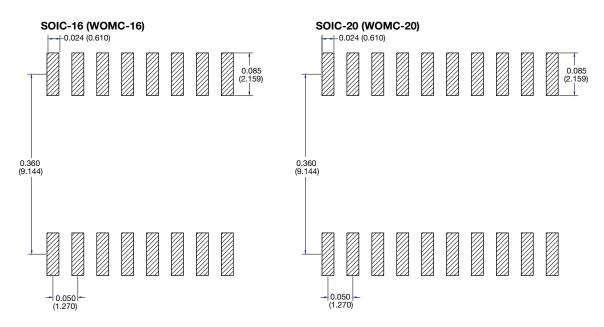
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Surface Mount Networks SOIC Medium Body 220 mils (TOMC Series)

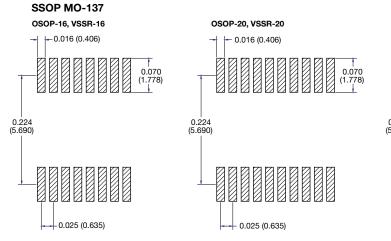


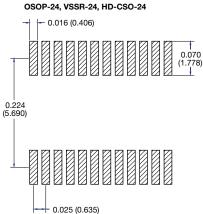
Surface Mount Networks SOIC Wide Body 300 mils (WOMC Series)

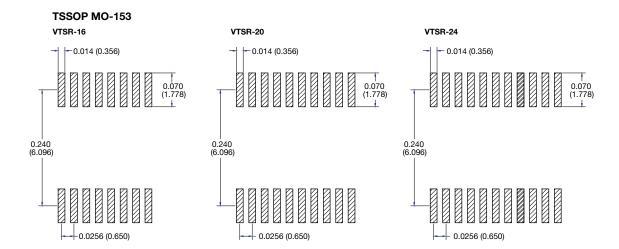




Surface Mount Networks High Density SSOP, TSOP (VSSR, VTSR Series)

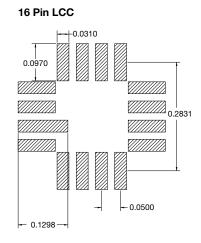


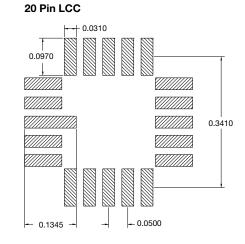




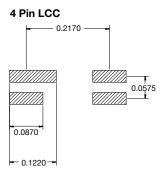


Surface Mount Leadless Networks (LCC Series)





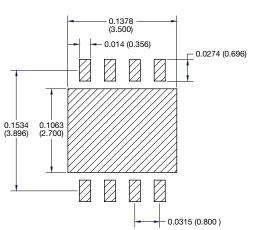
Surface Mount Leadless Networks (MPH Series)



Surface Mount Leadless Packages DUAL/ QUAD Flat No Lead (DFN, QFN Series)

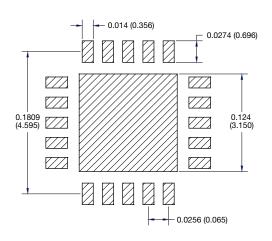


DFN-8 4 x 5 mm Sq



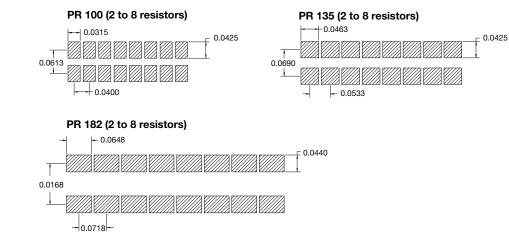
QFN MLP

QFN-20 5 x 5 mm Sq



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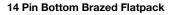
Surface Mount Leadless Resistor Arrays (PR Series)

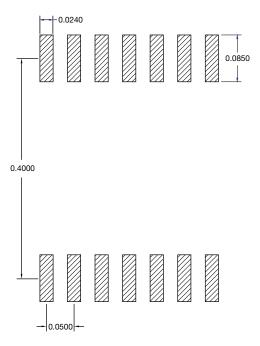


Note

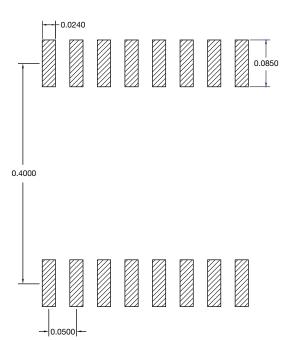
• All dimensions in inches (mm)

Flatpack





16 Pin Bottom Brazed Flatpack



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