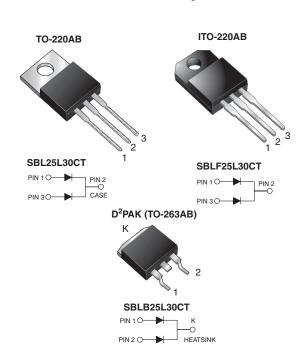


## SBL25L30CT, SBLF25L30CT, SBLB25L30CT

Vishay General Semiconductor

# **Dual Low V<sub>F</sub> Common Cathode Schottky Rectifier**



#### **DESIGN SUPPORT TOOLS**

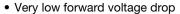
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PRIMARY CHARACTERISTICS						
I <sub>F(AV)</sub>	2 x 12.5 A					
$V_{RRM}$	30 V					
I <sub>FSM</sub>	180 A					
V <sub>F</sub>	0.39 V					
T <sub>J</sub> max.	150 °C					
Package	TO-220AB, ITO-220AB, D <sup>2</sup> PAK (TO-263AB)					
Circuit configuration	Common cathode					

#### **FEATURES**

- Power pack
- Low power loss, high efficiency



- High forward surge capability
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C (for D<sup>2</sup>PAK (TO-263AB) package)
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB and ITO-220AB package)
- AEC-Q101 qualified (for ITO-220AB and D<sup>2</sup>PAK (TO-263AB) package)
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912"><u>www.vishay.com/doc?99912</u></a>

#### TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, switching mode power supplies, freewheeling diodes, OR-ing diodes, DC/DC converters, and polarity protection application.

### **MECHANICAL DATA**

Case: TO-220AB, ITO-220AB, D<sup>2</sup>PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-E3 - RoHS-compliant, commercial grade Base P/NHE3\_X - RoHS-compliant, AEC-Q101 qualified ("X" denotes revision code, e.g. A, B, ...)

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test, HE3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>C</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	SBL25L30CT	UNIT	
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	30	V	
Maximum average forward rectified current at $T_C = 95^{\circ}\text{C}$	total device	1	25	А	
	per diode	I <sub>F(AV)</sub>	12.5		
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I <sub>FSM</sub>	180		
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-55 to +150	°C	
Isolation voltage (ITO-220AB only) from terminal to heatsink, t = 1 min		V <sub>AC</sub>	1500	V	



# SBL25L30CT, SBLF25L30CT, SBLB25L30CT

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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>C</sub> = 25 °C unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS		VALUE	UNIT		
Maximum instantaneous forward voltage	V <sub>F</sub> <sup>(1)</sup>	12.5 A	T <sub>J</sub> = 125 °C	0.39	V		
			T <sub>J</sub> = 25 °C	0.49			
Maximum instantaneous reverse current at DC blocking voltage per diode	I <sub>R</sub> <sup>(2)</sup>	Rated V <sub>R</sub>	T <sub>J</sub> = 25 °C	0.90			
			T <sub>J</sub> = 100 °C	50	mA		
			T <sub>J</sub> = 125 °C	100			

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: pulse width  $\leq$  40 ms

THERMAL CHARACTERISTICS (T <sub>C</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SBL	SBLF	SBLB	UNIT
Typical thermal resistance from junction to case per diode	$R_{ heta JC}$	1.5	4.0	1.5	°C/W

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-220AB	SBL25L30CT-E3/45	1.85	45	50/tube	Tube	
ITO-220AB	SBLF25L30CT-E3/45	1.99	45	50/tube	Tube	
TO-263AB	SBLB25L30CT-E3/45	1.35	45	50/tube	Tube	
TO-263AB	SBLB25L30CT-E3/81	1.35	81	800/reel	Tape and reel	
ITO-220AB	SBLF25L30CTHE3_A/P (1)	1.99	Р	50/tube	Tube	
TO-263AB	SBLB25L30CTHE3_B/P (1)	1.35	Р	50/tube	Tube	
TO-263AB	SBLB25L30CTHE3_B/I (1)	1.35	I	800/reel	Tape and reel	

#### Note

(1) AEC-Q101 qualified, available in ITO-220AB and D2PAK (TO-263AB)

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### **RATINGS AND CHARACTERISTICS CURVES** (T<sub>C</sub> = 25 °C unless otherwise noted)

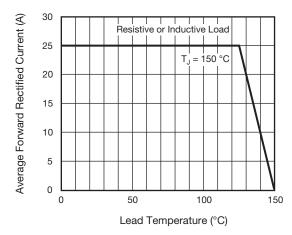


Fig. 1 - Forward Current Derating Curve

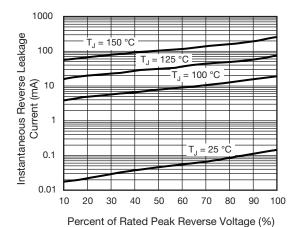


Fig. 4 - Typical Reverse Characteristics Per Diode

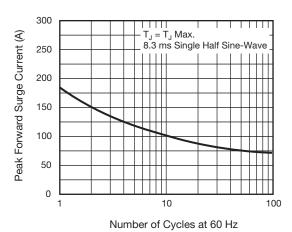


Fig. 2 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

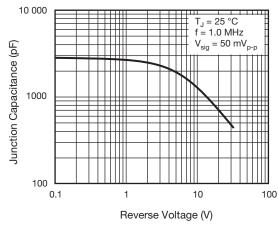


Fig. 5 - Typical Junction Capacitance Per Diode

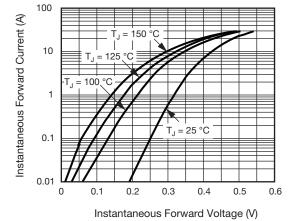


Fig. 3 - Typical Instantaneous Forward Characteristics
Per Diode

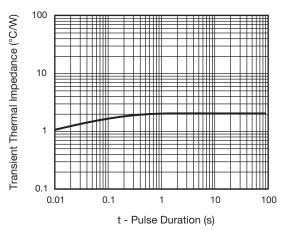


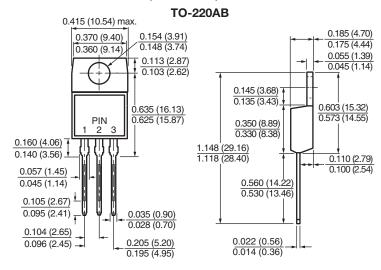
Fig. 6 - Typical Transient Thermal Impedance Per Diode

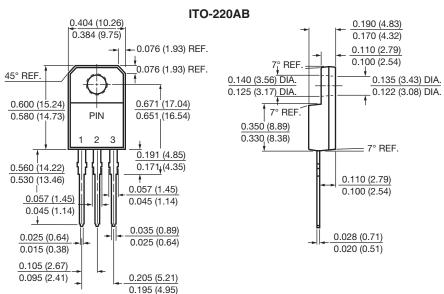




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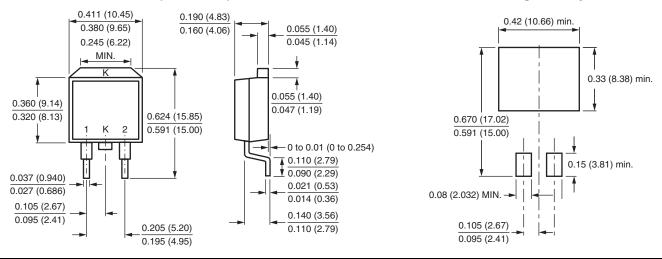
### **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





### D<sup>2</sup>PAK (TO-263AB)

### **Mounting Pad Layout**





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