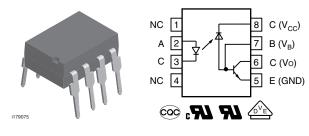
High Speed Optocoupler, 1 MBd, Transistor Output



LINKS TO ADDITIONAL RESOURCES

www.vishay.com



Models

DESCRIPTION

The SFH6135 and SFH6136 optocouplers feature a high signal transmission rate and a high isolation resistance. They have a GaAlAs infrared emitting diode, optically coupled with an integrated photo detector which consists of a photo diode and a high-speed transistor in a DIP-8 plastic package. Signals can be transmitted between two electrically separated circuits up to frequencies of 2 MHz. The potential difference between the circuits to be coupled should not exceed the maximum permissible reference voltages.

FEATURES

- Isolation test voltage 5300 V_{RMS}
- TTL compatible
- High bit rates: 1 MBit/s
- High common mode interference immunity
- Bandwidth 2 MHz
- Open collector output
- External base wiring possible
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

AGENCY APPROVALS

- <u>UL 1577</u>
- <u>cUL 1577</u>
- DIN EN 60747-5-5 (VDE 0884-5)
- <u>CQC</u>

ORDERING INFORMATION		
SFH613#	- X 0 # # T PACKAGE OPTION TAPE AND REEL	DIP-8 Option 6 7.62 mm Option 7 Option 8 Option 9 9.27 mm > 0.7 mm
AGENCY CERTIFIED / PACKAGE	CTR	R (%)
UL, cUL, CQC	≥7	≥ 19
DIP-8	SFH6135	SFH6136
SMD-8, option 7	SFH6135-X007T	SFH6136-X007
SMD-8, option 9	-	SFH6136-X009T
VDE, UL, cUL, CQC	≥7	≥ 19
DIP-8	-	SFH6136-X001
DIP-8, 400 mil, option 6	-	SFH6136-X016
SMD-8, option 7	-	SFH6136-X017T ⁽¹⁾
SMD-8, option 8	-	SFH6136-X018
SMD-8, option 9	_	SFH6136-X019

Notes

Additional options may be possible, please contact sales office

⁽¹⁾ Also available in tubes; do not add T to end

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ABSOLUTE MAXIMUM R	ATINGS (T _{amb} = 25 °C, unless oth	nerwise specifie	ed)	
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
INPUT				
Reverse voltage		V _R	3	V
Forward current		١ _F	25	mA
Peak forward current	t = 1 ms, duty cycle 50 %	I _{FM}	50	mA
Maximum surge forward current	$t \le 1 \ \mu s$, 300 pulses/s	I _{FSM}	1	А
Thermal resistance		R _{thja}	700	K/W
Power dissipation		P _{diss}	45	mW
OUTPUT	· · · · ·			
Supply voltage		Vs	-0.5 to 30	V
Output voltage		Vo	-0.5 to 25	V
Emitter base voltage		V _{EBO}	5	V
Output current		Ι _Ο	8	mA
Maximum output current		Ι _Ο	16	mA
Base current		Ι _Β	5	mA
Thermal resistance		R _{thja}	300	K/W
Power dissipation	T _{amb} = 70 °C	P _{diss}	100	mW
COUPLER				
Isolation test voltage		V _{ISO}	5300	V _{RMS}
Pollution degree (DIN VDE 0110)			2	
logistion registeres	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω
Isolation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω
Storage temperature range		T _{stg}	-55 to +125	°C
Ambient temperature range		T _{amb}	-55 to +100	°C
Soldering temperature ⁽¹⁾	Max. ≤ 10 s, dip soldering ≥ 0.5 mm distance from case bottom	T _{sld}	260	°C

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

⁽¹⁾ Refer to wave profile for soldering conditions for through hole devices

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT								
Forward voltage	I _F = 16 mA		V _F	-	1.6	1.9	V	
Breakdown voltage	I _R = 10 μA		V _{BR}	3	-	-	V	
Reverse current	V _R = 3 V		I _R	-	0.5	10	μA	
Capacitance	$V_R = 0 V, f = 1 MHz$		Co	-	125	-	pF	
Temperature coefficient of forward voltage	I _F = 16 mA		$\Delta V_F / \Delta T_{amb}$	-	1.7	-	mV/°C	
OUTPUT								
Logic low supply current	$I_F = 16 \text{ mA}, V_O = \text{open}, V_{CC} = 15 \text{ V}$		I _{CCL}	-	150	-	μA	
Logic high supply current	$I_F = 0 V, V_O = open, V_{CC} = 15 V$		I _{CCH}	-	0.01	1	μA	
	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, I_O = 1.1 \text{ mA}$	SFH6135	V _{OL}	-	0.1	0.4	V	
Output voltage, output low	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, I_O = 2.4 \text{ mA}$	SFH6136	V _{OL}	-	0.1	0.4	V	
Output ourropt, output high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		I _{OH}	-	3	500	nA	
Output current, output high	$I_F = 0 \text{ mA}, V_O = V_{CC} = 15 \text{ V}$		I _{OH}	-	0.01	1	μA	

SFH6135, SFH6136



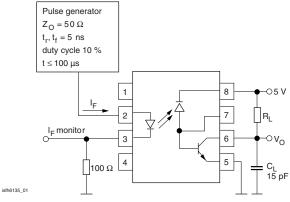
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ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER TEST CONDITION PART SYMBOL MIN. TYP. MAX. UNIT							UNIT
COUPLER							
Capacitance (input to output) f = 1 MHz C _{IO} - 0.6 - pF							

Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements

CURRENT TRANSFER RATIO							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Current transfer ratio	$I_{\rm E} = 16 \text{ mA}$. $V_{\rm O} = 0.4 \text{ V}$. $V_{\rm CC} = 4.5 \text{ V}$	SFH6135	CTR	7	16	-	%
		SFH6136	CTR	19	35	-	%
	$I_F = 16 \text{ mA}, V_O = 0.5 \text{ V}, V_{CC} = 4.5 \text{ V}$	SFH6135	CTR	5	-	-	%
		SFH6136	CTR	15	-	-	%



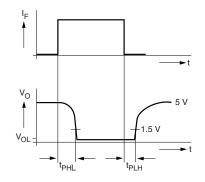
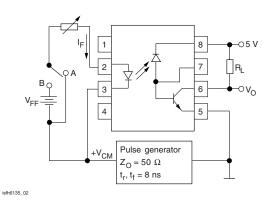


Fig. 1 - Schematics

SWITCHING CHARACTERISTICS							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
High to low	I_F = 16 mA, V_{CC} = 5 V, R_L = 4.1 k Ω	SFH6135	t _{PHL}	-	0.3	1.5	μs
High to low	I_F = 16 mA, V_{CC} = 5 V, R_L = 1.9 k Ω	SFH6136	t _{PHL}	-	0.2	0.8	μs
Low to high	I_F = 16 mA, V_{CC} = 5 V, R_L = 4.1 $k\Omega$	SFH6135	t _{PLH}	-	0.3	1.5	μs
Low to high	I_F = 16 mA, V_{CC} = 5 V, R_L = 1.9 k Ω	SFH6136	t _{PLH}	-	0.2	0.8	μs



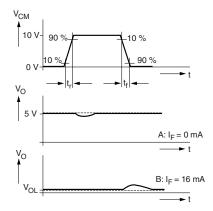


Fig. 1 - Common Mode Interference Immunity

3



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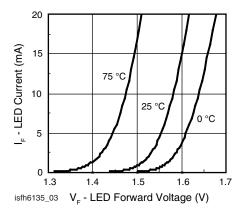
COMMON MODE TRANSIENT IMMUNITY								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
High	$\label{eq:V_CM} \begin{split} V_{CM} &= 10 \ V_{P\text{-}P}, \ V_{CC} = 5 \ V, \\ I_F &= 0 \ \text{mA}, \ R_L = 4.1 \ \text{k}\Omega \end{split}$	SFH6135	CM _H	-	1000	-	V/µs	
	$V_{CM} = 10 V_{P-P}, V_{CC} = 5 V,$ $I_F = 0 mA, R_L = 1.9 k\Omega$ SFH6136 CM	СМ _Н	-	1000	-	V/µs		
Low	$\label{eq:V_CM} \begin{split} V_{CM} &= 10 \; V_{P\text{-}P}, V_{CC} = 5 \; V, \\ I_F &= 0 \; \text{mA}, R_L = 4.1 \; \text{k}\Omega \end{split}$	SFH6135	CML	-	1000	-	V/µs	
	$\label{eq:V_CM} \begin{split} V_{CM} &= 10 \ V_{P\text{-}P}, \ V_{CC} = 5 \ V, \\ I_F &= 0 \ \text{mA}, \ R_L = 1.9 \ \text{k}\Omega \end{split}$	SFH6136	CML	-	1000	-	V/µs	

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Climatic classification (according to IEC 68 part 1)			-	55 / 100 / 21	-			
Comparative tracking index		CTI	175	-	399			
V _{IOTM}			8000	-	-	V		
V _{IORM}			890	-	-	V		
P _{SO}			-	-	500	mW		
I _{SI}			-	-	300	mA		
T _{SI}			-	-	175	°C		
Creepage distance	Standard DIP-8		7	-	-	mm		
Clearance distance	Standard DIP-8		7	-	-	mm		
Creepage distance	400 mil DIP-8		8	-	-	mm		
Clearance distance	400 mil DIP-8		8	-	-	mm		

Note

• As per IEC 60747-5-5, §7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits

TYPICAL CHARACTERISTICS ($T_{amb} = 25$ °C, unless otherwise specified)





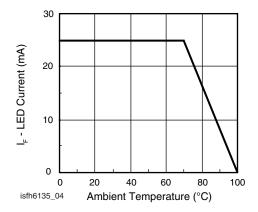


Fig. 4 - Permissible Forward LED Current vs. Temperature



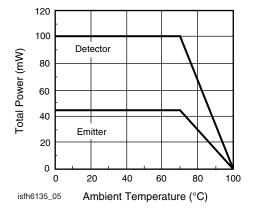


Fig. 5 - Permissible Power Dissipation vs. Temperature

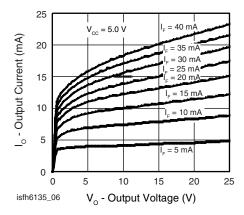


Fig. 6 - Output Current vs. Output Voltage

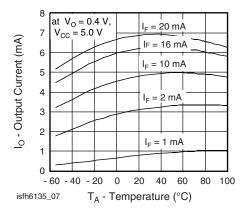


Fig. 7 - Output Current vs. Temperature

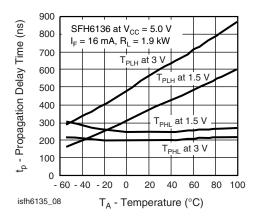


Fig. 8 - Propagation Delay vs. Ambient Temperature - SFH6136

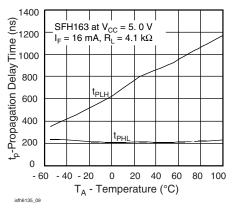


Fig. 9 - Propagation Delay vs. Ambient Temperature - SFH6135

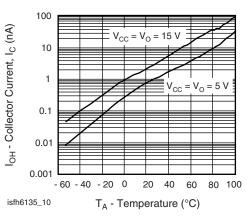


Fig. 10 - Logic High Output Current vs. Temperature



SFH6135, SFH6136

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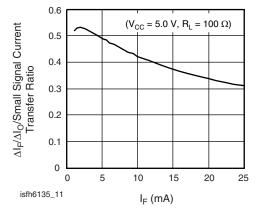
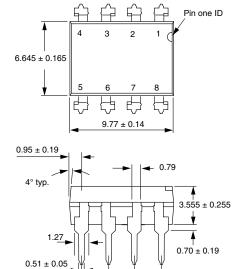
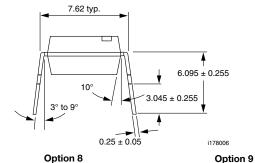


Fig. 11 - Small Signal Current Transfer Ratio vs. Quiescent Input Current





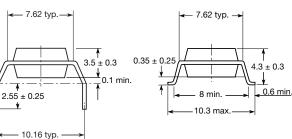


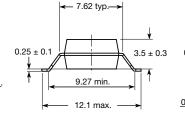


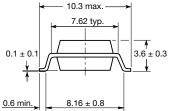
Option 6

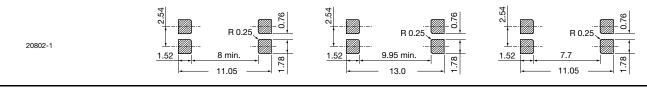


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6 For technical questions, contact: <u>optocoupleranswers@vishay.com</u> Document Number: 83668

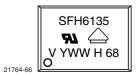
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