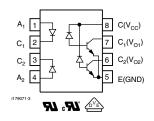


High Speed Optocoupler, Dual Channel, 1 MBd, Transistor Output





FEATURES

- Isolation test voltage, 5300 V_{RMS}
- TTL compatible
- Bit rates: 1 MBit/s
- · High common mode transient immunity
- Bandwidth 2 MHz
- Open collector output
- Compliant to RoHS Directive 2002/95/EC and in accordance to WEEE 2002/96/EC





ROHS

DESCRIPTION

The SFH6325 and SFH6326 are dual channel optocouplers with a GaAlAs infrared emitting diode, optically coupled with an integrated photo detector which consists of a photo diode and a high-speed transistor in a DIP-8 plastic package. Signals can be transmitted between two electrically separated circuits up to frequencies of 2 MHz. The potential difference between the circuits to be coupled should not exceed the maximum permissible reference voltages.

AGENCY APPROVALS

- UL1577, file no. E52744 system code H double protection
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-2 (VDE0884)/DIN EN 60747-5-5 (pending), available with option 1

ORDERING INFORMATION					
S F H 6 3 2 PART NUMBER	# - X 0 # # PACKAGE OPTION	TAPE AND REEL Option 7 Option 9 > 0.1 mm			
AGENCY CERTIFIED/PACKAGE	CTR (%)			
UL, cUL	≥7	≥ 19			
DIP-8	SFH6325	SFH6326			
DIP-8, 400 mil, option 6	-	SFH6326-X006			
SMD-8, option 7	-	SFH6326-X007T ⁽¹⁾			
SMD-8, option 9	SFH6325-X009T ⁽¹⁾	SFH6326-X009T ⁽¹⁾			
VDE, UL, cUL	≥7	≥ 19			
DIP-8	-	SFH6326-X001			
SMD-8, option 7	SFH6325-X017T	SFH6326-X017T ⁽¹⁾			

Note

- · Additional options may be possible, please contact sales office.
- (1) Also available in tubes; do not add T to end.

ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)										
PARAMETER TEST CONDITION SYMBOL VALUE UNIT										
INPUT										
Reverse voltage		V _R	4.5	V						
Forward continuous current		I _F	25	mA						
Peak forward current	t = 1 ms, duty cycle 50 %	I _{FM}	50	mA						
Maximum surge forward current	t ≤ 1 µs, 300 pulses/s	I _{FSM}	1	Α						
Derate linearly from 25 °C			0.6	mW/°C						
Power dissipation	T _{amb} ≤ 70 °C	P _{diss}	50	mW						



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
OUTPUT								
Supply voltage		Vs	- 0.5 to 30	V				
Output voltage		V _O	- 0.5 to 25	V				
Collector output current		I _{CO}	8	mA				
Derate linearly from 25 °C			1.33	mW/°C				
Power dissipation	T _{amb} ≤ 70 °C	P _{diss}	50	mW				
COUPLER								
Isolation test voltage	t = 1 s	V _{ISO}	5300	V _{RMS}				
Pollution degree (DIN VDE0109)			2					
Creepage distance			≥ 7	mm				
Clearance distance			≥ 7	mm				
Derate linearly from 25 °C			1.93	mW/°C				
Total package dissipation		P _{tot}	145	mW				
Comparative tracking index per DIN IEC112/VDE0303 part 1, group Illa per DIN VDE6110			175					
Isolation resistance	V _{IO} = 500 V, T _{amb} = 25 °C	R _{IO}	≥ 10 ¹²	Ω				
Isolation resistance	V _{IO} = 500 V, T _{amb} = 100 °C	R _{IO}	≥ 10 ¹¹	Ω				
Storage temperature range		T _{stg}	- 55 to + 125	°C				
Ambient temperature range		T _{amb}	- 55 to +100	°C				
Soldering temperature (1)	max. 10 s, dip soldering distance to seating plane ≥ 1.5 mm	T _{sld}	260	°C				

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD). Refer to wave profile for soldering conditions for through hole devices (DIP).

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT (1)									
Forward voltage	I _F = 16 mA		V_{F}		1.33	1.9	V		
Breakdown voltage	I _R = 10 μA		V_{BR}	4.5			V		
Reverse current	V _R = 4.5 V		I_R		0.5	10	μA		
Capacitance	V _R = 0 V, f = 1 MHz		Co		30		pF		
Temperature coefficient of forward voltage	I _F = 16 mA		$\Delta V_F / \Delta T_{amb}$		- 1.7		mV/°C		
OUTPUT									
Logic low supply current	$I_F = 16$ mA, $V_O = open$, $V_{CC} = 4.5$ V		I _{CCL}		100	200	μA		
Supply current, logic high	$I_F = 0$ mA, $V_O = open$, $V_{CC} = 15$ V		I _{CCH}		0.01	4	μA		
Logic low output voltage	I_F = 16 mA, V_{CC} = 4.5 V, I_O = 1.1 mA	SFH6325	V_{OL}		0.1	0.5	V		
Logic low output voltage	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V}, I_O = 3 \text{ mA}$	SFH6326	V_{OL}	1.33 4.5 0.5 30 -1.7 100 0.01 0.01 3 0.1 3	0.5	V			
Logic high output current	$I_F = 0 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		I _{OH}		3	500	nA		
Logic night output current	$I_F = 0 \text{ mA}, V_O = V_{CC} = 15 \text{ V}$		I _{OH}			50	μA		
Channel to channel (2) crosstalk	$I_F = 16 \text{ mA}, V_O = V_{CC} = 5.5 \text{ V}$		I _{OH-XT}			500	nA		
COUPLER									
Capacitance (input to output)	f = 1 MHz		C _{IO}		0.6		pF		

Notes

- Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
 evaluation. Typical values are for information only and are not part of the testing requirements.
- (1) T_{amb} = 0 °C to 70 °C, unless otherwise specified.
- (2) To measure crosstalk, turn on the LED for channel 1 and the output current for channel 2 in logic high. Repeat for channel 2.



CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Current transfer ratio	$I_{\rm F}$ = 16 mA, $V_{\rm CC}$ = 4.5 V,	SFH6325	CTR	7	16		%	
	$V_O = 0.4 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$	SFH6326	SFH6326 CTR	19	35		%	
	$I_F = 16 \text{ mA}, V_{CC} = 4.5 \text{ V},$	SFH6325	CTR	5			%	
	$V_O = 0.5 \text{ V}, T_{amb} = 0 ^{\circ}\text{C} \text{ to } 70 ^{\circ}\text{C}$	SFH6326	CTR	15			%	

SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
LP-1-1-1-	I_F = 16 mA, V_{CC} = 5 V, R_L = 4.1 k Ω	SFH6325	t _{PHL}		0.3	1.5	μs	
High to low	$I_F = 16 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1.9 \text{ k}\Omega$	SFH6326	t _{PHL}		1 1111	μs		
Low to high	I_F = 16 mA, V_{CC} = 5 V, R_L = 4.1 $k\Omega$	SFH6325	t _{PLH}		0.6	1.5	μs	
Low to high	I_F = 16 mA, V_{CC} = 5 V, R_L = 1.9 k Ω	SFH6326	t _{PLH}		0.5	1.5 0.8 1.5	μs	

COMMON MODE TRANSIENT IMMUNITY (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
CMTI at logic high level output	$I_F = 0$ mA, $C_{CM} = 10 V_{P-P}$, $V_{CC} = 5 V$, $R_L = 4.1 k\Omega$	SFH6325	CM _H		1000		V/µs	
	$I_F = 0$ mA, $C_{CM} = 10 V_{P-P}$, $V_{CC} = 5 V$, $R_L = 1.9 k\Omega$	SFH6326	CM _H		1000		V/µs	
CMTI at legis levelevel cutout	$I_F = 16$ mA, $C_{CM} = 10$ V_{P-P} , $V_{CC} = 5$ V, $R_L = 4.1$ k Ω	SFH6325	CML		1000		V/µs	
CMTI at logic low level output	I_F = 16 mA, C_{CM} = 10 V_{P-P} , V_{CC} = 5 V , R_L = 1.9 $k\Omega$	SFH6326	CML		1000		V/µs	

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

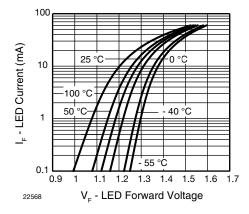


Fig. 1 - LED Forward Current vs. Forward Voltage

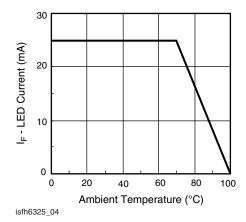


Fig. 2 - Permissible Forward LED Current vs. Temperature



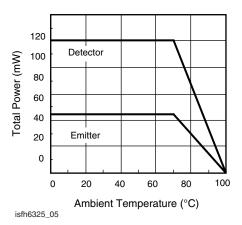


Fig. 3 - Permissible Power Dissipation vs. Temperature

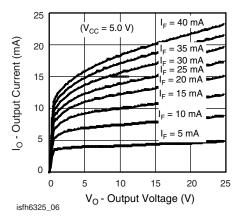


Fig. 4 - Output Current vs. Output Voltage

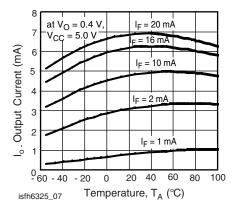


Fig. 5 - Output Current vs. Temperature

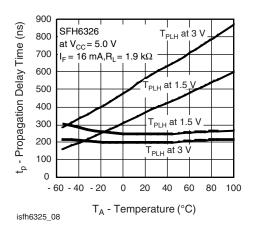


Fig. 6 - Propagation Delay vs. Ambient Temperature

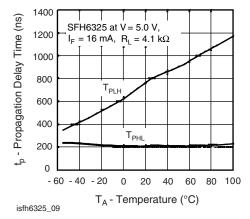


Fig. 7 - Propagation Delay vs. Ambient Temperature

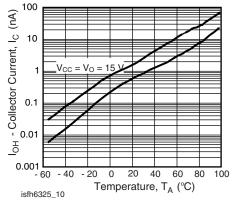


Fig. 8 - Logic High Output Current vs. Temperature

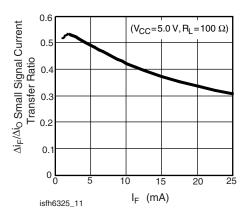


Fig. 9 - Small Signal Current Transfer Ratio vs. Input Current

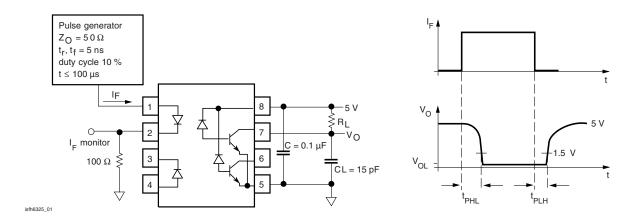


Fig. 10 - Switching Time and Test Circuit

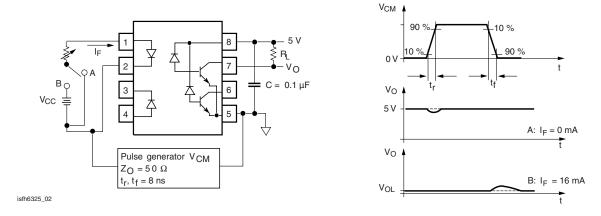
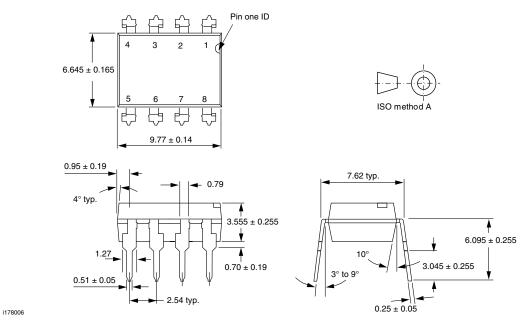
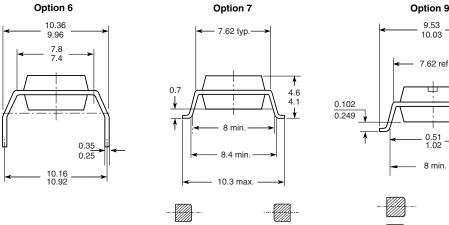


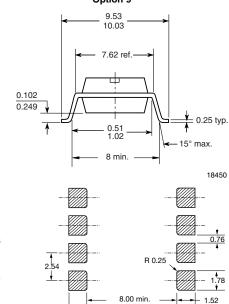
Fig. 11 - Waveform and Test Circuit for Common Mode Transient Immunity



PACKAGE DIMENSIONS in millimeters







PACKAGE MARKING



8.00 min. 11.05

Notes

- Only options 1 and 7 are reflected in the package marking.
- The VDE Logo is only marked on option1 parts.
- Tape and reel suffix (T) is not part of the package marking.



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