# SFH636

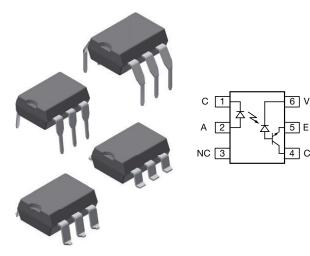
RoHS

COMPLIANT



Vishay Semiconductors

# High Speed Optocoupler, Phototransistor Output, 1 MBd, 10 kV/µs CMR, Split Collector Transistor Output



## DESCRIPTION

The SFH636 is an optocoupler with a GaAlAs infrared emitting diode, optically coupled to an integrated photo detector consisting of a photo diode and a high speed transistor in a DIP-6 plastic package. The device is functionally similar to 6N136 except there is no base connection and the foot print is different. Noise and common mode rejection performance is enhanced by not bringing out the base connection.

Signals can be transmitted between two electrically separated circuits up to frequencies of 2.0 MHz.

## FEATURES

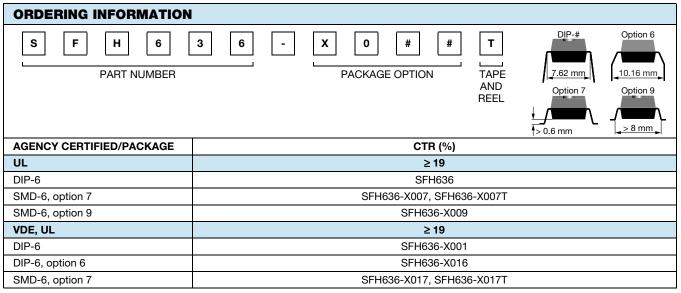
- High CMR of 10 kV/µs
- High speed optocoupler without base connection
- GaAlAs emitter
- Integrated detector with photo diode and transistor
- TTL and CMOS compatible
- Open collector output
  - Supply voltage up to 30 V
  - High CTR
  - · Good CTR linearity relative to forward current
  - Low coupling capacitance
  - Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- IGBT drivers and MOSFET driver stages
- Data communications
- Programmable controllers
- IPM drivers

#### AGENCY APPROVALS

- UL1577, file no. E52744, double protection
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1



Note

Additional options may be possible, please contact sales office

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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)					
PARAMETER	CONDITIONS	SYMBOL	VALUE	UNIT	
INPUT					
Reverse voltage		V <sub>R</sub>	3.0	V	
DC forward current		I <sub>F</sub>	25	mA	
Surge forward current	$t_P \le 1.0 \ \mu s$ , 300 pulses/s	I <sub>FSM</sub>	1.0	А	
Power dissipation		P <sub>diss</sub>	45	mW	
OUTPUT					
Supply voltage		VS	-0.5 to +30	V	
Output voltage		Vo	-0.5 to +20	V	
Output current		Ι <sub>Ο</sub>	8	mA	
Power dissipation		P <sub>diss</sub>	100	mW	
COUPLER					
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C	
Ambient temperature range		T <sub>amb</sub>	-55 to +100	°C	
Junction temperature		Тj	100	°C	
Soldering temperature	Max. 10 s, dip soldering: distance to seating plane ≥ 1.5 mm	T <sub>sld</sub>	260	°C	

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

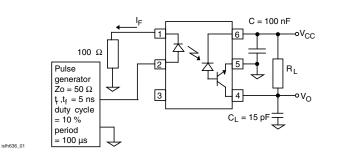
<b>ELECTRICAL CHARACTERIST</b>	<b>ICS</b> (T <sub>amb</sub> = 0 °C to 70 °C unless	otherwise	specifie	d, typ. va	lues T <sub>amb</sub>	= 25 °C)
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
input						
Forward voltage	I <sub>F</sub> = 16 mA	V <sub>F</sub>	-	1.5	1.8	V
Reverse current	V <sub>R</sub> = 3 V	I <sub>R</sub>	-	0.5	10	μA
Capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	Co	-	125	-	pF
Thermal resistance		R <sub>thja</sub>	-	700	-	K/W
output						
Logic high supply current	$I_{F}$ = 0 V, V_O (open), $V_{CC}$ = 15 V, $T_{amb}$ = 25 °C	I <sub>CCH</sub>	-	0.01	1	μΑ
	$I_F = 0 \text{ V}, \text{ V}_O \text{ (open)}, \text{ V}_{CC} = 15 \text{ V}$	I <sub>CCH</sub>	-	0.01	2	μA
Output current, output high	$I_F = 0 \text{ V}, \text{ V}_O \text{ (open)},$ $V_{CC} = 5.5 \text{ V}, \text{ T}_{amb} = 25 \text{ °C}$	I <sub>OH</sub>	-	0.003	0.5	μA
	$I_F = 0 V$ , $V_O$ (open), $V_{CC} = 15 V$ , $T_{amb} = 25 °C$	I <sub>OH</sub>	-	0.01	1	μA
	$I_F = 0 \text{ V}, \text{ V}_O \text{ (open)}, \text{ V}_{CC} = 15 \text{ V}$	I <sub>ОН</sub>	-		50	μA
Collector emitter capacitance	V <sub>CE</sub> = 5 V, f = 1 MHz	C <sub>CE</sub>	-	3	-	pF
Thermal resistance		R <sub>thja</sub>	-	300	-	K/W
coupler						
Coupling capacitance		C <sub>C</sub>	-	0.6	-	pF
Collector emitter saturation voltage	$I_F = 16 \text{ mA}, I_O = 2.4 \text{ mA}, V_{CC} = 4.5 \text{ V}; T_{amb} = 25 \text{ °C}$	V <sub>OL</sub>	-	0.1	0.4	V
Supply current, logic low	$I_F = 16 \text{ mA}, V_O \text{ open}, V_{CC} = 15 \text{ V}$	I <sub>DD</sub>	-	80	-	

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.







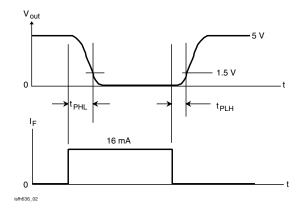


Fig. 1 - Test Setup



<b>CURRENT TRANSFER RATIO</b> (T <sub>amb</sub> = 0 °C to 70 °C unless otherwise specified, typ. values T <sub>amb</sub> = 25 °C)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	$I_F = 16 \text{ mA}, V_O = 0.4 \text{ V}, V_{CC} = 4.5 \text{ V}, \\ T_{amb} = 25 \text{ °C}$	CTR	19	30	-	%
	$I_F = 16 \text{ mA}, V_O = 0.5 \text{ V}, V_{CC} = 4.5 \text{ V}$	CTR	15	-	-	%

SWITCHING CHARACTERISTICS (T <sub>amb</sub> = 25 °C unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Propagation delay time (high to low)	$I_F$ = 16 mA, $V_{CC}$ = 5.0 V, $R_L$ = 1.9 $k\Omega$	t <sub>PHL</sub>	-	0.3	0.8	μs
Propagation delay time (low to low)	$I_F$ = 16 mA, $V_{CC}$ = 5.0 V, $R_L$ = 1.9 $k\Omega$	t <sub>PLH</sub>	-	0.3	0.8	μs

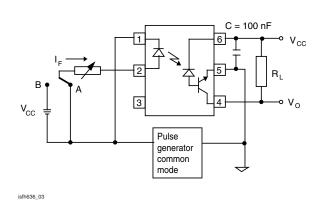


Fig. 3 - Common Mode Transient Test

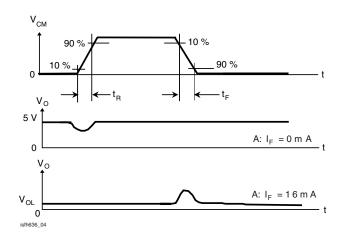


Fig. 4 - Measurement Waveform of CMR

<b>COMMON MODE TRANSIENT IMMUNITY</b> (T <sub>amb</sub> = 25 °C unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Common mode transient immunity (high)	$\begin{split} I_{O} &= 0 \text{ mA, } V_{CM} = 1500 \text{ V}_{\text{P-P}}, \\ R_{\text{L}} &= 1.9 \text{ k}\Omega, \text{ V}_{\text{CC}} = 5.0 \text{ V} \end{split}$	CM <sub>H</sub>	-	10 000	-	V/µs
Common mode transient immunity (low)	$\begin{split} I_{O} &= 16 \text{ mA}, \text{ V}_{CM} = 1500 \text{ V}_{\text{P-P}}, \\ R_{L} &= 1.9 \text{ k}\Omega, \text{ V}_{CC} = 5.0 \text{ V} \end{split}$	CM <sub>L</sub>	-	10 000	-	V/µs

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Document Number: 83681

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## **Vishay Semiconductors**

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		55/100/21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index	Insulation group IIIa	CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V <sub>ISO</sub>	4420	V <sub>RMS</sub>
Tested withstanding isolation voltage	According to UL1577, t = 1 s	V <sub>ISO</sub>	5300	V <sub>RMS</sub>
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V <sub>IOTM</sub>	8000	V <sub>peak</sub>
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	VIORM	890	V <sub>peak</sub>
ladation registeres	$T_{amb} = 25 \ ^{\circ}C, V_{IO} = 500 \ V$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω
Isolation resistance	$T_{amb} = 100 \text{ °C}, V_{IO} = 500 \text{ V}$	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω
Output safety power		P <sub>SO</sub>	700	mW
Input safety current		I <sub>SI</sub>	400	mA
Input safety temperature		T <sub>S</sub>	175	°C
Creepage distance	DIP-6		≥7	mm
Clearance distance	DIP-6		≥7	mm
Creepage distance	DIP-6, option 6		≥8	mm
Clearance distance	DIP-6, option 6		≥8	mm
Creepage distance	SMD-6, option 7		≥7	mm
Clearance distance	SMD-6, option 7		≥7	mm
Creepage distance	SMD-6, option 9		≥7	mm
Clearance distance	SMD-6, option 9		≥7	mm
Insulation thickness		DTI	≥ 0.4	mm

Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

## TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

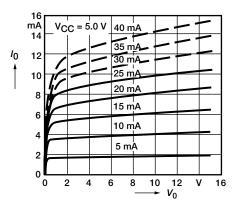


Fig. 5 - Output Characteristics-Output Current vs. Output Voltage

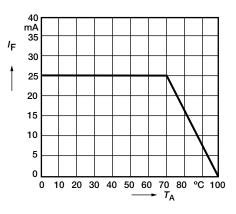
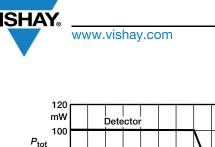


Fig. 6 - Permissible Forward Current of Emitting Diode vs. Ambient Temperature

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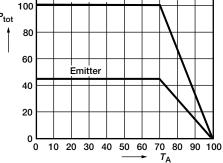


Fig. 7 - Permissible Total Power Dissipation vs. Ambient Temperature

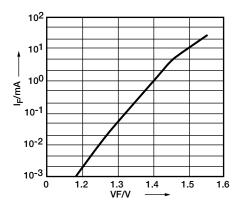


Fig. 8 - Forward Current of Emitting Diode vs. Forward Voltage

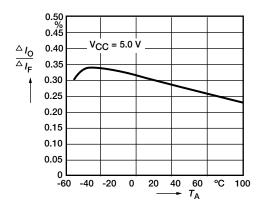


Fig. 9 - Small Signal Transfer Ratio vs. Forward Current

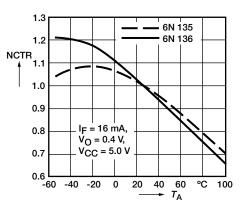


Fig. 10 - Current Transfer Ratio (Normalized) vs. Ambient Temperature

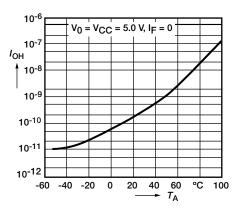


Fig. 11 - Output Current (High) vs. Ambient Temperature

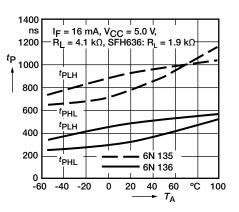


Fig. 12 - Delay Times vs. Ambient Temperature

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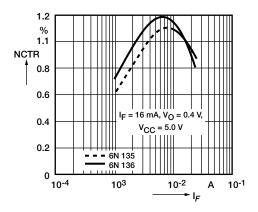
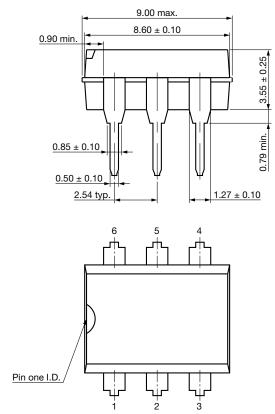
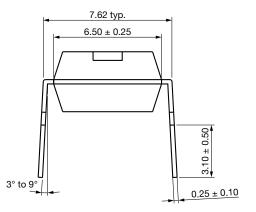


Fig. 13 - Current Transfer Ratio (Normalized) vs. Forward Current

## **PACKAGE DIMENSIONS** in inches (millimeters)

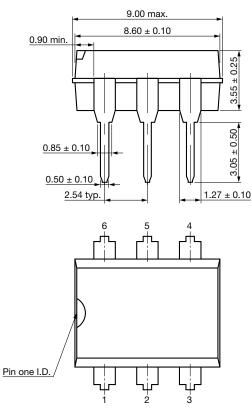




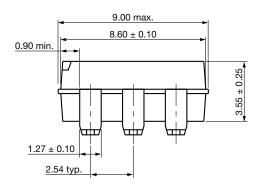


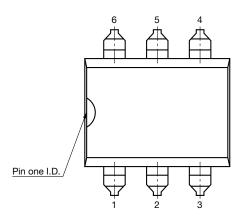
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## DIP-6, Option 6



#### SMD-6, Option 7





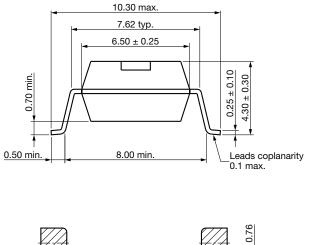
 $\begin{array}{||||} & 0.25 \pm 0.10 \\ \hline \\ \hline \\ \hline \\ \end{array}$ 

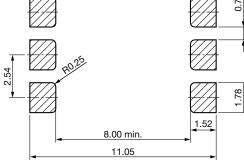
10.16 typ.

7.62 typ.

 $6.50 \pm 0.25$ 

0.10 min.





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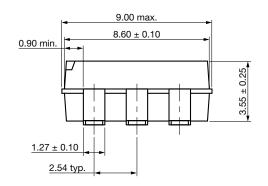
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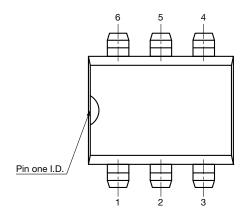
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SMD-6, Option 9

ISHA)





## SOLDER PROFILES

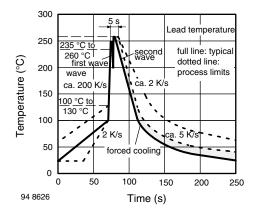


Fig. 14 - Wave Soldering Double Wave Profile According to J.STD-020 for DIP Devices

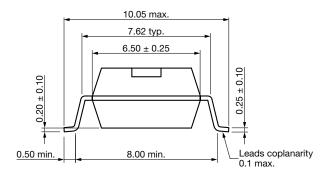
## HANDLING AND STORAGE CONDITIONS

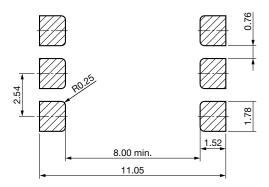
ESD level: HBM class 2

Floor life: unlimited

Conditions:  $T_{amb}$  < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020





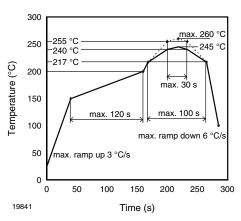


Fig. 15 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

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# **Footprint and Schematic Information for SFH636**

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
SFH636	www.snapeda.com/parts/SFH636/Vishay/view-part
SFH636-X001	www.snapeda.com/parts/SFH636-X001/Vishay/view-part

For technical issues and product support, please contact optocoupleranswers@vishav.com.





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 TLP290(V4GBTP,SE(T
 PS9121-F3-AX
 PS9123-F3-AX
 TLP5774H(TP4,E
 TLP5771H(TP,E
 HCPL2631SD
 HCPL-4661-500E

 TLP118(TPL,E)
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 TLP2309(TPL,E)

 TLP2355(TPL,E
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 TLP521-4GR
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 TLP621-4X
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 IS281-4GB
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 IS181GR
 ICPL2631

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 ACPL-064L-500E
 PS2501-1XSM
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 PS2913-1-F3-AX
 PS9821-2-F3-AX
 FOD0721R2