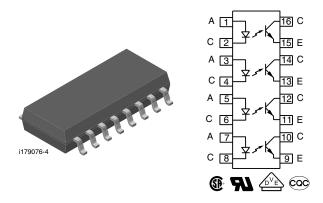


Optocoupler, Phototransistor Output, Quad Channel, SSOP-16, Half Pitch Mini-Flat Package



LINKS TO ADDITIONAL RESOURCES







DESCRIPTION

The SFH6916 has a GaAs infrared emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a 16 pin 50 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits.

FEATURES

- SSOP (shrink small outline package)
- Isolation test voltage, 3750 V_{RMS}
- High collector emitter voltage, V_{CEO} = 70 V
- · Low saturation voltage
- · Fast switching times
- Temperature stable
- Low coupling capacitance
- End stackable, 0.050" (1.27 mm) spacing

please see www.vishay.com/doc?99912

• Material categorization: for definitions of compliance

Pb-free



ROHS COMPLIANT HALOGEN

FREE GREEN (5-2008)

AGENCY APPROVALS

- <u>UL1577</u>
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- BSI
- CQC GB4943.1-2011
- <u>CQC GB8898-2011</u> (suitable for installation altitude below 2000 m)
- CSA
- <u>FIMKO</u>

ORDERING INFORMATION					
S F H 6 9 PART NUMBER	1 6 - X 0 0 1 T VDE OPTION TAPE AND REEL				
AGENCY CERTIFIED / PACKAGE	CTR (%)				
UL, cUL, BSI, CQC, CSA, FIMKO	50 to 300				
SSOP-16, quad channel	SFH6916T ⁽¹⁾				
UL, cUL, VDE (option 1), BSI, CQC, CSA, FIMKO	50 to 300				
SSOP-16, quad channel	SFH6916-X001				

Notes

- · Additional options may be possible, please contact sales office
- (1) Also available in tubes, do not put "T" to the end



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
INPUT								
Reverse voltage		V_R	6	V				
DC forward current		I _F	50	mA				
Surge forward current	t _p ≤ 10 μs	I _{FSM}	1.5	Α				
Total power dissipation		P _{diss}	80	mW				
OUTPUT								
Collector emitter voltage		V_{CEO}	70	V				
Emitter collector voltage		V _{ECO}	7	V				
Collector current		I _C	50	mA				
Collector current	$t_p = 1.0 \text{ ms}$	Ic	100	mA				
Total power dissipation per channel		P _{diss}	150	mW				
COUPLER								
Storage temperature range		T _{stg}	-55 to +125	°C				
Ambient temperature range		T _{amb}	-55 to +100	°C				
Junction temperature		Tj	125	°C				
Soldering temperature (1)	Max. 10 s dip soldering distance to seating plane ≥ 1.5 mm		260	°C				
Total power dissipation		P _{tot}	250	mW				

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
 implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
 maximum ratings for extended periods of the time can adversely affect reliability
- (1) Refer to reflow profile for soldering conditions for surface mounted devices

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
INPUT						
Forward voltage	I _F = 5 mA	V _F	-	1.15	1.4	V
Reverse current	V _R = 6 V	I _R	-	0.01	10	μA
Capacitance	Co	Co	-	8	-	pF
OUTPUT						
Collector emitter leakage current	V _{CE} = 20 V	I _{CEO}	-		100	nA
Collector emitter capacitance	V _{CE} = 5 V, f = 1 MHz	C _{CE}	-	6.0	-	pF
COUPLER						
Collector emitter saturation voltage	$I_F = 20 \text{ mA}, I_C = 1 \text{ mA}$	V _{CEsat}	-	0.1	0.4	V
Coupling capacitance	f = 1 MHz	C _C	-	1	-	pF

Note

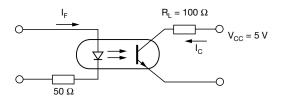
 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements

CURRENT TRANSFER RATIO (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER TEST CONDITION SYMBOL MIN. TYP. MAX. UNIT						UNIT
Current transfer ratio	$I_F = 5 \text{ mA}, V_{CC} = 5 \text{ V}$	CTR	50	-	300	%



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SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
NON-SATURATED						
Rise time	$I_C = 2$ mA, $V_{CC} = 5$ V, $R_L = 100 \Omega$	t _r	ı	5.5	-	μs
Fall time	I_C = 2 mA, V_{CC} = 5 V, R_L = 100 Ω	t _f	ı	7	-	μs
Turn-on time	I_C = 2 mA, V_{CC} = 5 V, R_L = 100 Ω	t _{on}	ı	9.5	-	μs
Turn-off time	$I_C = 2$ mA, $V_{CC} = 5$ V, $R_L = 100 \Omega$	t _{off}	-	8.5	-	μs
SATURATED						
Turn-on time	$I_F = 10$ mA, $V_{CC} = 5$ V, $R_L = 1$ k Ω	t _{on}	ı	3	-	μs
Turn-off time	$I_F = 10 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 1 \text{ k}\Omega$	t _{off}	-	20	=	μs



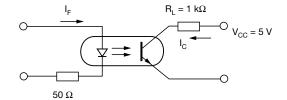


Fig. 1 - Switching Operation (without saturation)

Fig. 2 - Switching Operation (with saturation)

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
Climatic classification	According to IEC 68 part 1		55 / 100 / 21			
Comparative tracking index		CTI	175			
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	3750	V_{RMS}		
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V _{IOTM}	6000	V _{peak}		
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	707	V _{peak}		
Isolation resistance	$V_{IO} = 500 \text{ V}, T_{amb} = 25 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹²	Ω		
Isolation resistance	$V_{IO} = 500 \text{ V}, T_{amb} = 100 ^{\circ}\text{C}$	R _{IO}	≥ 10 ¹¹	Ω		
Output safety power		P _{SO}	350	mW		
Input safety current		I _{SI}	200	mA		
Safety temperature		T _S	175	°C		
Creepage distance			≥ 5	mm		
Clearance distance			≥ 5	mm		
Insulation thickness		DTI	≥ 0.4	mm		

Note

As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

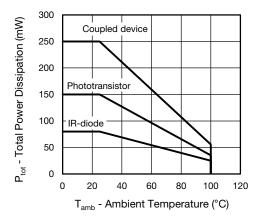


Fig. 3 - Total Power Dissipation vs. Ambient Temperature

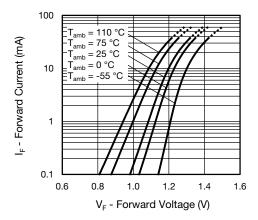


Fig. 4 - Forward Voltage vs. Forward Current

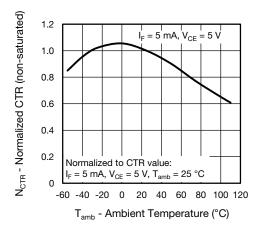


Fig. 5 - Normalized Current Transfer Ratio (non-saturated) vs.
Ambient Temperature

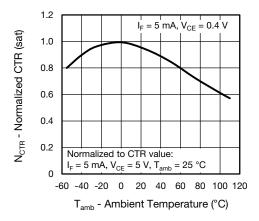


Fig. 6 - Normalized Current Transfer Ratio (saturated) vs.
Ambient Temperature

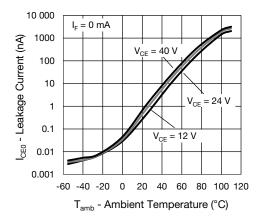


Fig. 7 - Collector Dark Current vs. Ambient Temperature

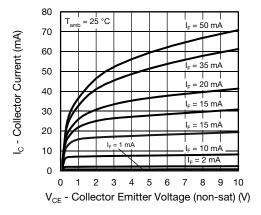


Fig. 8 - Collector Current vs. Collector Emitter Voltage (non-saturated)



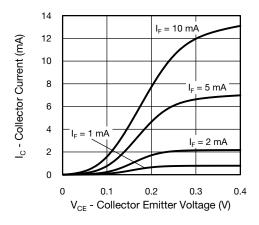


Fig. 9 - Collector Current vs. Collector Emitter Voltage (saturated)

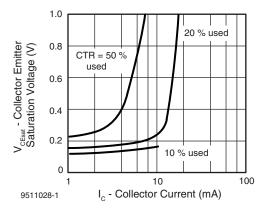


Fig. 10 - Collector Emitter Saturated Voltage vs. Collector Current

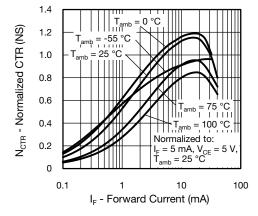


Fig. 11 - Normalized CTR (non-saturated) vs. Forward Current

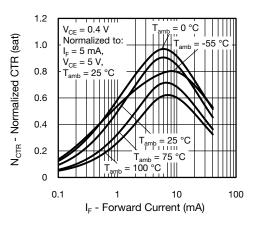


Fig. 12 - Normalized CTR (saturated) vs. Forward Current

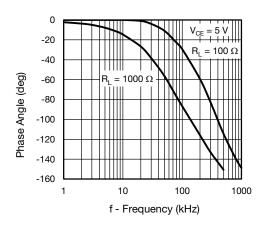


Fig. 13 - F_{CTR} vs. Phase Angle

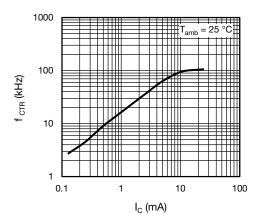


Fig. 14 - f_{CTR} vs. Collector Current



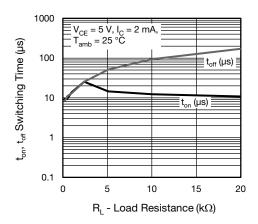
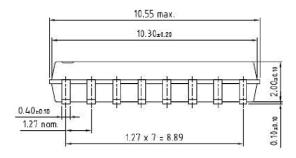
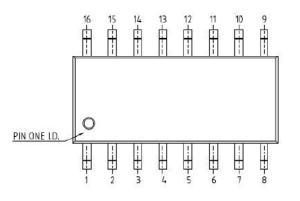
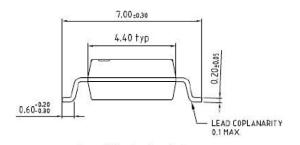


Fig. 15 - Switching Time vs. Load Resistance

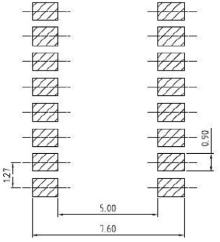
PACKAGE DIMENSIONS in millimeters







Possible footprint



PACKAGE MARKING

SFH6916 O V YWW 68



TAPE AND REEL PACKAGING in millimeters

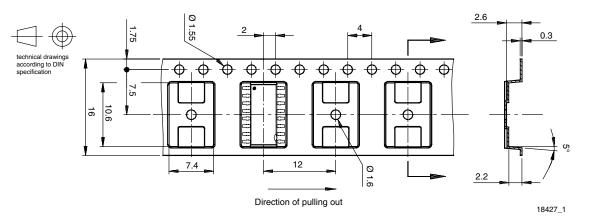


Fig. 16 - 2000 pcs/reel

SOLDER PROFILE

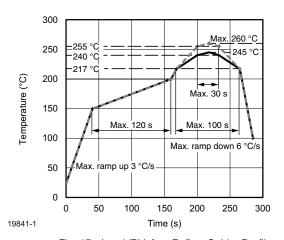


Fig. 17 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2

Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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