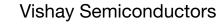
### Rev. 2.1, 12-Apr-13

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Document Number: 83719



## **Optocoupler, Phototransistor Output, SOP-4, AC Input, Mini-Flat Package**

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## DESCRIPTION

The SFH691AT has a GaAs infrared emitting diode emitter, which is optically coupled to silicon planar phototransistor detector, and is incorporated in a 4 pin 100 mil lead pitch miniflat package. It features a high current transfer ratio, low coupling capacitance, and high isolation voltage.

The coupling devices are designed for signal transmission between two electrically separated circuits.

## **FEATURES**

- SOP (small outline package)
- Isolation test voltage, 3750 V<sub>BMS</sub> (1 s)
- High collector emitter breakdown voltage,  $V_{CEO} = 70 V$
- Bidirectional AC input
- · Low saturation voltage
- · Fast switching times
- Temperature stable
- Low coupling capacitance
- End stackable, 0.100" (2.54 mm) spacing
- · Material categorization: For definitions of compliance please see www.vishay.com/doc?99912

### APPLICATIONS

- · High density mounting or space sensitive PCBs
- PLCs
- Telecommunication

## AGENCY APPROVALS

- UL1577, file no. E52744 system code U
- cUL tested to CSA Bulletin 22.2 5A
- FIMKO
- DIN EN 60747-5-5 (VDE 0884)
- CQC GB4943.1-2011 (suitable for installation altitude below 2000 m)

| S F H 6 9 1 A T SOP-4   PART NUMBER PART NUMBER TAPE AND REEL TAPE AND REEL 7.21 mm   AGENCY CERTIFIED/PACKAGE CTR (%)   UL, cull, FIMKO 50 to 300 | ORDERING INFORMATION     |          |  |  |  |  |
|--|--------------------------|----------|--|--|--|--|
| AGENCY CERTIFIED/PACKAGE CTR (%)   | S F H 6 9                |          |  |  |  |  |
|  | PART NUMBER              |          |  |  |  |  |
| UL, cUL, FIMKO 50 to 300   | AGENCY CERTIFIED/PACKAGE | CTR (%)  |  |  |  |  |
|  | UL, cUL, FIMKO 50 to 300 |          |  |  |  |  |
| SOP-4 SFH691AT   | SOP-4                    | SFH691AT |  |  |  |  |





COMPLIANT HALOGEN FREE <u>GREEN</u>

(5-2008)



## **Vishay Semiconductors**

| ABSOLUTE MAXIMUM RATI                                  | <b>NGS</b> (T <sub>amb</sub> = 25 °C, unless othe                | erwise specified  | )                  |                  |
|--|--|-------------------|--------------------|------------------|
| PARAMETER  | TEST CONDITION   | SYMBOL            | VALUE              | UNIT             |
| INPUT  | · · · · · · · · · · · · · · · · · · ·                            |                   |                    |                  |
| DC forward current                                     |  | I <sub>F</sub>    | ± 50               | mA               |
| Surge forward current                                  | t <sub>p</sub> ≤ 10 μs   | I <sub>FSM</sub>  | ± 2.5              | A                |
| Total power dissipation                                |  | P <sub>diss</sub> | 80                 | mW               |
| OUTPUT   |  |                   |                    |                  |
| Collector emitter voltage                              |  | V <sub>CEO</sub>  | 70                 | V                |
| Emitter collector voltage                              |  | V <sub>ECO</sub>  | 7                  | V                |
| Collector current                                      |  | Ι <sub>C</sub>    | 50                 | mA               |
| Collector current                                      | t <sub>p</sub> ≤ 1 ms  | Ι <sub>C</sub>    | 100                | mA               |
| Total power dissipation                                |  | P <sub>diss</sub> | 150                | mW               |
| COUPLER  |  |                   |                    |                  |
| Isolation test voltage between emitter<br>and detector | 1 s  | V <sub>ISO</sub>  | 3750               | V <sub>RMS</sub> |
| Isolation resistance                                   | $V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 25 ^{\circ}\text{C}$  | R <sub>IO</sub>   | ≥ 10 <sup>12</sup> | Ω                |
| Isolation resistance                                   | $V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$ | R <sub>IO</sub>   | ≥ 10 <sup>11</sup> | Ω                |
| Storage temperature range                              |  | T <sub>stg</sub>  | - 55 to + 150      | °C               |
| Ambient temperature range                              |  | T <sub>amb</sub>  | - 55 to + 100      | °C               |
| Junction temperature                                   |  | Tj                | 100                | °C               |
| Soldering temperature <sup>(1)</sup>                   | max. 10 s dip soldering distance<br>to seating plane ≥ 1.5 mm    | T <sub>sld</sub>  | 260                | °C               |

#### Notes

• Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Refer to reflow profile for soldering conditions for surface mounted devices.

| <b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified) |   |                    |      |      |      |      |  |
|--|---|--------------------|------|------|------|------|--|
| PARAMETER  | TEST CONDITION                                    | SYMBOL             | MIN. | TYP. | MAX. | UNIT |  |
| INPUT  |   |                    |      |      |      |      |  |
| Forward voltage  | $I_F = \pm 5 \text{ mA}$                          | V <sub>F</sub>     |      | 1.15 | 1.4  | V    |  |
| Capacitance  | V <sub>R</sub> = 0 V, f = 1 MHz                   | Co                 |      | 29   |      | pF   |  |
| Thermal resistance   |   | R <sub>thja</sub>  |      | 750  |      | °C/W |  |
| OUTPUT   |   |                    |      |      |      |      |  |
| Collector emitter leakage current  | V <sub>CE</sub> = 20 V                            | I <sub>CEO</sub>   |      |      | 100  | nA   |  |
| Collector emitter capacitance  | $V_{CE} = 5 V$ , f = 1 MHz                        | C <sub>CE</sub>    |      | 5    |      | pF   |  |
| Thermal resistance   |   | R <sub>thja</sub>  |      | 500  |      | °C/W |  |
| COUPLER  |   |                    |      |      |      |      |  |
| Collector emitter saturation voltage   | $I_{F} = \pm 10 \text{ mA}, I_{C} = 2 \text{ mA}$ | V <sub>CEsat</sub> |      | 0.1  | 0.3  | V    |  |
| Coupling capacitance   | f = 1 MHz   | C <sub>C</sub>     |      | 0.4  |      | pF   |  |

#### Note

• Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

| <b>CURRENT TRANSFER RATIO</b> ( $T_{amb} = 25$ °C, unless otherwise specified) |  |        |      |      |      |      |
|--|--|--------|------|------|------|------|
| PARAMETER  | TEST CONDITION                                 | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Current transfer ratio   | $I_F = \pm 5 \text{ mA}, V_{CE} = 5 \text{ V}$ | CTR    | 50   | 120  | 300  | %    |
| CTR1/CTR2  | $CTR1 = I_{C1}/I_{F1}, CTR2 = I_{C2}/I_{F2}$   |        | 0.3  |      | 3    |      |





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| SWITCHING CHARACTERISTICS ( $T_{amb}$ = 25 °C, unless otherwise specified) |  |                  |      |      |      |      |
|--|--|------------------|------|------|------|------|
| PARAMETER  | TEST CONDITION   | SYMBOL           | MIN. | TYP. | MAX. | UNIT |
| Rise time  | $I_C$ = 5 mA, $V_{CC}$ = 5 V, $R_L$ = 100 $\Omega$     | tr               |      | 3    |      | μs   |
| Fall time  | $I_{C}$ = 5 mA, $V_{CC}$ = 5 V, $R_{L}$ = 100 $\Omega$ | t <sub>f</sub>   |      | 4    |      | μs   |
| Turn-on time   | $I_{C}$ = 5 mA, $V_{CC}$ = 5 V, $R_{L}$ = 100 $\Omega$ | t <sub>on</sub>  |      | 5    |      | μs   |
| Turn-off time  | $I_C$ = 5 mA, $V_{CC}$ = 5 V, $R_L$ = 100 $\Omega$     | t <sub>off</sub> |      | 3    |      | μs   |

| SAFETY AND INSULATION RATINGS |                            |        |      |           |      |      |  |
|-------------------------------|----------------------------|--------|------|-----------|------|------|--|
| PARAMETER                     | TEST CONDITION             | SYMBOL | MIN. | TYP.      | MAX. | UNIT |  |
| Climatic classification       | according to IEC 68 part 1 |        |      | 55/100/21 |      |      |  |
| Comparative tracking index    |                            | CTI    | 175  |           | 399  |      |  |
| V <sub>IOTM</sub>             |                            |        | 6000 |           |      | V    |  |
| V <sub>IORM</sub>             |                            |        | 707  |           |      | V    |  |
| P <sub>SO</sub>               |                            |        |      |           | 350  | mW   |  |
| I <sub>SI</sub>               |                            |        |      |           | 150  | mA   |  |
| T <sub>SI</sub>               |                            |        |      |           | 175  | °C   |  |
| Creepage distance             |                            |        | 5    |           |      | mm   |  |
| Clearance distance            |                            |        | 5    |           |      | mm   |  |
| Insulation thickness          |                            |        | 0.4  |           |      | mm   |  |

#### Note

• As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

### TYPICAL CHARACTERISTICS (Tamb = 25 °C, unless otherwise specified)

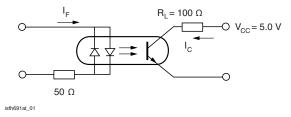
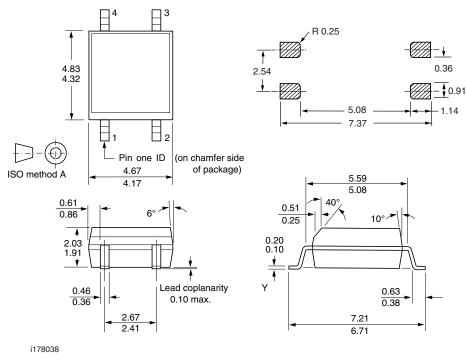


Fig. 1 - Linear Operation (without Saturation)

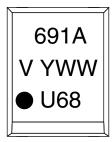


Vishay Semiconductors

## **PACKAGE DIMENSIONS** in millimeters



### **PACKAGE MARKING** (example)



### Note

• Tape and reel suffix (T) is not part of the package marking.



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