Vishay Siliconix

# P-Channel 20 V (D-S) MOSFET

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A)	Q <sub>g</sub> (TYP.) (nC)		
	0.760 at V <sub>GS</sub> = -4.5 V	-0.45			
-20	1.040 at V <sub>GS</sub> = -2.5 V	-0.40	1		
	1.500 at V <sub>GS</sub> = -1.8 V	-0.32			

# SC-89 (3 leads)

G Fop View

### **FEATURES**

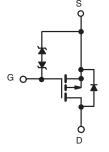
- TrenchFET® power MOSFET
- 100 % R<sub>g</sub> tested
- Typical ESD protection: 1000 V (HBM)
- Fast switching speed
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



FREE

### **APPLICATIONS**

- Load / power switch for portable devices
- Drivers: relays, solenoids, displays
- · Battery operated systems



P-Channel MOSFET

# Marking Code: 6 Ordering Information:

Si1013CX-T1-GE3 (Lead (Pb)-free and Halogen-free)

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C, unless otherwise noted)					
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-Source Voltage		V <sub>DS</sub>	-20	V	
Gate-Source Voltage		V <sub>GS</sub>	± 8	7 v	
Continuous Dunis Comment /T 150 °C\	T <sub>A</sub> = 25 °C		-0.45 <sup>b, c</sup>		
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 70 °C	I <sub>D</sub>	-0.36 <sup>b, c</sup>	^	
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	-1.5	A	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	Is	-0.16 <sup>b, c</sup>		
Maximum Daway Dissination	T <sub>A</sub> = 25 °C	В	0.19 <sup>b, c</sup>	W	
Maximum Power Dissipation	T <sub>A</sub> = 70 °C	P <sub>D</sub>	0.12 <sup>b, c</sup>	VV	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	°C	

THERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT		
Maximum Junction-to-Ambient <sup>a, b</sup>	t ≤ 5 s	Б	440	530	°C/W	
Maximum Junction-to-Ambient 4, 2	Steady State	$R_{thJA}$	540	650		

### Notes

- a. Maximum under steady state conditions is 650 °C/W.
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.



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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static					I		
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0$ , $I_D = -250 \mu A$	-20	-	-	V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$		-	-12	-	mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = -250 μA	-	1.8	-		
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = -250 \mu A$	-0.4	-	-1	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$	-	-	± 30	μΑ	
		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$	-	-	± 1		
	I <sub>DSS</sub> -	V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V	-	-	-1		
Zero Gate Voltage Drain Current		V <sub>DS</sub> = -20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 85 °C	-	-	-10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} = \ge 5 \text{ V}, V_{GS} = -4.5 \text{ V}$	-1.5	-	-	Α	
	, ,	$V_{GS} = -4.5 \text{ V}, I_D = -0.4 \text{ A}$	-	0.630	0.760		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = -2.5 \text{ V}, I_D = -0.2 \text{ A}$	-	0.865	1.040	Ω	
		$V_{GS} = -1.8 \text{ V}, I_D = -0.1 \text{ A}$	-	1.200	1.500		
Forward Transconductance	9fs	$V_{DS} = -10 \text{ V}, I_D = 0.4 \text{ A}$	-	1	-	S	
Dynamic <sup>b</sup>				1	,		
Input Capacitance	C <sub>iss</sub>		-	45	-	pF	
Output Capacitance	Coss	$V_{DS} = -10 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$	-	15	-		
Reverse Transfer Capacitance	C <sub>rss</sub>		-	10	-		
Total Cata Chausa	Q <sub>g</sub>	$V_{DS} = -10 \text{ V}, V_{GS} = -4.5 \text{ V}, I_D = -0.4 \text{ A}$	- 1.65 2.5		2.50		
Total Gate Charge			-	1	2	nC	
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = -0 \text{ V}, V_{GS} = -2.5 \text{ V}, I_D = -0.4$	-	0.2	-		
Gate-Drain Charge	Q <sub>gd</sub>		-	0.26	-		
Gate Resistance	$R_g$	f = 1 MHz	2.4	12	24	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>		-	9	18		
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_L = 33.3 \Omega$	-	10	20		
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D \cong -0.3 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$	-	10	20		
Fall Time	t <sub>f</sub>		-	8	16		
Turn-On Delay Time	t <sub>d(on)</sub>		-	1	2	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = -10 \text{ V}, R_L = 33.3 \Omega$	-	8	16	- - -	
Turn-Off DelayTime	t <sub>d(off)</sub>	$I_D\cong$ -0.3 A, $V_{GEN}=$ -8 V, $R_g=$ 1 $\Omega$	-	9	18		
Fall Time	t <sub>f</sub>		-	5	10		
<b>Drain-Source Body Diode Characteris</b>	tics						
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>		-	-	-1.5	Α	
Body Diode Voltage	$V_{SD}$	I <sub>S</sub> = -0.3 A	-	-0.8	-1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>		-	16	24	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1 000 41/24 400 4/:	-	8	16	nC	
Reverse Recovery Fall Time	ta	$I_F = -0.3 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}$	-	11	-	ns	
Reverse Recovery Rise Time	t <sub>b</sub>		-	5	-		

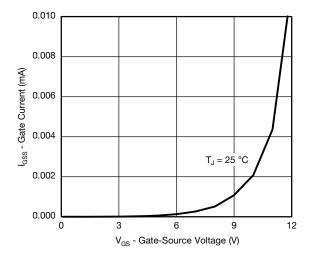
### Notes

- a. Pulse test; pulse width  $\leq 300~\mu s,$  duty cycle  $\leq 2~\%.$
- b. Guaranteed by design, not subject to production testing.

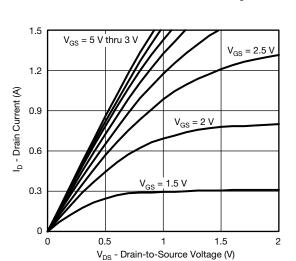
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



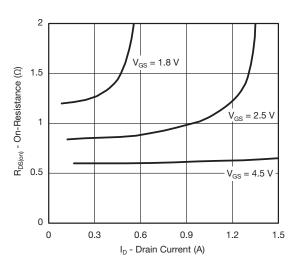
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



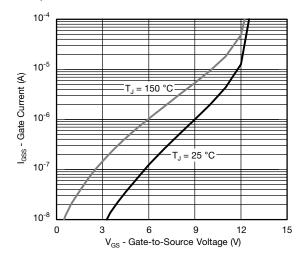
### Gate Current vs. Gate-Source Voltage



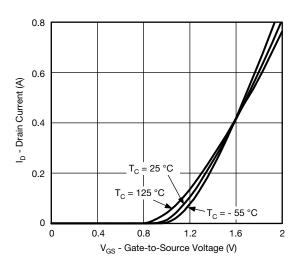
### **Output Characteristics**



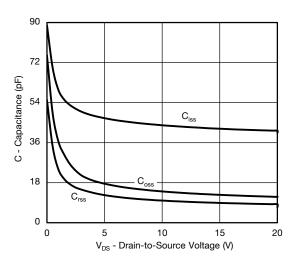
On-Resistance vs. Drain Current



**Gate Current vs. Gate-Source Voltage** 



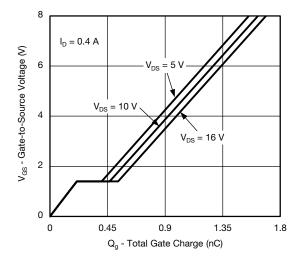
### **Transfer Characteristics**



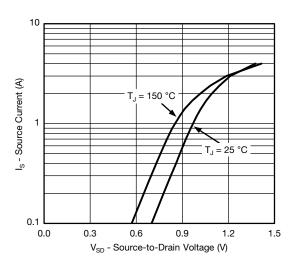
Capacitance



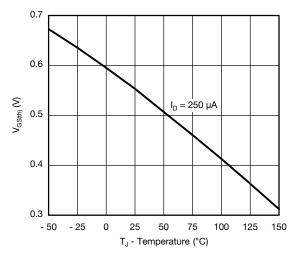
### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



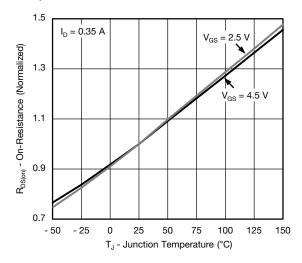
### **Gate Charge**



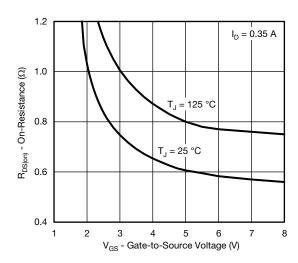
### Source-Drain Diode Forward Voltage



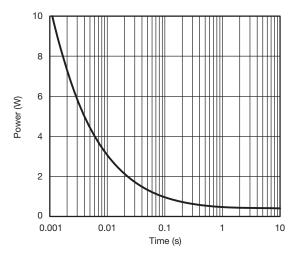
**Threshold Voltage** 



On-Resistance vs. Junction Temperature



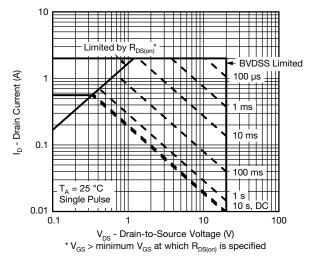
On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

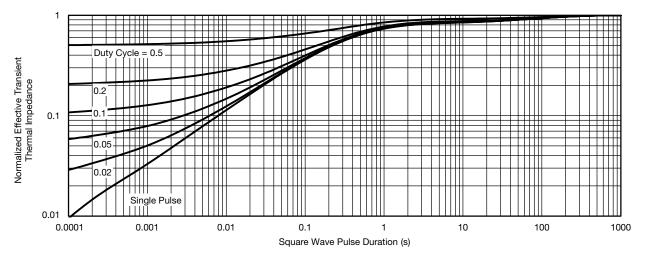


0.20
0.15
0.10
0.05
0 25 50 75 100 125 150
T<sub>A</sub> - Ambient Temperature (°C)

0.25

Safe Operating Area, Junction-to-Ambient

Power Derating, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Ambient

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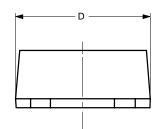
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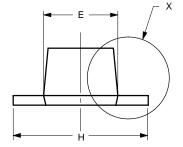


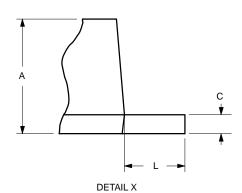


# Vishay Siliconix

### SC89-3





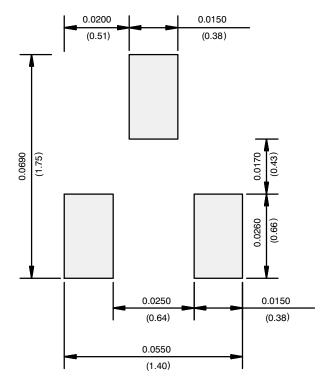


**MILLIMETERS INCHES** Dim Min Max Min Max 0.60 0.80 0.024 0.031 Α 0.23 0.33 0.009 0.013 b С 0.10 0.20 0.004 0.008 1.50 0.067 D 1.70 0.059 Ε 0.75 0.95 0.030 0.037 1.00 BSC 0.040 BSC е 0.50 BSC 0.020 BSC e<sub>1</sub> Н 1.50 0.059 0.067 0.30 0.012 0.020 L 0.50

ECN: S-03946—Rev. B, 09-Jul-01 DWG: 5869



### **RECOMMENDED MINIMUM PADS FOR SC-89: 3-Lead**



Recommended Minimum Pads Dimensions in Inches/(mm)

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Revision: 02-Oct-12 Document Number: 91000

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IPS70R2K0CEAKMA1 BUK954R8-60E DMN3404LQ-7 NTE6400 SQJ402EP-T1-GE3 2SK2614(TE16L1,Q) 2N7002KW-FAI

DMN1017UCP3-7 EFC2J004NUZTDG ECH8691-TL-W FCAB21350L1 P85W28HP2F-7071 DMN1053UCP4-7 NTE221 NTE2384

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NTE2911 US6M2GTR TK10A80W,S4X(S SSM6P69NU,LF