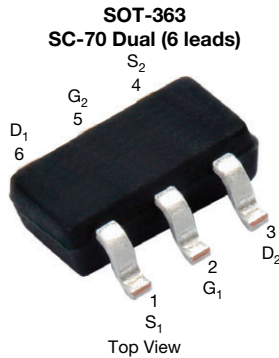


## N- and P-Channel 30 V (D-S) MOSFET



### FEATURES

- TrenchFET® power MOSFET
- 100 % R<sub>g</sub> tested
- Material categorization: for definitions of compliance please see [www.vishay.com/doc?99912](http://www.vishay.com/doc?99912)



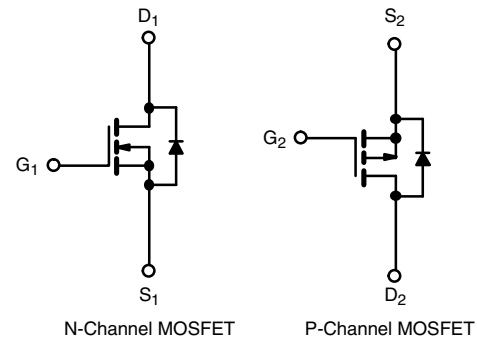
**RoHS**  
COMPLIANT  
HALOGEN  
**FREE**

### APPLICATIONS

- DC/DC converter
- Load switch

Marking code: RI

PRODUCT SUMMARY		
	N-CHANNEL	P-CHANNEL
V <sub>DS</sub> (V)	30	-30
R <sub>DS(on)</sub> (Ω) at V <sub>GS</sub> = ± 10 V	0.388	1.070
R <sub>DS(on)</sub> (Ω) at V <sub>GS</sub> = ± 4.5 V	0.525	2.590
Q <sub>g</sub> typ. (nC)	0.55	0.8
I <sub>D</sub> (A) <sup>a</sup>	0.7	-0.46
Configuration	Dual	



ORDERING INFORMATION	
Package	SOT-363
Lead (Pb)-free and halogen-free	Si1539DDL-T1-GE3

ABSOLUTE MAXIMUM RATINGS (T <sub>A</sub> = 25 °C, unless otherwise noted)				
PARAMETER	SYMBOL	N-CHANNEL	P-CHANNEL	UNIT
Drain-source voltage	V <sub>DS</sub>	30	-30	V
Gate-source voltage	V <sub>GS</sub>	± 20	± 20	
Continuous drain current (T <sub>J</sub> = 150 °C)	I <sub>D</sub>	T <sub>C</sub> = 25 °C	0.7	A
		T <sub>C</sub> = 70 °C	0.6	
		T <sub>A</sub> = 25 °C	0.7 <sup>b, c</sup>	
		T <sub>A</sub> = 70 °C	0.5 <sup>b, c</sup>	
Source-drain current diode current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	0.3	A
		T <sub>A</sub> = 25 °C	0.2 <sup>b, c</sup>	
Pulsed drain current (t = 100 μs)	I <sub>DM</sub>	2	-1	
Maximum power dissipation	P <sub>D</sub>	T <sub>C</sub> = 25 °C	0.34	W
		T <sub>C</sub> = 70 °C	0.22	
		T <sub>A</sub> = 25 °C	0.29 <sup>b, c</sup>	
		T <sub>A</sub> = 70 °C	0.18 <sup>b, c</sup>	
Operating junction and storage temperature range	T <sub>J</sub> , T <sub>stg</sub>	-55 to +150		°C

THERMAL RESISTANCE RATINGS							
PARAMETER	SYMBOL	N-CHANNEL		P-CHANNEL		UNIT	
		TYP.	MAX.	TYP.	MAX.		
Maximum junction-to-ambient <sup>b, d</sup>	R <sub>thJA</sub>	365	438	365	438	°C/W	
Maximum junction-to-foot (drain)	R <sub>thJF</sub>	308	370	308	370		

### Notes

- Based on T<sub>C</sub> = 25 °C
- Surface mounted on 1" x 1" FR4 board
- t = 10 s
- Maximum under steady state conditions is 486 °C/W (N-Channel) and 486 °C/W (P-Channel)



SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP. <sup>a</sup>	MAX.	UNIT	
<b>Static</b>							
Drain-source breakdown voltage	V <sub>DS</sub>	V <sub>GS</sub> = 0 V, I <sub>D</sub> = 250 μA	N-Ch	30	-	-	V
		V <sub>GS</sub> = 0 V, I <sub>D</sub> = -250 μA	P-Ch	-30	-	-	
V <sub>DS</sub> temperature coefficient	ΔV <sub>DS</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA	N-Ch	-	30	-	mV/°C
		I <sub>D</sub> = -250 μA	P-Ch	-	-25	-	
V <sub>GS(th)</sub> temperature coefficient	ΔV <sub>GS(th)</sub> /T <sub>J</sub>	I <sub>D</sub> = 250 μA	N-Ch	-	-3.6	-	
		I <sub>D</sub> = -250 μA	P-Ch	-	3.1	-	
Gate-source threshold voltage	V <sub>GS(th)</sub>	V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = 250 μA	N-Ch	1.2	-	2.5	V
		V <sub>DS</sub> = V <sub>GS</sub> , I <sub>D</sub> = -250 μA	P-Ch	-1.5	-	-3	
Gate-body leakage	I <sub>GSS</sub>	V <sub>DS</sub> = 0 V, V <sub>GS</sub> = ± 20 V	N-Ch	-	-	± 100	nA
			P-Ch	-	-	± 100	
Zero gate voltage drain current	I <sub>DSS</sub>	V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V	N-Ch	-	-	1	μA
		V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V	P-Ch	-	-	-1	
		V <sub>DS</sub> = 30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	N-Ch	-	-	10	
		V <sub>DS</sub> = -30 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C	P-Ch	-	-	-10	
On-state drain current <sup>b</sup>	I <sub>D(on)</sub>	V <sub>DS</sub> = 5 V, V <sub>GS</sub> = 10 V	N-Ch	2	-	-	A
		V <sub>DS</sub> = -5 V, V <sub>GS</sub> = -10 V	P-Ch	-1	-	-	
Drain-source on-state resistance <sup>b</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.6 A	N-Ch	-	0.323	0.388	Ω
		V <sub>GS</sub> = -10 V, I <sub>D</sub> = -0.4 A	P-Ch	-	0.890	1.070	
		V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.1 A	N-Ch	-	0.437	0.525	
		V <sub>GS</sub> = -5 V, I <sub>D</sub> = -0.1 A	P-Ch	-	1.850	2.590	
		V <sub>GS</sub> = -4.35 V, I <sub>D</sub> = -0.1 A	P-Ch	-	2.800	-	
Forward transconductance <sup>b</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 0.6 A	N-Ch	-	1.2	-	S
		V <sub>DS</sub> = -15 V, I <sub>D</sub> = -0.4 A	P-Ch	-	0.6	-	
<b>Dynamic <sup>a</sup></b>							
Input capacitance	C <sub>iss</sub>	N-Channel V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz P-Channel V <sub>DS</sub> = -15 V, V <sub>GS</sub> = 0 V, f = 1 MHz	N-Ch	-	28	-	pF
Output capacitance	C <sub>oss</sub>		P-Ch	-	21	-	
			N-Ch	-	10	-	
Reverse transfer capacitance	C <sub>rss</sub>		P-Ch	-	10	-	
			N-Ch	-	5	-	
Total gate charge	Q <sub>g</sub>		V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 10 V, I <sub>D</sub> = 0.6 A	N-Ch	-	1	
		V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -10 V, I <sub>D</sub> = -0.4 A	P-Ch	-	1.5	3	
Gate-source charge	Q <sub>gs</sub>	N-Channel V <sub>DS</sub> = 15 V, V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 0.6 A P-Channel V <sub>DS</sub> = -15 V, V <sub>GS</sub> = -4.5 V, I <sub>D</sub> = -0.4 A	N-Ch	-	0.55	1.1	
			P-Ch	-	0.8	1.2	
			N-Ch	-	0.2	-	
			P-Ch	-	0.4	-	
Gate-drain charge	Q <sub>gd</sub>	N-Ch	-	0.2	-		
		P-Ch	-	0.35	-		
Gate resistance	R <sub>g</sub>	f = 1 MHz	N-Ch	0.7	3.7	7.4	Ω
			P-Ch	0.3	15	30	



SPECIFICATIONS (T <sub>J</sub> = 25 °C, unless otherwise noted)							
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP. <sup>a</sup>	MAX.	UNIT	
<b>Dynamic <sup>a</sup></b>							
Turn-on delay time	t <sub>d(on)</sub>	N-Channel V <sub>DD</sub> = 15 V, R <sub>L</sub> = 30 Ω I <sub>D</sub> ≅ 0.5 A, V <sub>GEN</sub> = 10 V, R <sub>g</sub> = 1 Ω  P-Channel V <sub>DD</sub> = -15 V, R <sub>L</sub> = 38 Ω I <sub>D</sub> ≅ -0.4 A, V <sub>GEN</sub> = -10 V, R <sub>g</sub> = 1 Ω	N-Ch	-	2	4	ns
			P-Ch	-	4	8	
Rise time	t <sub>r</sub>		N-Ch	-	14	21	
			P-Ch	-	18	30	
Turn-off delay time	t <sub>d(off)</sub>		N-Ch	-	11	20	
			P-Ch	-	8	16	
Fall time	t <sub>f</sub>		N-Ch	-	9	18	
			P-Ch	-	18	30	
Turn-on delay time	t <sub>d(on)</sub>	N-Channel V <sub>DD</sub> = 15 V, R <sub>L</sub> = 30 Ω I <sub>D</sub> ≅ 0.5 A, V <sub>GEN</sub> = 4.5 V, R <sub>g</sub> = 1 Ω  P-Channel V <sub>DD</sub> = -15 V, R <sub>L</sub> = 50 Ω I <sub>D</sub> ≅ -0.3 A, V <sub>GEN</sub> = -4.5 V, R <sub>g</sub> = 1 Ω	N-Ch	-	26	39	
			P-Ch	-	22	33	
Rise time	t <sub>r</sub>		N-Ch	-	25	38	
			P-Ch	-	28	42	
Turn-off delay time	t <sub>d(off)</sub>		N-Ch	-	14	21	
			P-Ch	-	4	8	
Fall Time	t <sub>f</sub>		N-Ch	-	15	23	
			P-Ch	-	18	30	
<b>Drain-Source Body Diode Characteristics</b>							
Continuous source-drain diode current	I <sub>S</sub>	T <sub>C</sub> = 25 °C	N-Ch	-	-	0.3	A
			P-Ch	-	-	-0.3	
Pulse diode forward current (t = 100 μs)	I <sub>SM</sub>		N-Ch	-	-	2	
			P-Ch	-	-	-1	
Body diode voltage	V <sub>SD</sub>	I <sub>S</sub> = 0.5 A	N-Ch	-	0.8	1.2	V
		I <sub>S</sub> = -0.4 A	P-Ch	-	-0.8	-1.2	
Body diode reverse recovery time	t <sub>rr</sub>	N-Channel I <sub>F</sub> = 0.5 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C  P-Channel I <sub>F</sub> = -0.5 A, di/dt = -100 A/μs, T <sub>J</sub> = 25 °C	N-Ch	-	10	20	ns
			P-Ch	-	13	20	
Body diode reverse recovery charge	Q <sub>rr</sub>		N-Ch	-	3	6	nC
			P-Ch	-	8	16	
Reverse recovery fall time	t <sub>a</sub>		N-Ch	-	6	-	ns
			P-Ch	-	7	-	
Reverse recovery rise time	t <sub>b</sub>		N-Ch	-	4	-	
			P-Ch	-	6	-	

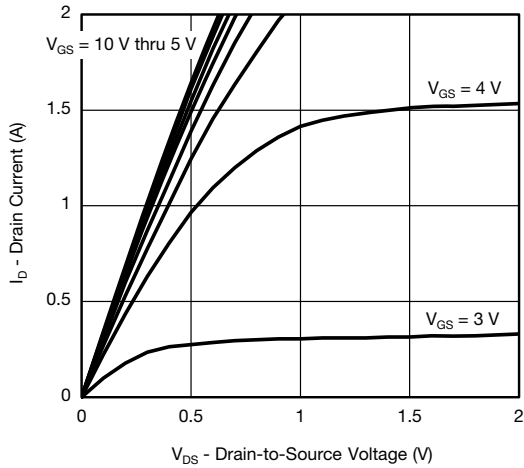
**Notes**

- a. Guaranteed by design, not subject to production testing
- b. Pulse test; pulse width ≤ 300 μs, duty cycle ≤ 2 %

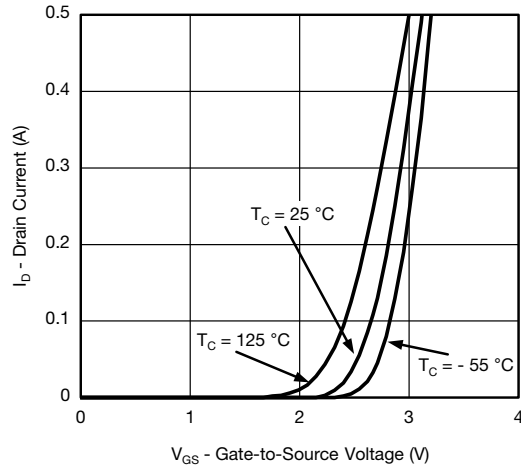
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



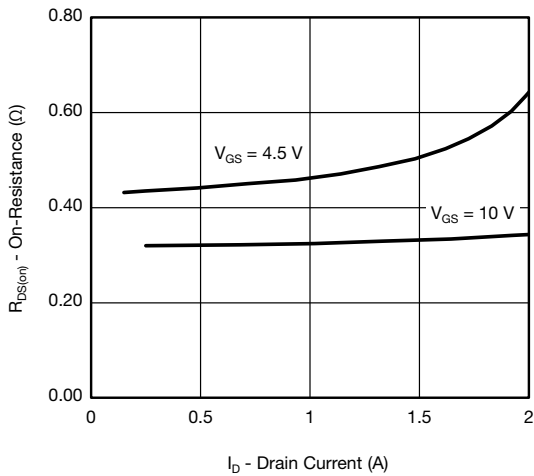
**N-CHANNEL TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



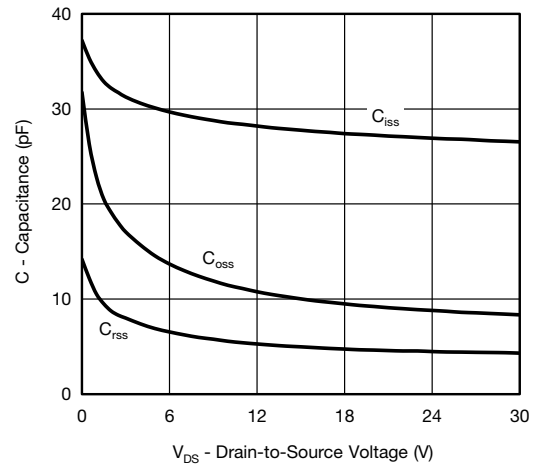
**Output Characteristics**



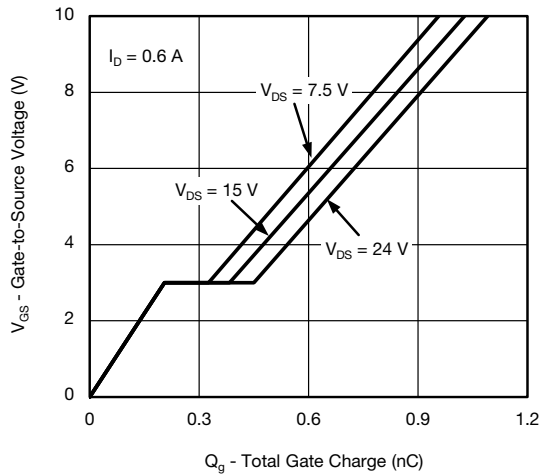
**Transfer Characteristics**



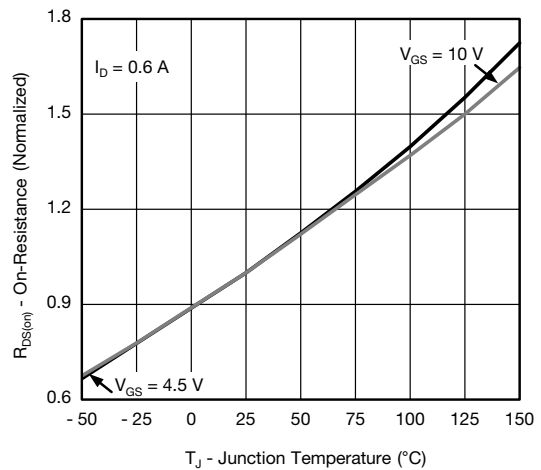
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**



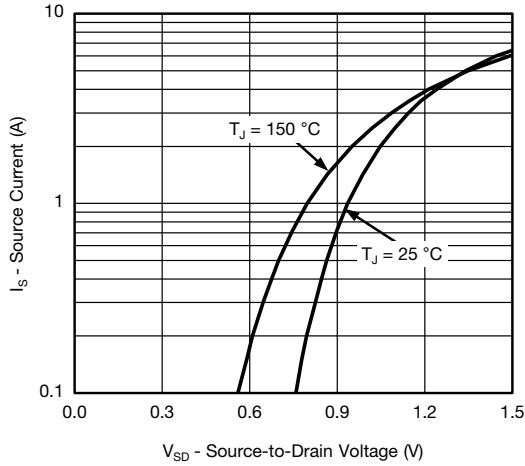
**Gate Charge**



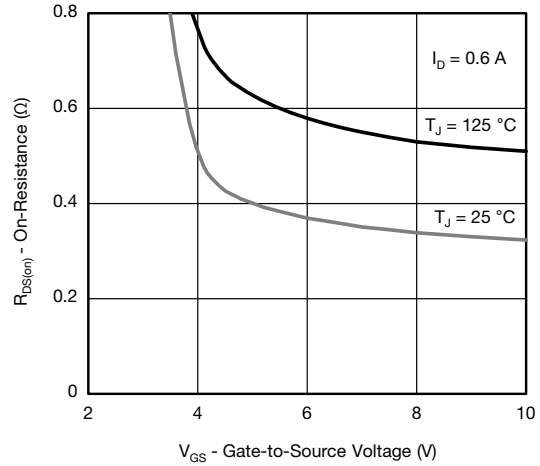
**On-Resistance vs. Junction Temperature**



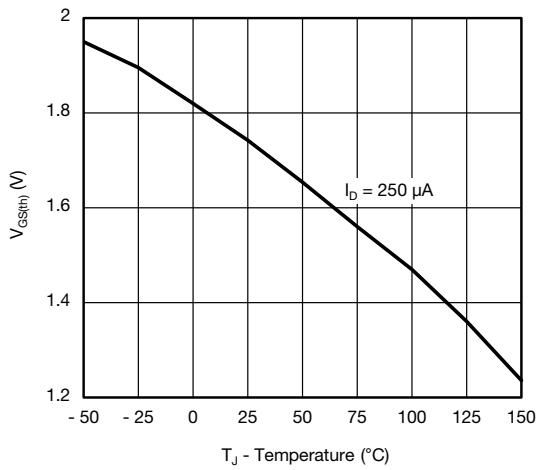
**N-CHANNEL TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



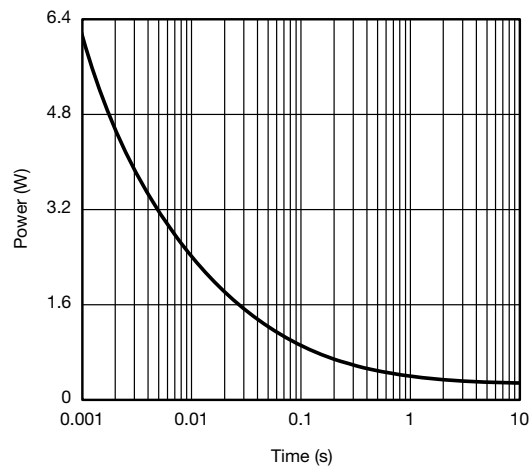
**Source-Drain Diode Forward Voltage**



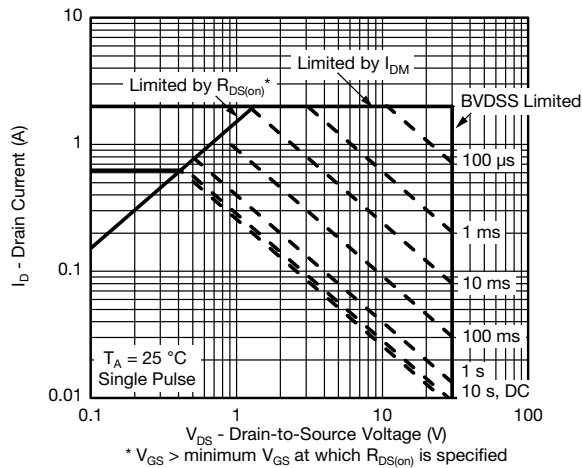
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**



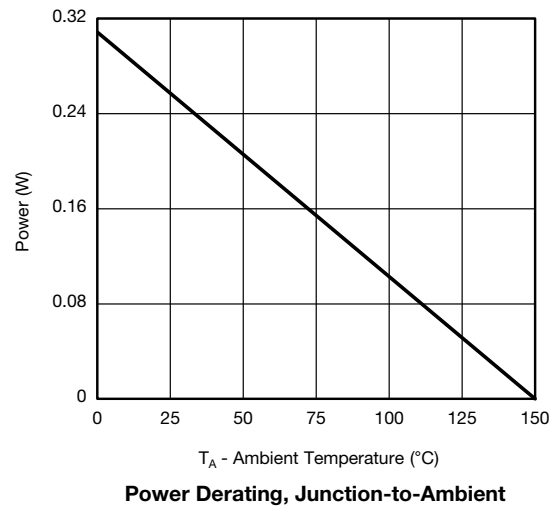
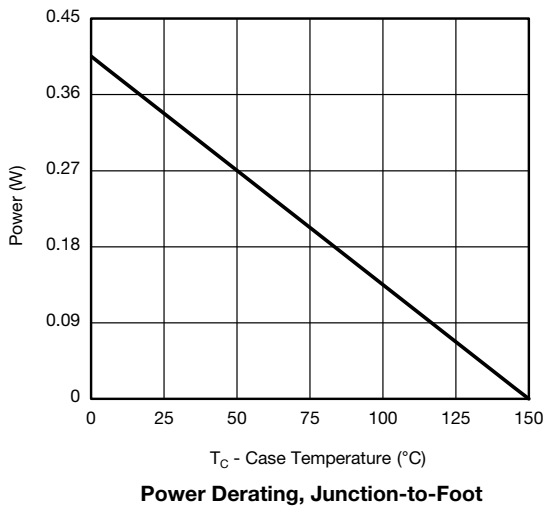
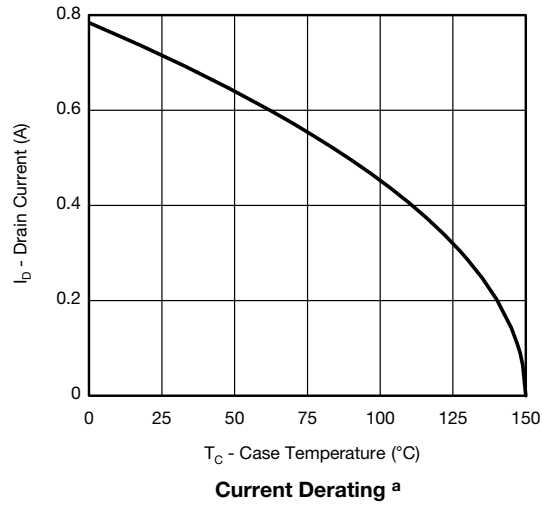
**Single Pulse Power, Junction-to-Ambient**



**Safe Operating Area, Junction-to-Ambient**



**N-CHANNEL TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)

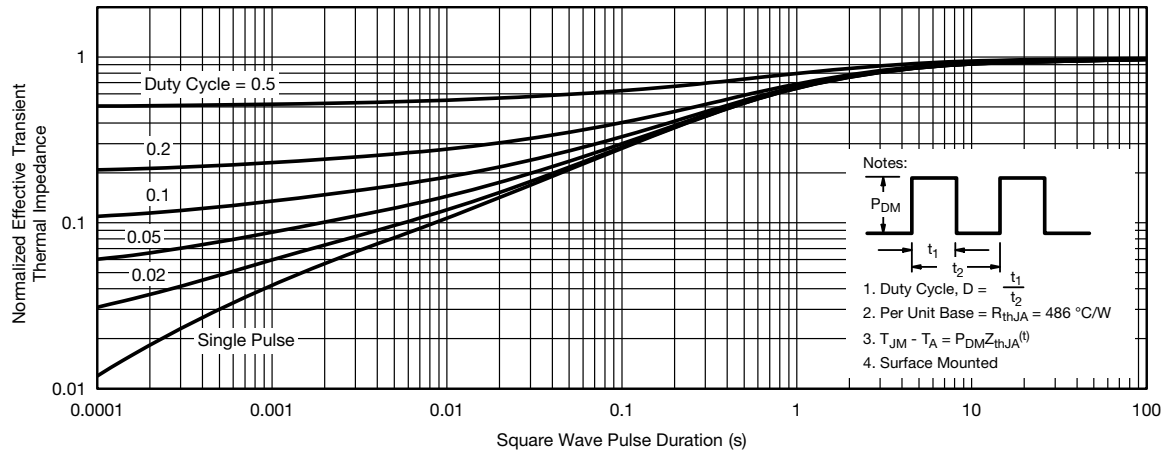


**Note**

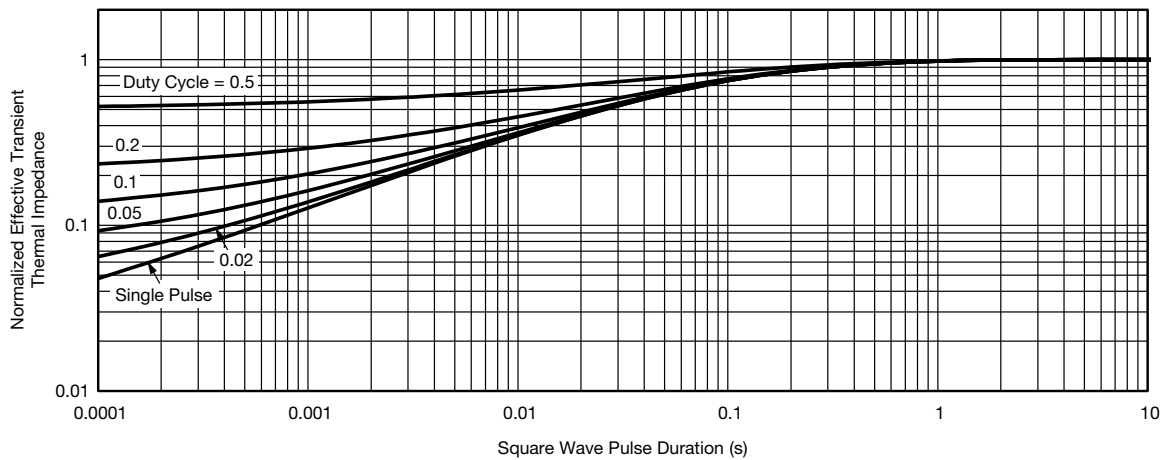
- a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



**N-CHANNEL TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



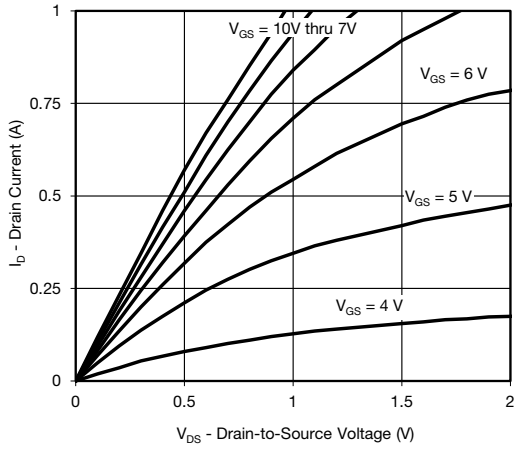
**Normalized Thermal Transient Impedance, Junction-to-Ambient**



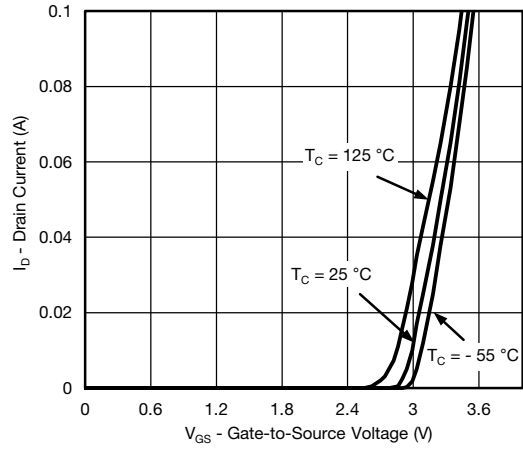
**Normalized Thermal Transient Impedance, Junction-to-Foot**



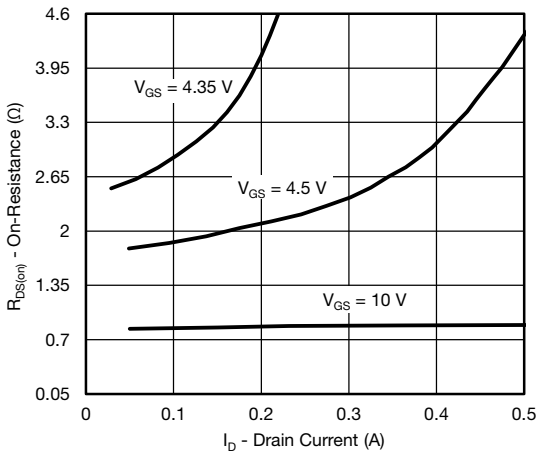
**P-CHANNEL TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



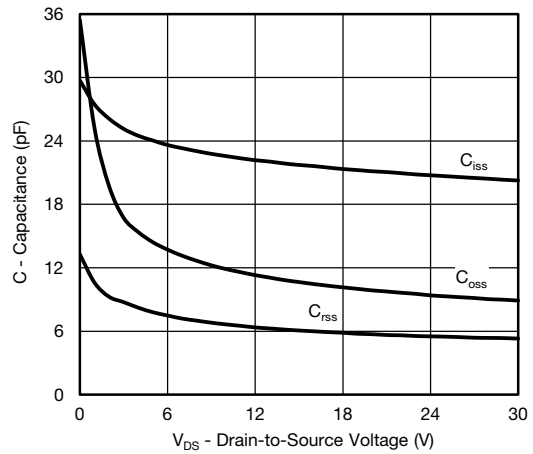
**Output Characteristics**



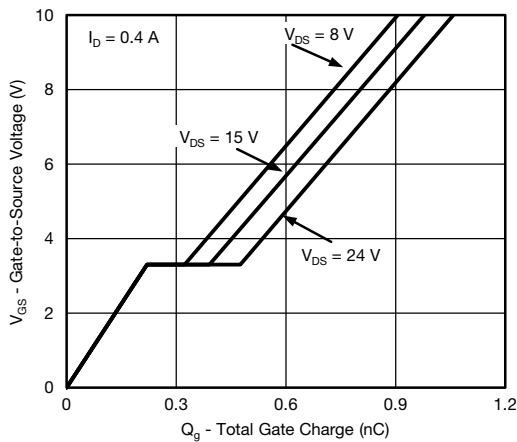
**Transfer Characteristics**



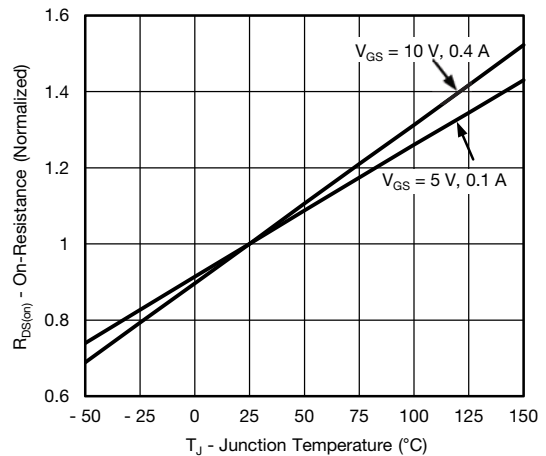
**On-Resistance vs. Drain Current and Gate Voltage**



**Capacitance**



**Gate Charge**

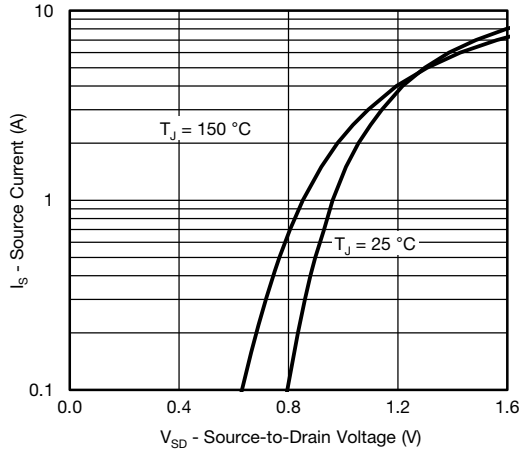


**On-Resistance vs. Junction Temperature**

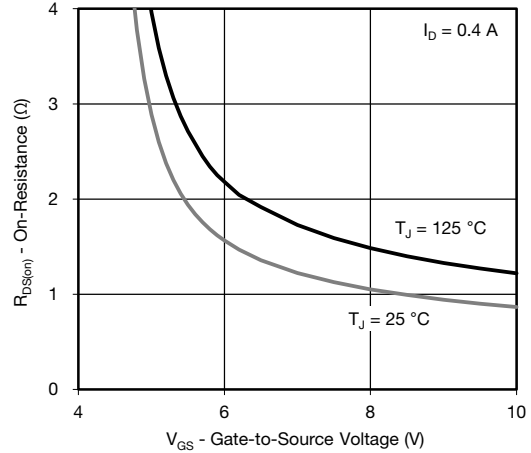




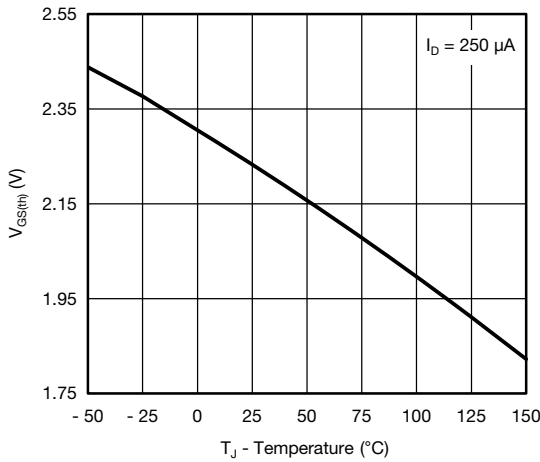
**P-CHANNEL TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



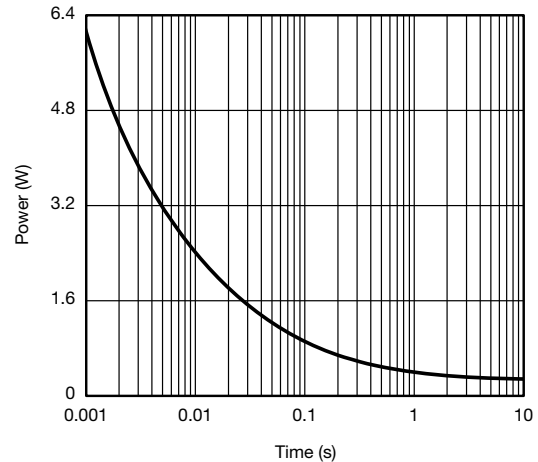
**Source-Drain Diode Forward Voltage**



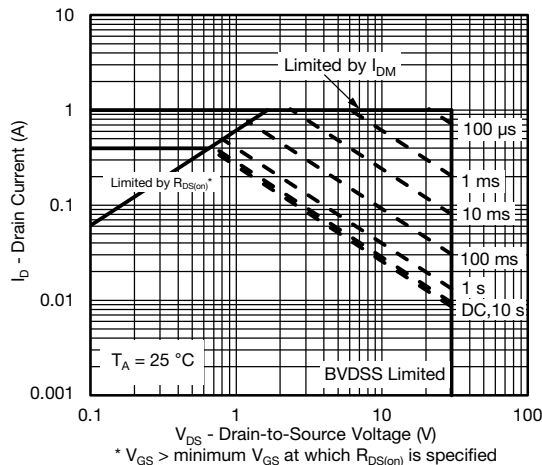
**On-Resistance vs. Gate-to-Source Voltage**



**Threshold Voltage**



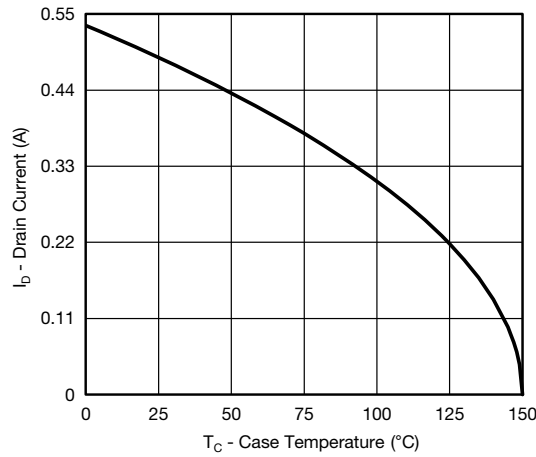
**Single Pulse Power, Junction-to-Ambient**



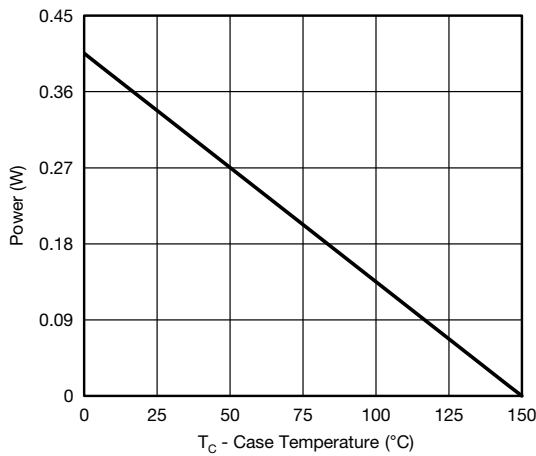
**Safe Operating Area, Junction-to-Ambient**



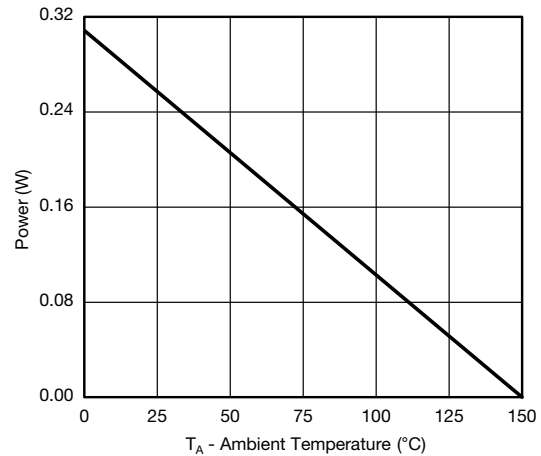
**P-CHANNEL TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**Current Derating <sup>a</sup>**



**Power Derating, Junction-to-Foot**



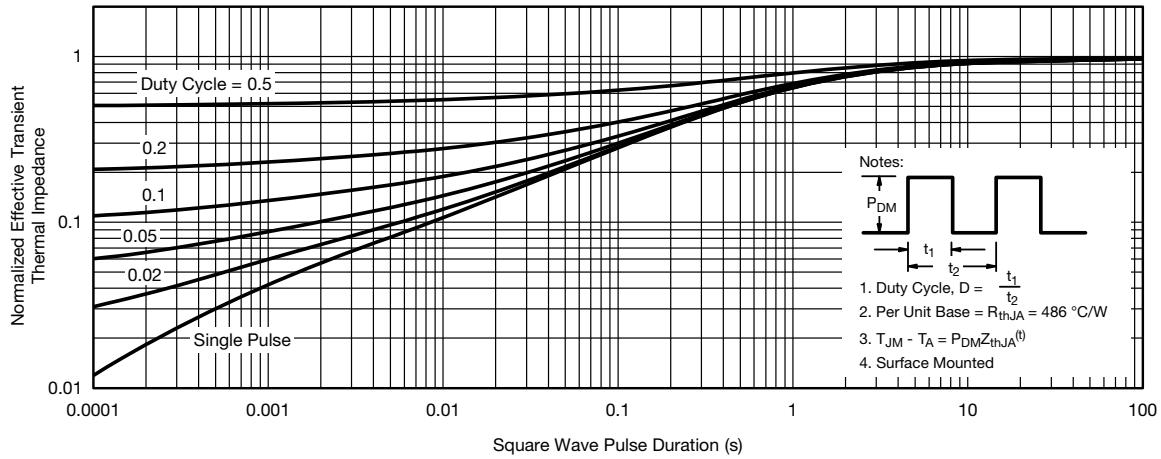
**Power Derating, Junction-to-Ambient**

**Note**

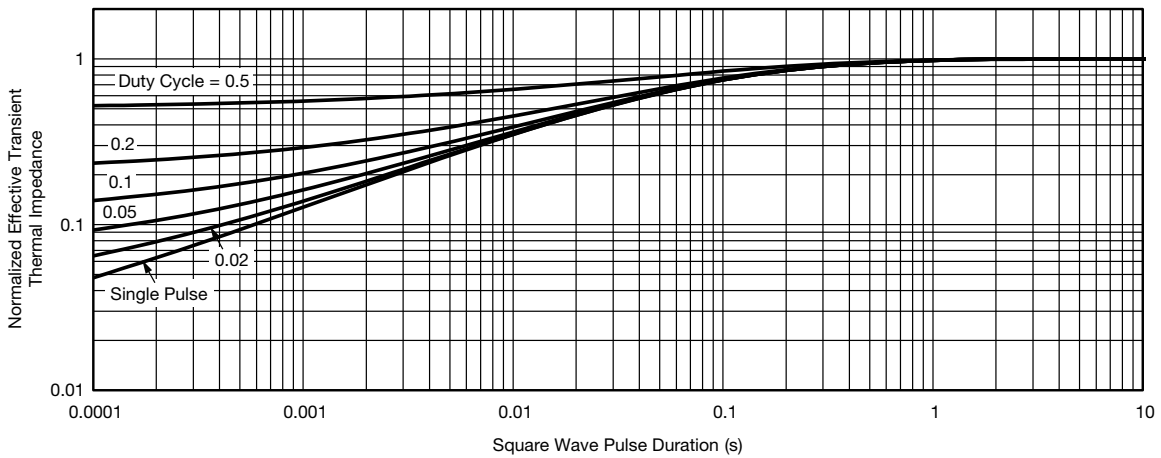
- a. The power dissipation  $P_D$  is based on  $T_J$  max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



**P-CHANNEL TYPICAL CHARACTERISTICS** (25 °C, unless otherwise noted)



**Normalized Thermal Transient Impedance, Junction-to-Ambient**



**Normalized Thermal Transient Impedance, Junction-to-Foot**

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