

**Vishay Siliconix** 

# P-Channel 30-V (D-S) MOSFET

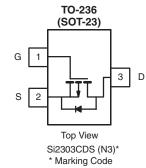
MOSFET PRODUCT SUMMARY					
V <sub>DS</sub> (V)	R <sub>DS(on)</sub> (Ω)	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)		
- 30	0.190 at V <sub>GS</sub> = - 10 V	- 2.7	2 nC		
- 30	0.330 at V <sub>GS</sub> = - 4.5 V	- 2.1	2110		

#### **FEATURES**

- Halogen-free According to IEC 61249-2-21
  Available
- TrenchFET<sup>®</sup> Power MOSFET
- 100 % R<sub>g</sub> Tested
- 100 % UIS Tested

#### **APPLICATIONS**

· Load Switch



Ordering Information: Si2303CDS-T1-E3 (Lead (Pb)-free) Si2303CDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter		Symbol	Limit	Unit
Drain-Source Voltage	V <sub>DS</sub>	- 30	V	
Gate-Source Voltage	V <sub>GS</sub>	± 20	v	
	T <sub>C</sub> = 25 °C		- 2.7	
Continuous Drain Current (T <sub>.1</sub> = 150 °C)	T <sub>C</sub> = 70 °C		- 2.2	
Continuous Drain Guneric (1) = 130°C)	T <sub>A</sub> = 25 °C	I <sub>D</sub>	- 1.9 <sup>b, c</sup>	
	T <sub>A</sub> = 70 °C		- 1.5 <sup>b, c</sup>	A
Pulsed Drain Current		I <sub>DM</sub>	- 10	A
Continuous Source-Drain Diode Current	T <sub>C</sub> = 25 °C	la la	- 1.75	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	I <sub>S</sub>	- 0.83 <sup>b, c</sup>	
Avalanche Current	L = 0.1 mH	I <sub>AS</sub>	- 5	
Single Pulse Avalanche Energy		E <sub>AS</sub>	1.25	mJ
	T <sub>C</sub> = 25 °C		2.3	
Maximum Dawar Dissinction	T <sub>C</sub> = 70 °C	P <sub>D</sub>	1.5	w
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	'D	1.0 <sup>b, c</sup>	vv
	T <sub>A</sub> = 70 °C		0.7 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range	T <sub>J</sub> , T <sub>stq</sub>	- 55 to 150	°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	≤ 5 s	R <sub>thJA</sub>	80	120	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	35	55	0/10	

Notes:

a. Based on T<sub>C</sub> = 25 °C.

b. Surface Mounted on 1" x 1" FR4 board.

d. Maximum under Steady State conditions is 160 °C/W.



HALOGEN FREE Available

c. t = 5 s.

# Si2303CDS

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static	-					<u>I</u>	
Drain-Source Breakdown Voltage	V <sub>DS</sub>	$V_{DS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$	- 30			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			- 27		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = - 250 μΑ		3.8			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_{D} = -250 \ \mu A$	- 1		- 3	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 20 V$			± 100	nA	
Zara Cata Valtaga Drain Current	1	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}$			- 1	μA	
Zero Gate Voltage Drain Current	IDSS	$V_{DS}$ = - 30 V, $V_{GS}$ = 0 V, $T_{J}$ = 55 °C			- 10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \leq$ - 5 V, $V_{GS}$ = - 10 V	- 10			Α	
	Р	V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 1.9 A		0.158	0.190		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = - 4.5 V, I <sub>D</sub> = - 1.4 A		0.275	0.330	Ω	
Forward Transconductance <sup>a</sup>	rward Transconductance <sup>a</sup> $g_{fs}$ $V_{DS} = -5 V, I_D = -1.9 A$			2		S	
Dynamic <sup>b</sup>	<u> </u>				<u>.</u>		
Input Capacitance	C <sub>iss</sub>			155			
Output Capacitance	C <sub>oss</sub>	V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = 0 V, f = 1 MHz		35		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			25			
		V <sub>DS</sub> = - 15 V, V <sub>GS</sub> = - 10 V, I <sub>D</sub> = - 1.9 A		4	8	nC	
Total Gate Charge				2	4		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS}$ = - 15 V, $V_{GS}$ = - 4.5 V, $I_{D}$ = - 1.9 A		0.6			
Gate-Drain Charge	Q <sub>gd</sub>			1			
Gate Resistance	R <sub>g</sub>	f = 1 MHz	1.7	8.5	17	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			4	8		
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 10 $\Omega$		11	18		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D$ = - 1.5 A, $V_{GEN}$ = - 10 V, $R_G$ = 1 $\Omega$		11	18		
Fall Time	t <sub>f</sub>			8	16		
Turn-On Delay Time	t <sub>d(on)</sub>			36	44	ns	
Rise Time	t <sub>r</sub>	$V_{DD}$ = - 15 V, $R_L$ = 10 $\Omega$		37	45	-	
Turn-Off Delay Time	t <sub>d(off)</sub>	${\rm I_D}\cong$ - 1.5 A, ${\rm V_{GEN}}$ = - 4.5 V, ${\rm R_G}$ = 1 $\Omega$		12	18		
Fall Time	t <sub>f</sub>			9	14		
Drain-Source Body Diode Characteristic	cs				<u> </u>		
Continuous Source-Drain Diode Current	ا <sub>S</sub>	T <sub>C</sub> = 25 °C			- 1.75	•	
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				- 10	A	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = - 1.5 A		- 0.8	- 1.2	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			17	26	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>			9	14	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	I <sub>F</sub> = - 1.5 A, di/dt = 100 A/μs, T <sub>J</sub> = 25 °C		12			
Reverse Recovery Rise Time	t <sub>b</sub>			5		ns	

Notes:

a. Pulse test; pulse width  $\leq$  300 µs, duty cycle  $\leq$  2 %.

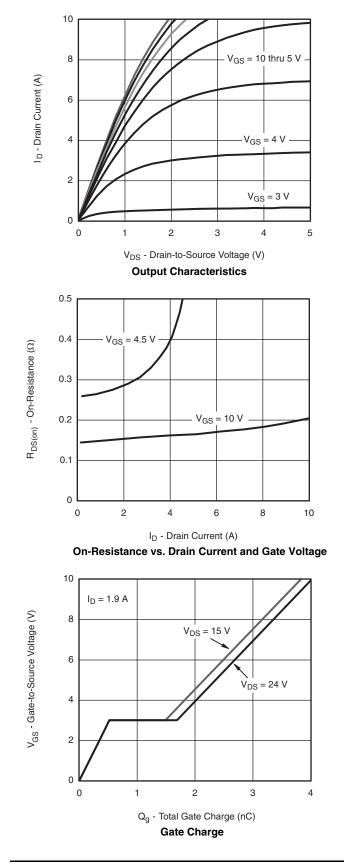
b. Guaranteed by design, not subject to production testing.

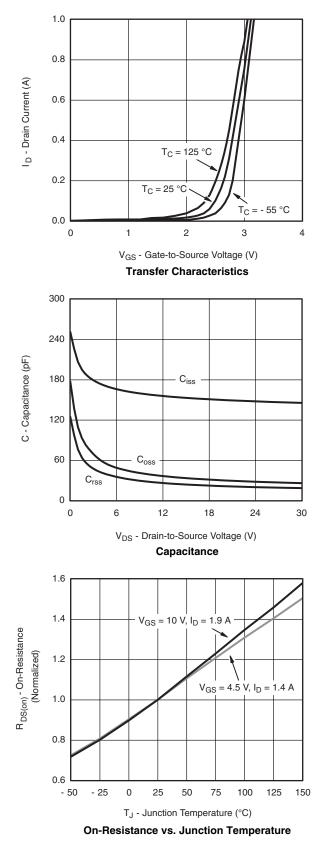
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



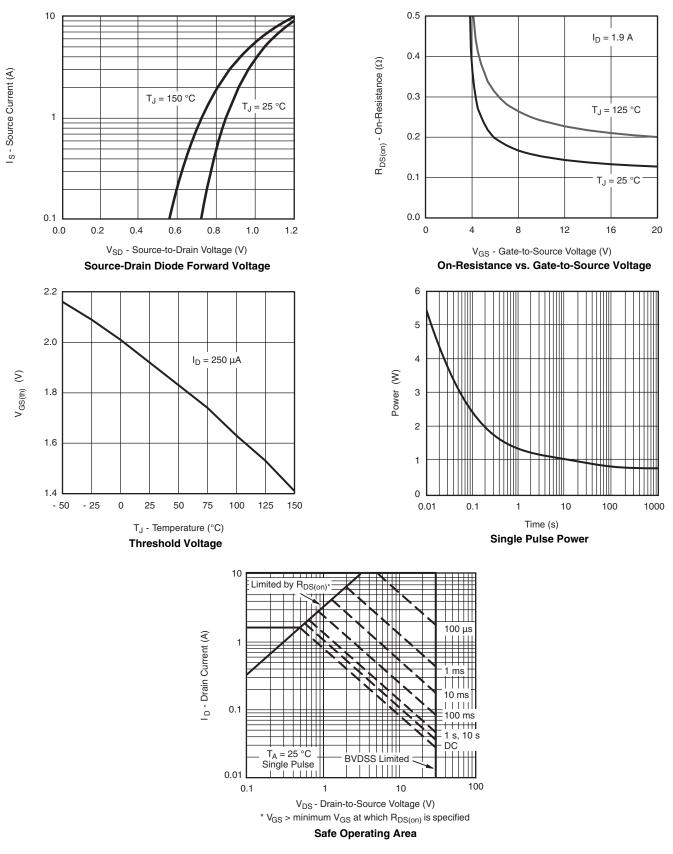


# Si2303CDS

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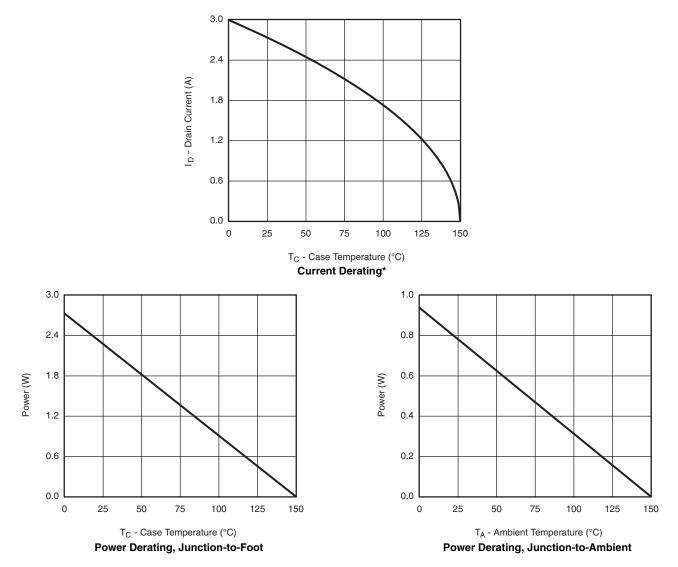
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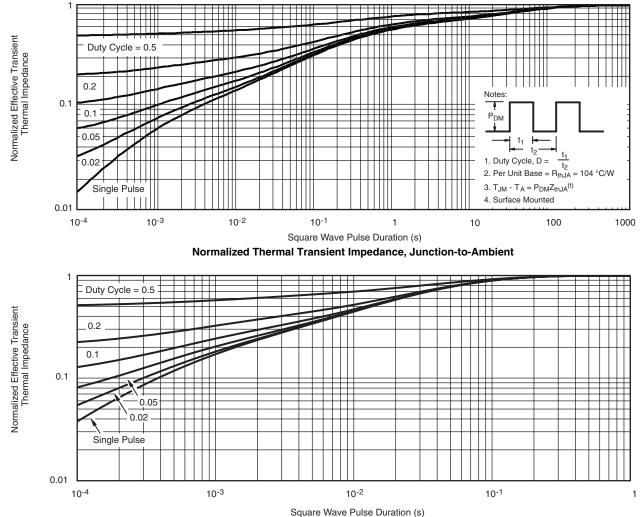
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\* The power dissipation  $P_D$  is based on  $T_{J(max.)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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#### TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see <u>www.vishay.com/ppg?69991</u>.



# Package Information

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#### SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES			
	Min	Max	Min	Мах		
Α	0.89	1.12	0.035	0.044		
A <sub>1</sub>	0.01	0.10	0.0004	0.004		
A <sub>2</sub>	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E <sub>1</sub>	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.0374 Ref			
e <sub>1</sub>	1.90	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L <sub>1</sub>	0.64 Ref		0.025	5 Ref		
S	0.50 Ref		0.020	) Ref		
q	3°	8°	3°	8°		



# Application Note 826

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#### **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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