## Si2312CDS

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#### Marking code: P5

PRODUCT SUMMARY					
V <sub>DS</sub> (V)	20				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 4.5 V	0.0318				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 2.5 V	0.0356				
$R_{DS(on)}$ max. ( $\Omega$ ) at $V_{GS}$ = 1.8 V	0.0414				
Q <sub>g</sub> typ. (nC)	8.8				
I <sub>D</sub> (A) <sup>a, e</sup>	6				
Configuration	Single				

#### **FEATURES**

N-Channel 20 V (D-S) MOSFET

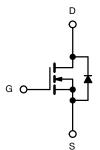
- TrenchFET<sup>®</sup> power MOSFET
- 100% R<sub>g</sub> tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **APPLICATIONS**

- DC/DC converters
- Load switch for portable applications



RoHS COMPLIANT HALOGEN



#### N-Channel MOSFET

#### **ORDERING INFORMATION**

	Package	SOT-23
ľ	Lead (Pb)-free and halogen-free	Si2312CDS-T1-GE3

ABSOLUTE MAXIMUM RATING	<b>S</b> (T <sub>A</sub> = 25 °C, ι	Inless otherwise I	noted)	
PARAMETER		SYMBOL	LIMIT	UNIT
Drain-source voltage		V <sub>DS</sub>	20	V
Gate-source voltage		V <sub>GS</sub>	± 8	v
	T <sub>C</sub> = 25 °C		6 <sup>a</sup>	
Continuous drain current (T <sub>J</sub> = 150 °C)	T <sub>C</sub> = 70 °C		5.1	
	T <sub>A</sub> = 25 °C	I <sub>D</sub>	5 b, c	
	T <sub>A</sub> = 70 °C		4 b, c	A
Pulsed drain current		I <sub>DM</sub>	20	
Continuous source-drain diode current	T <sub>C</sub> = 25 °C		1.75	
	T <sub>A</sub> = 25 °C	I <sub>S</sub>	1.04 <sup>b, c</sup>	
Maximum power dissipation	T <sub>C</sub> = 25 °C		2.1	
	T <sub>C</sub> = 70 °C		1.3	
	T <sub>A</sub> = 25 °C	P <sub>D</sub>	1.25 <sup>b, c</sup>	W
	T <sub>A</sub> = 70 °C	1	0.8 <sup>b, c</sup>	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>stg</sub>	-55 to +150	*0
Soldering recommendations (peak temperature)			260	°C

#### THERMAL RESISTANCE RATINGS

PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient b, d	t≤5 s	R <sub>thJA</sub>	80	100	°C/W
Maximum junction-to-foot (drain)	Steady state	R <sub>thJF</sub>	40	60	0/10

Notes

a. Package limited

b. Surface mounted on 1" x 1" FR4 board

c. t = 5 s

d. Maximum under steady state conditions is 125 °C/W

e. Based on  $T_C = 25 \ ^{\circ}C$ 

S10-0641-Rev. A, 22-Mar-10

1

Document Number: 65900

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static			•		•	
Drain-source breakdown voltage	V <sub>DS</sub>	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = 250 \mu\text{A}$	20	-	-	V
V <sub>DS</sub> temperature coefficient	$\Delta V_{DS}/T_{J}$		-	25	-	
V <sub>GS(th)</sub> temperature coefficient	$\Delta V_{GS(th)}/T_J$	I <sub>D</sub> = 250 μA	-	-2.6	-	mV/°C
Gate-source threshold voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}, I_D = 250 \ \mu A$	0.45	-	1	V
Gate-source leakage	I <sub>GSS</sub>	$V_{DS} = 0 V, V_{GS} = \pm 8 V$	-	-	± 100	nA
		$V_{DS} = 20 V, V_{GS} = 0 V$	-	-	1	μA
Zero gate voltage drain current	IDSS	V <sub>DS</sub> = 20 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 70 °C	-	-	10	
On-state drain current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \le 5 \text{ V}, \text{ V}_{GS} = 4.5 \text{ V}$	20	-	-	А
		$V_{GS} = 4.5 \text{ V}, I_{D} = 5 \text{ A}$	-	0.0265	0.0318	
Drain-source on-state resistance <sup>a</sup>	R <sub>DS(on)</sub>	$V_{GS} = 2.5 \text{ V}, I_D = 4.7 \text{ A}$	-	0.0296	0.0356	Ω
		V <sub>GS</sub> = 1.8 V, I <sub>D</sub> = 4.3 A	-	0.0345	0.0414	
Forward transconductance <sup>a</sup>	g <sub>fs</sub>	V <sub>DS</sub> = 10 V, I <sub>D</sub> = 5 A	-	24	-	S
Dynamic <sup>b</sup>					1	1
Input capacitance	C <sub>iss</sub>		-	865	-	
Output capacitance	C <sub>oss</sub>	V <sub>DS</sub> = 10 V, V <sub>GS</sub> = 0 V, f = 1 MHz	-	105	-	pF
Reverse transfer capacitance	C <sub>rss</sub>		-	55	-	
	$V_{DS} = 10 \text{ V}, V_{GS} = 5 \text{ V}, I_{D} = 5 \text{ A}$	-	12	18		
Total gate charge	Qg		-	8.8	14	nC
Gate-source charge	Q <sub>qs</sub>	$V_{DS} = 10 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 5 \text{ A}$	_	1.1	_	
Gate-drain charge	Q <sub>ad</sub>		_	0.7	_	
Gate resistance	R <sub>g</sub>	f = 1 MHz	0.5	2.4	4.8	Ω
Turn-on delay time	t <sub>d(on)</sub>		-	8	16	
Rise time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, \text{ R}_{\text{I}} = 2.2 \Omega$	_	17	26	4
Turn-off delay time	t <sub>d(off)</sub>	$I_D \cong 4 \text{ A}, V_{\text{GEN}} = 4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	_	31	47	
Fall time	t <sub>f</sub>		-	8	16	
Turn-on delay time	t <sub>d(on)</sub>		-	5	10	ns
Rise time	t <sub>r</sub>	$V_{DD} = 10 \text{ V}, \text{ R}_{\text{I}} = 2.2 \Omega$		13	20	
Turn-off delay time	t <sub>d(off)</sub>	$V_{DD} = 10$ V, $H_{L} = 2.2$ S2 $I_{D} \cong 4$ A, $V_{GEN} = 5$ V, $R_{g} = 1 \Omega$	-	21	32	-
Fall time	t <sub>f</sub>		_	6	12	
Drain-Source Body Diode Characteristi	· · ·		I			
Continuous source-drain diode current	Is I	T <sub>C</sub> = 25 °C	-	-	1.75	
Pulse diode forward current	I <sub>SM</sub>	10 - 20 0	-	-	20	A
Body diode voltage	V <sub>SD</sub>	$I_{\rm S} = 4$ A, $V_{\rm GS} = 0$ V		0.75	1.2	v
, ,		15 - 77, VGS - 0 V	-	12	20	ns
Body diode reverse recovery time	t <sub>rr</sub>			5	20 10	
Body diode reverse recovery charge	Q <sub>rr</sub>	I <sub>F</sub> = 4 A, di/dt = 100 A/μs, T <sub>.1</sub> = 25 °C	-		10	nC
Reverse recovery fall time	ta	13-25 0	-	7		ns

Notes

a. Pulse test; pulse width  $\leq 300~\mu s,~duty~cycle \leq 2~\%$ 

b. Guaranteed by design, not subject to production testing

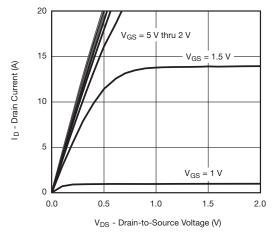
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2

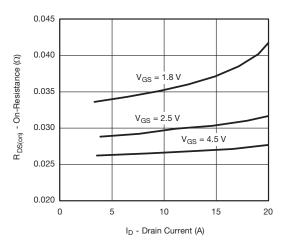


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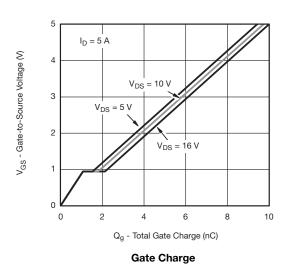
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

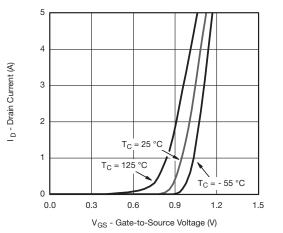




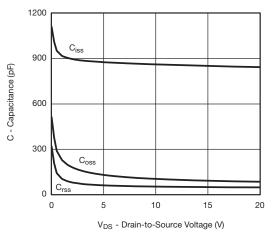


**On-Resistance vs. Drain Current and Gate Voltage** 

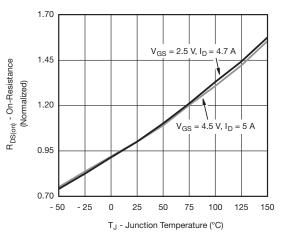




Transfer Characteristics



Capacitance



**On-Resistance vs. Junction Temperature** 

S10-0641-Rev. A, 22-Mar-10

3

Document Number: 65900

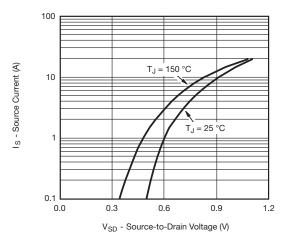
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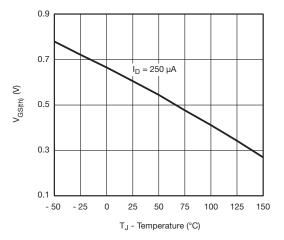
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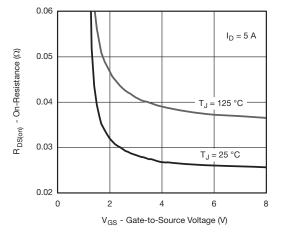
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



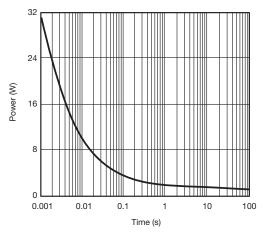
Source-Drain Diode Forward Voltage



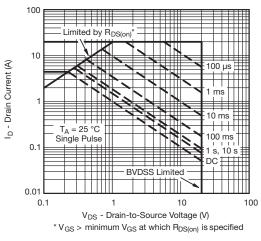




**On-Resistance vs. Gate-to-Source Voltage** 



Single Pulse Power (Junction-to-Ambient)



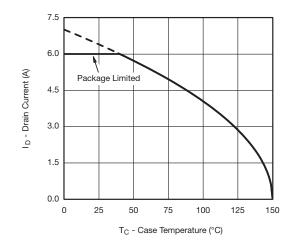
Safe Operating Area, Junction-to-Ambient

4

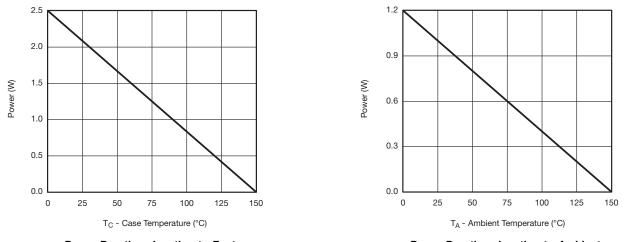


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#### TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)







Power Derating, Junction-to-Foot



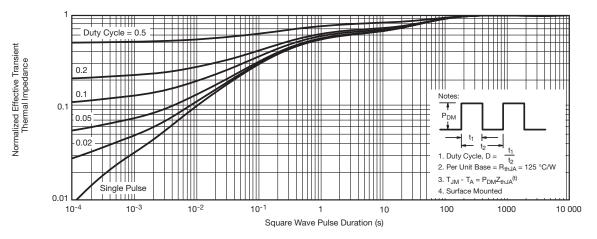
#### Note

a. The power dissipation P<sub>D</sub> is based on T<sub>J</sub> max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit

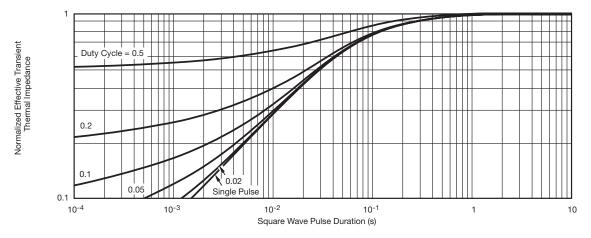


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## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for silicon technology and package reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?65900.

6



# Package Information

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## SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES			
	Min	Max	Min	Мах		
Α	0.89	1.12	0.035	0.044		
A <sub>1</sub>	0.01	0.10	0.0004	0.004		
A <sub>2</sub>	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E <sub>1</sub>	1.20	1.40	0.047	0.055		
е	0.95	0.95 BSC		0.0374 Ref		
e <sub>1</sub>	1.90 BSC		0.0748 Ref			
L	0.40	0.60	0.016	0.024		
L <sub>1</sub>	0.6	4 Ref	0.025	5 Ref		
S	0.50 Ref		0.020 Ref			
q	3°	8°	3°	8°		



# Application Note 826

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## **RECOMMENDED MINIMUM PADS FOR SOT-23**



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index



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