



P-Channel 1.8-V (G-S) MOSFET

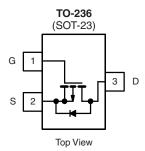
PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}(\Omega)$	I _D (A)		
	0.050 at V _{GS} = - 4.5 V	- 3.85		
- 12	0.065 at V _{GS} = - 2.5 V	- 3.4		
	0.100 at V _{GS} = - 1.8V	- 2.7		

FEATURES

- Halogen-free Option Available
- TrenchFET® Power MOSFETs: 1.8 V Rated



RoHS*



Si2315BDS *(M5)

* Marking Code

Ordering Information: Si2315BDS-T1

Si2315BDS-T1-E3 (Lead (Pb)-free)

Si2315BDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 12		V	
Gate-Source Voltage		V _{GS}	± 8			
Continuous Dunin Comment /T 150 90\8	T _A = 25 °C	- I _D	- 3.85	- 3.0		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 70 °C		- 3.0	- 2.45		
Pulsed Drain Current ^a		I _{DM}	- 12		Α	
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.0	- 0.62	I	
Danier Directoralisma	T _A = 25 °C	- P _D	1.19	0.75	W	
Power Dissipation ^a	T _A = 70 °C] ' ['] D	0.76	0.48	, vv	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS					
Parameter		Symbol	Тур.	Max.	Unit
Mariana Indiana Indian	t ≤ 5 s	R _{thJA}	85	105	°C/W
Maximum Junction-to-Ambient ^a	Steady State		130	166	
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	75	

Notes:

a. Surface Mounted on FR4 board.

b. t ≤ 5 s.

For SPICE model information via the Worldwide Web: http://www.vishay.com/www/product/spice.htm.

* Pb containing terminations are not RoHS compliant, exemptions may apply.

Si2315BDS

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SPECIFICATIONS T _J = 25 °C, unless otherwise noted								
-			Limits			Unit		
Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Onit		
Static	Static							
Drain-Source Breakdown Voltage	V _{(BR)DSS}	$V_{GS} = 0 \text{ V}, I_D = -10 \mu\text{A}$	- 12			V		
Gate-Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \mu A$	- 0.45		- 0.90	V		
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA		
Zoro Coto Voltogo Droin Current	l	V _{DS} = - 12 V, V _{GS} = 0 V			- 1	μА		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -12 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10			
0.00.00.00.00.00	1	$V_{DS} \le -5 \text{ V}, V_{GS} = -4.5 \text{ V}$	- 6			А		
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -2.5 \text{ V}$	- 3					
		V _{GS} = - 4.5 V, I _D = - 3.85 A		0.040	0.050			
Drain-Source On Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 3.4 A		0.050	0.065	Ω		
		V _{GS} = - 1.8 V, I _D = - 2.7 A		0.071	0.100			
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 3.85 A		7		S		
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.6 A, V _{GS} = 0 V			- 1.2	V		
Dynamic ^b								
Total Gate Charge	Qg	V 6VV 45V		8	15			
Gate-Source Charge	Q _{gs}	$V_{DS} = -6 \text{ V}, V_{GS} = -4.5 \text{ V}$ $I_{D} \cong -3.85 \text{ A}$		1.1		nC		
Gate-Drain Charge	Q _{gd}	ID = - 0.00 A		2.3				
Input Capacitance	C _{iss}			715				
Output Capacitance	C _{oss}	$V_{DS} = -6 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		275		pF		
Reverse Transfer Capacitance	C _{rss}			200		1		
Switching ^b								
Turn-On Time	t _{d(on)}	V 0VP 00		15	20			
Turn-On Time	t _r	$V_{DD} = -6 \text{ V}, R_L = 6 \Omega$ $I_D \cong -1.0 \text{ A}, V_{GEN} = -4.5 \text{ V}$		35	50	ne		
Turn-Off Time	t _{d(off)}	$R_G = 6 \Omega$		50	70	ns		
Turr-On Time	t _f	u -		50	75			

Notes:

- a. For DESIGN AID ONLY, not subject to production testing.
- b. Pulse test: PW $\leq 300~\mu s$ duty cycle $\leq 2~\%.$
- c. Switching time is essentially independent of operating temperature.

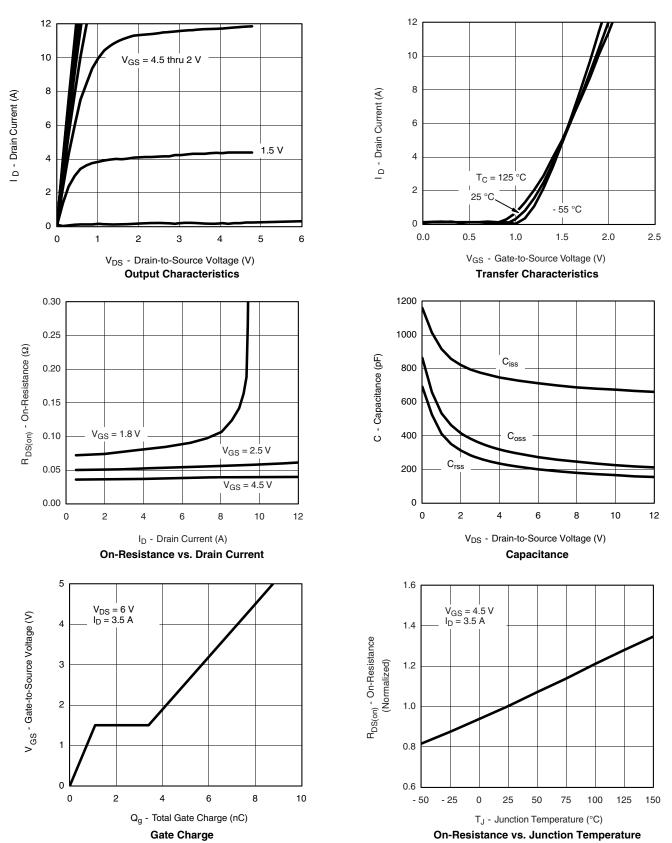
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.





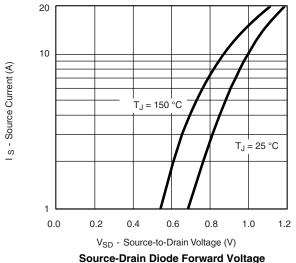


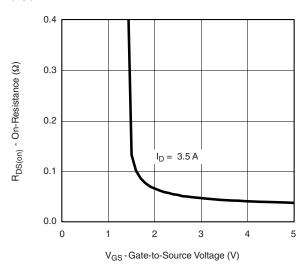
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



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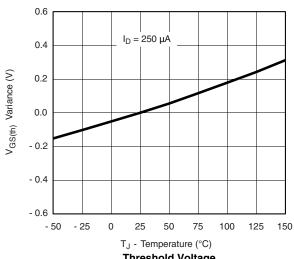
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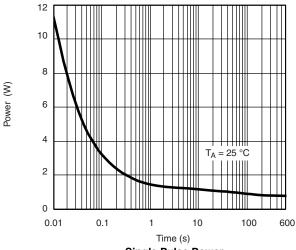




Source-Drain Diode Forward Voltage

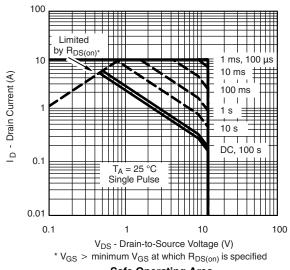






Threshold Voltage

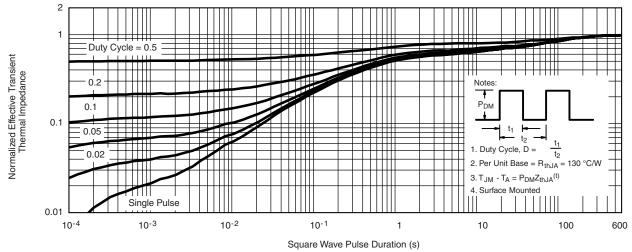
Single Pulse Power



Safe Operating Area



TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Ambient

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Document Number: 72014 S-80642-Rev. E, 24-Mar-08

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SOT-23 (TO-236): 3-LEAD







Dim	MILLI	METERS	INCHES			
	Min	Max	Min	Max		
Α	0.89	1.12	0.035	0.044		
A ₁	0.01	0.10	0.0004	0.004		
A ₂	0.88	1.02	0.0346	0.040		
b	0.35	0.50	0.014	0.020		
С	0.085	0.18	0.003	0.007		
D	2.80	3.04	0.110	0.120		
E	2.10	2.64	0.083	0.104		
E ₁	1.20	1.40	0.047	0.055		
е	0.95 BSC		0.0374 Ref			
e ₁	1.90 BSC		0.074	0.0748 Ref		
L	0.40	0.60	0.016	0.024		
L ₁	0.64 Ref		0.025 Ref			
S	0.50 Ref		0.020 Ref			
q	3°	8°	3°	8°		
FCN: S-03946-Rev K 09-	lul-01	•				

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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APPLICATION NOTE



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