Si2369DS

RoHS

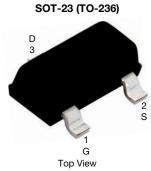
COMPLIANT

HALOGEN

FREE

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Marking code: H9

PRODUCT SUMMARY					
V _{DS} (V)	-30				
$R_{DS(on)}$ max. (Ω) at V_{GS} = -10 V	0.029				
$R_{DS(on)}$ max. (Ω) at V_{GS} = -6 V	0.034				
$R_{DS(on)}$ max. (Ω) at V_{GS} = -4.5 V	0.040				
Q _g typ. (nC)	11.4				
I _D (A) ^a	-7.6				
Configuration	Single				

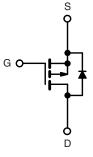
FEATURES

P-Channel 30 V (D-S) MOSFET

- TrenchFET® power MOSFET
- 100 % R_g tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- For mobile computing
- Load switch
- Notebook adaptor switch
- DC/DC converter



P-Channel MOSFET

ORDERING INFORMATION				
Package	SOT-23			
Lead (Pb)-free and halogen-free	Si2369DS-T1-GE3			

ABSOLUTE MAXIMUM RATING	iS (T _A = 25 °C, u	nless otherv	vise noted)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	-30	V	
Gate-source voltage		V _{GS}	± 20	v	
Continuous drain current (T _J = 150 °C)	T _C = 25 °C		-7.6		
	T _C = 70 °C	1.	-6.1		
	T _A = 25 °C	I _D	-5.4 ^{b, c}		
	T _A = 70 °C		-4.3 ^{b, c}	А	
Pulsed drain current (t = 100 μs)		I _{DM}	-80		
	T _C = 25 °C		-2.1		
Continuous source-drain diode current	T _A = 25 °C	I _S	-1 ^{b, c}		
	T _C = 25 °C		2.5		
Maximum power dissipation	T _C = 70 °C		1.6		
	T _A = 25 °C	P _D	1.25 ^{b, c}	W	
	T _A = 70 °C		0.8 ^{b, c}		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	TYPICAL	MAXIMUM	UNIT
Maximum junction-to-ambient b, d	$t \le 5 s$	R _{thJA}	75	100	°C/W
Maximum junction-to-foot (drain)	Steady state	R _{thJF}	40	50	C/W

Notes

a. Based on T_C = 25 °C

b. Surface mounted on 1" x 1" FR4 board

c. t = 5 s

d. Maximum under steady state conditions is 166 °C/W

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Si2369DS

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SPECIFICATIONS ($T_J = 25 \ ^{\circ}C$, u	inless otherv	wise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT	
Static	•		•	•	•	•	
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, \text{ I}_{D} = -250 \mu\text{A}$	-30	-	-	V	
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	1 250 4	-	-19	-		
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$	I _D = -250 μA	-	4	-	mV/°C	
Gate-source threshold voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = -250 \ \mu A$	-1.2	-	-2.5	V	
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 20 V$	-	-	± 100	nA	
Zero gate voltage drain current	lass	$V_{DS} = -30 \text{ V}, \text{ V}_{GS} = 0 \text{ V}$	-	-	-1		
Zero gate voltage drain current	I _{DSS}	V_{DS} = -30 V, V_{GS} = 0 V, T_{J} = 55 $^{\circ}C$	-	-	-5	μA	
On-state drain current ^a	I _{D(on)}	$V_{DS} \leq$ -5 V, V_{GS} = -10 V	-25	-	-	А	
		$V_{GS} = -10 \text{ V}, \text{ I}_{D} = -5.4 \text{ A}$	-	0.024	0.029		
Drain-source on-state resistance ^a	R _{DS(on)}	$V_{GS} = -6 V, I_D = -5 A$	-	0.028	0.034	Ω	
		$V_{GS} = -4.5 \text{ V}, \text{ I}_{D} = -4.6 \text{ A}$	-	0.033	0.040		
Forward transconductance ^a	9 _{fs}	$V_{DS} = -15 \text{ V}, \text{ I}_{D} = -5.4 \text{ A}$	-	18	-	S	
Dynamic ^b							
Input capacitance	C _{iss}		-	1295	-	pF	
Output capacitance	C _{oss}	$V_{DS} = -15 \text{ V}, V_{GS} = 0 \text{ V}, \text{ f} = 1 \text{ MHz}$	-	150	-		
Reverse transfer capacitance	C _{rss}		-	130	-		
Tabal a sha sha sa	0	$V_{DS} = -15 \text{ V}, \text{ V}_{GS} = -10 \text{ V}, \text{ I}_{D} = -5.4 \text{ A}$	-	24	36	nC	
Total gate charge	Qg		-	11.4	17		
Gate-source charge	Q _{gs}	V_{DS} = -15 V, V_{GS} = -4.5 V, I_D = -5.4 A	-	3.4	-		
Gate-drain charge	Q _{gd}		-	3.8	-		
Gate resistance	Rg	f = 1 MHz	1.5	7.7	15.4	Ω	
Turn-on delay time	t _{d(on)}		-	13	20	1	
Rise time	t _r	$V_{DD} = -15 \text{ V}, \text{ R}_{L} = 3.5 \Omega$	-	4	8		
Turn-off delay time	t _{d(off)}	$I_D\cong$ -4.3 A, V_{GEN} = -10 V, R_g = 1 Ω	-	38	57		
Fall time	t _f		-	6	12		
Turn-on delay time	t _{d(on)}		-	28	42	ns	
Rise time	t _r	$V_{DD} = -15 \text{ V}, \text{ R}_{L} = 3.5 \Omega$	-	16	24		
Turn-off delay time	t _{d(off)}	$I_D \cong -4.3$ Å, $V_{GEN} = -4.5$ V, $R_g = 1 \ \Omega$	-	30	45		
Fall time	t _f		-	10	20		
Drain-Source Body Diode Characteristi	cs					1	
Continuous source-drain diode current	I _S	T _C = 25 °C	-	-	-2.1	•	
Pulse diode forward current (t = 100 µs)	I _{SM}		-	-	-80	A	
Body diode voltage	V _{SD}	I _S = -4.3 A, V _{GS} = 0 V	-	-0.8	-1.2	V	
Body diode reverse recovery time	t _{rr}		-	15	23	ns	
Body diode reverse recovery charge	Q _{rr}	I _F = -4.3 A, di/dt = 100 A/μs,	-	7	14	nC	
Reverse recovery fall time	t _a	$T_J = 25 \text{°C}$	-	8	-		
Reverse recovery rise time	t _b	1	-	7	-	ns	

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

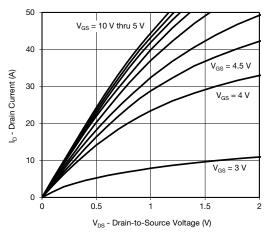
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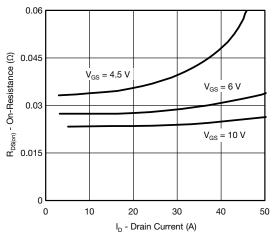


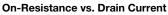
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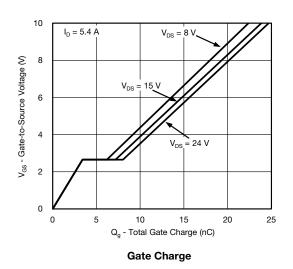
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

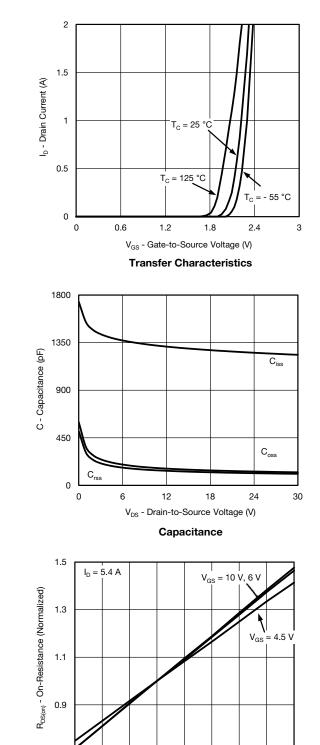












- 50 - 25 0 25 50 75 100 125 150 T_J - Junction Temperature (°C)

On-Resistance vs. Junction Temperature

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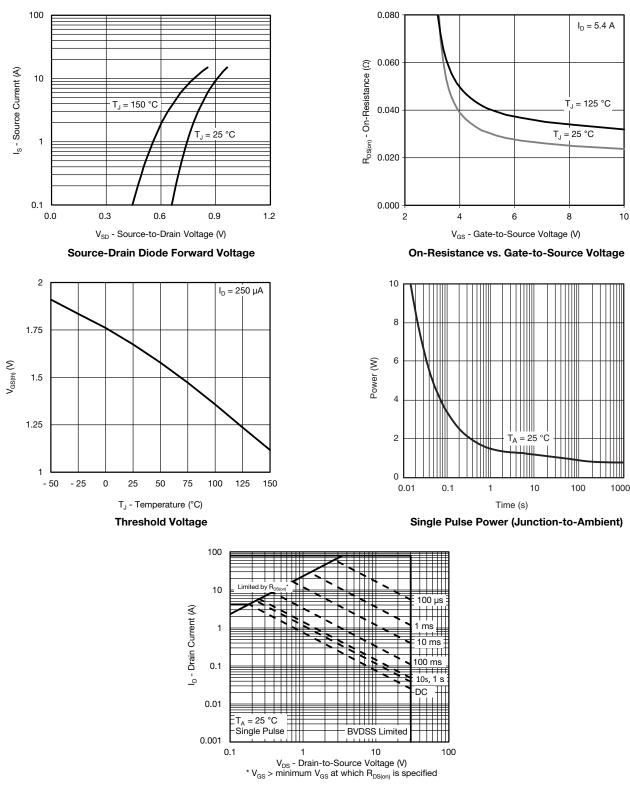
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



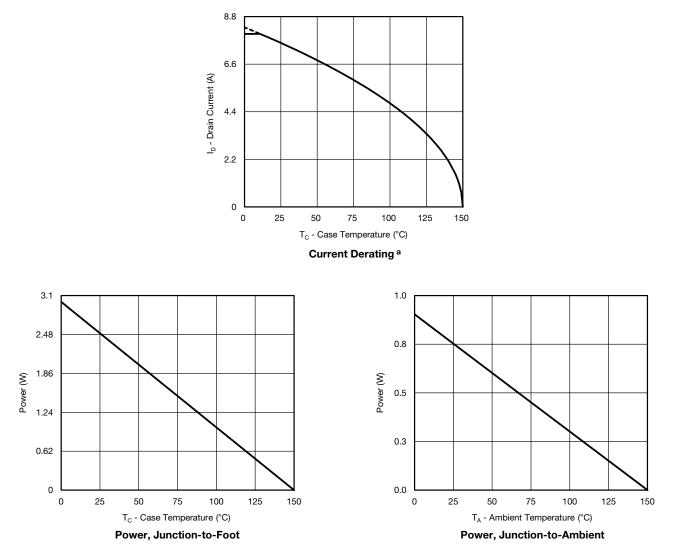
Safe Operating Area, Junction-to-Ambient

4



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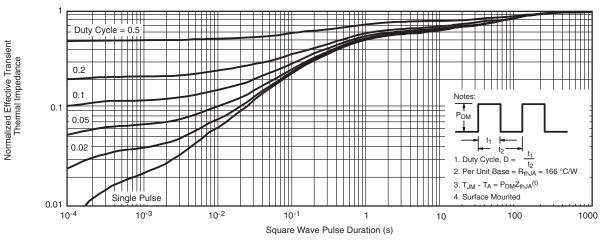
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



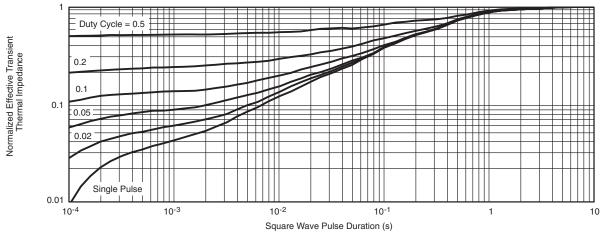
Note

a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit





Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package / tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62865.

Si2369DS

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Package Information

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SOT-23 (TO-236): 3-LEAD







Dim	MILLIN	METERS	INCHES		
Dim	Dim Min		Min	Мах	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.95	5 BSC	0.037	4 Ref	
e ₁	1.90 BSC		0.0748 Ref		
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025	5 Ref	
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	



Application Note 826

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RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

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