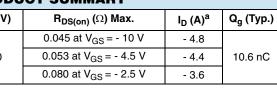
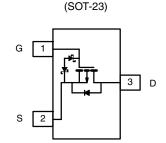




P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$ Max.	I _D (A) ^a	Q _g (Typ.)			
- 30	0.045 at V _{GS} = - 10 V	- 4.8				
	0.053 at V _{GS} = - 4.5 V	- 4.4	10.6 nC			
	0.080 at V _{GS} = - 2.5 V	- 3.6				





TO-236

Top View Si2371EDS (E6)* * Marking Code

Ordering Information:

Si2371EDS-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

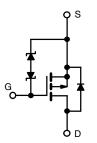
- TrenchFET® Power MOSFET
- 100 % R_g Tested
- Built-in ESD Protection
 - Typical ESD Performance 3000 V
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



HALOGEN FREE

APPLICATIONS

- Power Management for Portable and Consumer
 - Load Switches
 - OVP (Over Voltage Protection) Switch



P-Channel MOSFET

ABSOLUTE MAXIMUM RATING	S (T _A = 25 °C, unle	ess otherwise no	ted)		
Parameter		Symbol	Limit	Unit	
Drain-Source Voltage	V _{DS}	- 30	V		
Gate-Source Voltage	V _{GS}	± 12	v		
	T _C = 25 °C		- 4.8		
Continuous Drain Current (T _{.1} = 150 °C)	T _C = 70 °C] . [- 3.8		
Continuous Diain Current (1) = 150 C)	T _A = 25 °C	I _D	- 3.7 ^{b,c}		
	T _A = 70 °C		- 2.9 ^{b,c}	Α	
Pulsed Drain Current (t = 300 μs)	I _{DM}	- 20			
Continuous Source-Drain Diode Current	T _C = 25 °C	I _S	- 1.4		
Continuous Source-Diam Diode Current	T _A = 25 °C	l 's	- 1 ^{b,c}		
	T _C = 25 °C		1.7		
Maximum Power Dissipation	T _C = 70 °C	P _D	1.1	w	
Maximum Fower Dissipation	T _A = 25 °C	1 ' ^D	1 ^{b,c}	• • • • • • • • • • • • • • • • • • • •	
	T _A = 70 °C		0.6 ^{b,c}	7	
Operating Junction and Storage Temperature R	T _J , T _{stg}	- 55 to 150	°C		
Soldering Recommendations (Peak Temperatur		260			

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 5 s	R_{thJA}	100	130	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	60	75	- C/VV		

Notes:

- a. $T_C = 25$ °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 175 °C/W.



Parameter	Symbol	nerwise noted) Test Conditions	Min.	Тур.	Max.	Unit		
Static	Cymbol	rest conditions		196.	mux.	Oint		
Drain-Source Breakdown Voltage	V _{DS}	V _{GS} = 0 V, I _D = - 250 μA	- 30			V		
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	-		- 24				
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = - 250 μA		2.2		mV/°C		
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = - 250 μA	- 0.6		- 1.5	V		
· · · · · · · · · ·		$V_{DS} = 0 \text{ V}, V_{GS} = \pm 12 \text{ V}$			± 10			
ate-Source Leakage	IGSS	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 4.5 \text{ V}$			± 1	μΑ		
7 0		V _{DS} = - 30 V, V _{GS} = 0 V			- 1			
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -30 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 55 ^{\circ}\text{C}$			- 10			
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 15			Α		
	, ,	V _{GS} = - 10 V, I _D = - 3.7 A		0.037	0.045	Ω		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 2 A		0.044	0.053			
		V _{GS} = - 2.5 V, I _D = - 2 A		0.066	0.080			
Dynamic ^b								
-		V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 3.7 A		22.8	35	nC		
Total Gate Charge	Q_g			10.6	16			
Gate-Source Charge	Q _{gs}	$V_{DS} = -15 \text{ V}, V_{GS} = -4.5 \text{ V}, I_{D} = -3.7 \text{ A}$		1.7				
Gate-Drain Charge	Q_{gd}			2.6				
Gate Resistance	R_g	f = 1 MHz	2.2	11	22	Ω		
Turn-On Delay Time	t _{d(on)}			28	42			
Rise Time	t _r	V_{DD} = - 15 V, R_{L} = 5.2 Ω		65	98			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 2.9 A, V_{GEN} = - 4.5 V, R_g = 1 Ω		47	71			
Fall Time	t _f			62	93	ns		
Turn-On Delay Time	t _{d(on)}			7	14	115		
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 5.2 \Omega$		8	16			
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ - 2.9 A, V_{GEN} = - 10 V, R_g = 1 Ω		52	78			
Fall Time	t _f			52	78			
Drain-Source Body Diode Characteristics								
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 1.4	Α		
Pulse Diode Forward Current	I _{SM}				- 20	, , , , , , , , , , , , , , , , , , ,		
Body Diode Voltage	V _{SD}	$I_S = -2.9 \text{ A}, V_{GS} = 0 \text{ V}$		- 0.8	- 1.2	V		
Body Diode Reverse Recovery Time	t _{rr}			13	20	ns		
Body Diode Reverse Recovery Charge	Q _{rr}	I _F = - 2.9 A, dl/dt = 100 A/μs, T _J = 25 °C		6	12	nC		
Reverse Recovery Fall Time	t _a			9		ns		
Reverse Recovery Rise Time	t _b			4				

Notes:

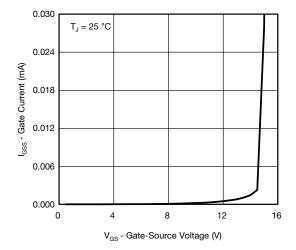
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

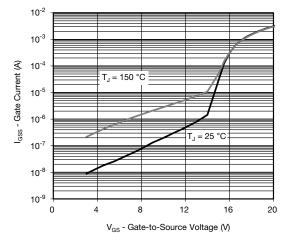
b. Guaranteed by design, not subject to production testing.



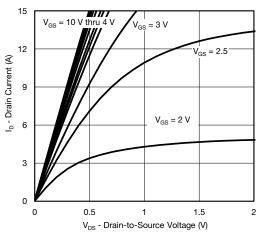
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



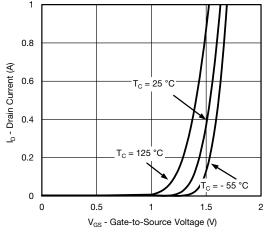
Gate Current vs. Gate-Source Voltage



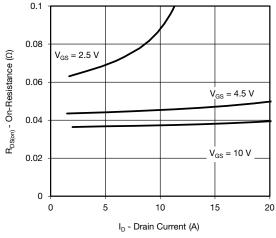
Gate Current vs. Gate-Source Voltage



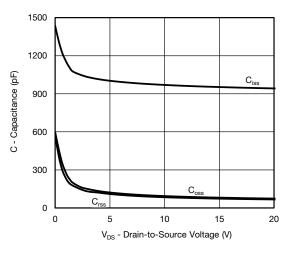
Output Characteristics



Transfer Characteristics

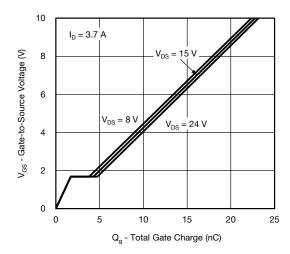


On-Resistance vs. Drain Current

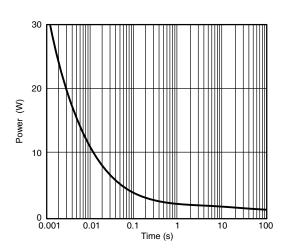


Capacitance

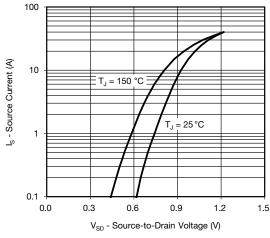
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



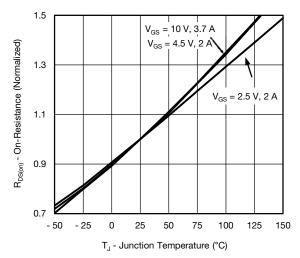
Gate Charge



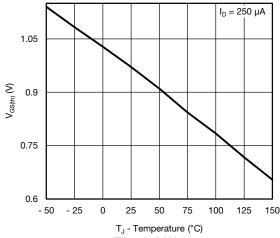
Single Pulse Power, Junction-to-Ambient



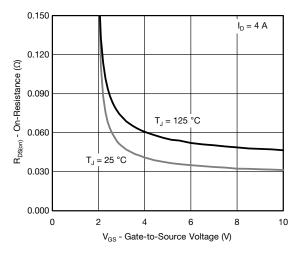
Soure-Drain Diode Forward Voltage



On-Resistance vs. Junction Temperature



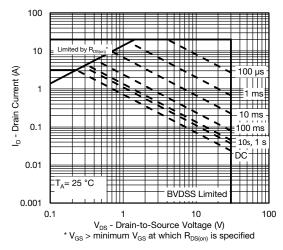
Threshold Voltage



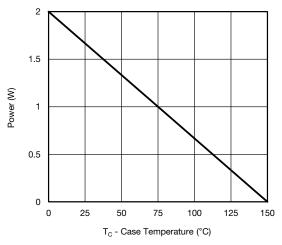
On-Resistance vs. Gate-to-Source Voltage



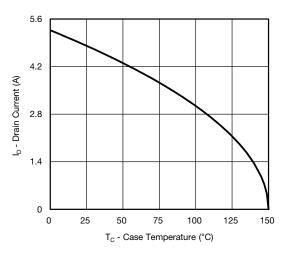
TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



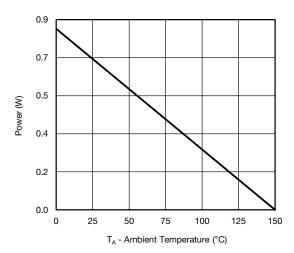
Safe Operating Area, Junction-to-Ambient



Power Junction-to-Case



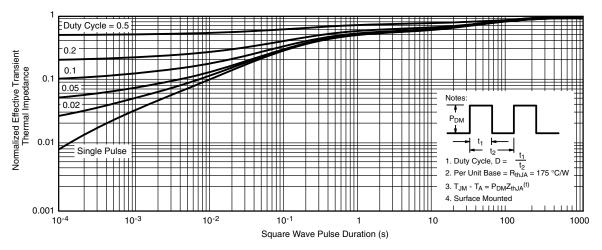
Current Derating*



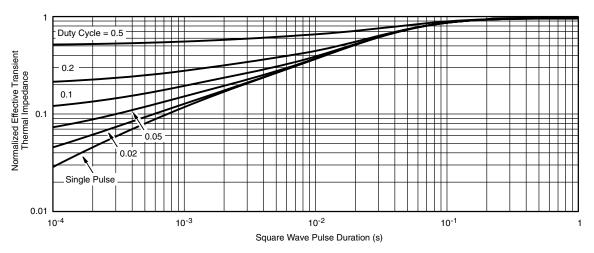
Power Junction-to-Ambient

 $^{^{\}star}$ The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

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SOT-23 (TO-236): 3-LEAD







Dim	MILLIMETERS		INCHES		
	Min	Max	Min	Max	
Α	0.89	1.12	0.035	0.044	
A ₁	0.01	0.10	0.0004	0.004	
A ₂	0.88	1.02	0.0346	0.040	
b	0.35	0.50	0.014	0.020	
С	0.085	0.18	0.003	0.007	
D	2.80	3.04	0.110	0.120	
E	2.10	2.64	0.083	0.104	
E ₁	1.20	1.40	0.047	0.055	
е	0.9	5 BSC	0.037	4 Ref	
e ₁	1.9	0 BSC	0.074	8 Ref	
L	0.40	0.60	0.016	0.024	
L ₁	0.64 Ref		0.025	0.025 Ref	
S	0.50 Ref		0.020 Ref		
q	3°	8°	3°	8°	
FCN: S-03946-Rev K 09-	lul-01	•			

ECN: S-03946-Rev. K, 09-Jul-01

DWG: 5479

Document Number: 71196 www.vishay.com 09-Jul-01



RECOMMENDED MINIMUM PADS FOR SOT-23



Recommended Minimum Pads Dimensions in Inches/(mm)

Return to Index

APPLICATION NOTE



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