



P-Channel 12-V (D-S) MOSFET

PRODUCT SUMMARY				
V _{DS} (V)	$R_{DS(on)}\left(\Omega\right)$	I _D (A)		
	0.040 at V _{GS} = - 4.5 V	- 6.0		
- 12	0.053 at V _{GS} = - 2.5 V	- 5.2		
	0.072 at V _{GS} = - 1.8 V	- 4.5		

FEATURES

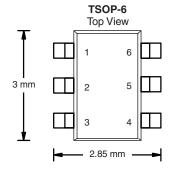
- Halogen-free According to IEC 61249-2-21 Definition
- TrenchFET[®] Power MOSFET: 1.8 V Rated
- Ultra Low On-Resistance
- Compliant to RoHS Directive 2002/95/EC



ROHS COMPLIANT HALOGEN FREE

APPLICATIONS

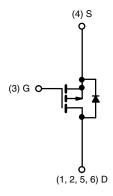
- Load Switch
- PA Switch



Ordering Information: Si3447BDV-T1-E3 (Lead (Pb)-free)

Si3447BDV-T1-GE3 (Lead (Pb)-free and Halogen-free)

Marking Code: B7xxx



P-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS T _A = 25 °C, unless otherwise noted						
Parameter		Symbol	5 s	Steady State	Unit	
Drain-Source Voltage		V _{DS}	- 12		V	
Gate-Source Voltage		V _{GS}	± 8			
Continuous Brain Comment /T 450 00\8	T _A = 25 °C	I _D	- 6.0	- 4.5		
Continuous Drain Current (T _J = 150 °C) ^a	T _A = 85 °C		- 4.3	- 3.3		
Pulsed Drain Current		I _{DM}	- 20		A	
Continuous Source Current (Diode Conduction) ^a		I _S	- 1.7	- 0.9		
	T _A = 25 °C	P _D	2.0	1.1	W	
Maximum Power Dissipation ^a	T _A = 85 °C		1.0	0.6		
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150		°C	

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Manipular landing to Austriant	t ≤ 5 s	R _{thJA}	50	62.5	°C/W	
Maximum Junction-to-Ambient ^a	Steady State		90	110		
Maximum Junction-to-Foot (Drain)	Steady State	R_{thJF}	30	36		

Notes

a. Surface Mounted on 1" x 1" FR4 board.

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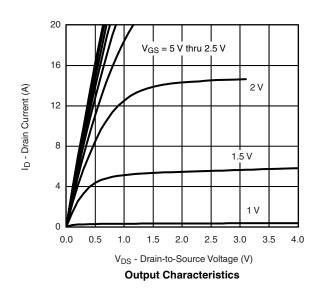
Parameter	Symbol	Test Conditions Min		Тур.	Max.	Unit	
Static							
Gate Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 0.45		1	V	
Gate-Body Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 8 \text{ V}$			± 100	nA	
7 0 1 1/1 5 1 0 1	_	V _{DS} = - 12 V, V _{GS} = 0 V	- 1		- 1		
Zero Gate Voltage Drain Current	I _{DSS}	$V_{DS} = -12 \text{ V}, V_{GS} = 0 \text{ V}, T_{J} = 85 ^{\circ}\text{C}$			- 5	μΑ	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} = -5 V$, $V_{GS} = -4.5 V$	- 20			Α	
		V _{GS} = - 4.5 V, I _D = - 6.0 A		0.033	0.040		
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 2.5 V, I _D = - 5.2 A		0.044	0.053	3 Ω	
		$V_{GS} = -1.8 \text{ V}, I_D = -2.0 \text{ A}$		0.060	0.072		
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 5 V, I _D = - 6.0 A		15		S	
Diode Forward Voltage ^a	V_{SD}	I _S = - 1.7 A, V _{GS} = 0 V		- 0.7	- 1.2	V	
Dynamic ^b							
Total Gate Charge	Q_g			9.3	14		
Gate-Source Charge	Q_{gs}	V _{DS} = -6 V, V _{GS} = -4.5 V, I _D = -6.0 A		1.5		nC	
Gate-Drain Charge	Q_{gd}			2.6			
Turn-On Delay Time	t _{d(on)}			20	30		
Rise Time	t _r	V_{DD} = - 6 V, R_L = 6 Ω		46	70		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ - 1 A, V_GEN = - 4.5 V, R_g = 6 Ω		62	95	ns	
Fall Time	t _f			62	95		
Source-Drain Reverse Recovery Time	t _{rr}	I _F = - 1.7 A, dI/dt = 100 A/μs		40	80		

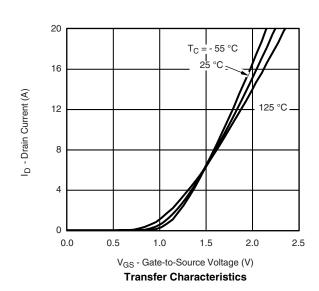
Notes:

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %.
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



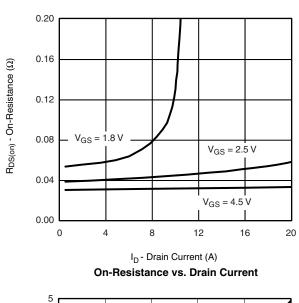


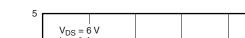


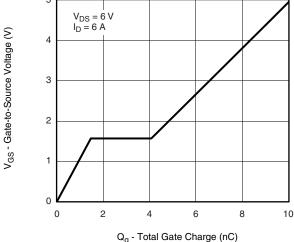




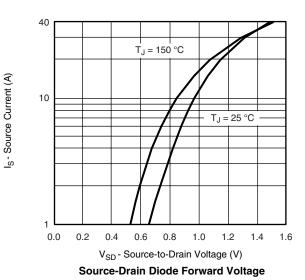
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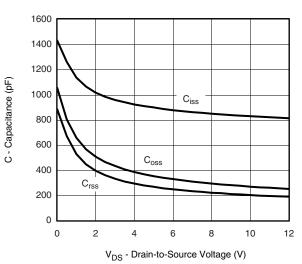


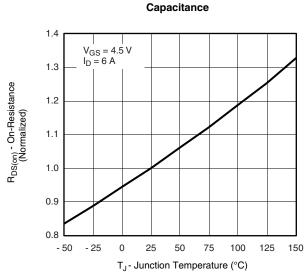




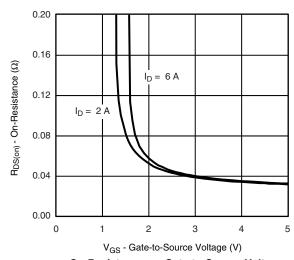
Gate Charge







On-Resistance vs. Junction Temperature

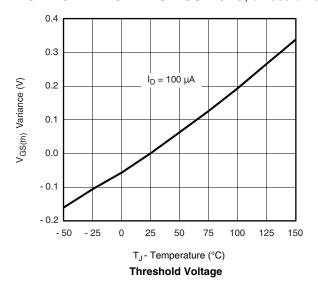


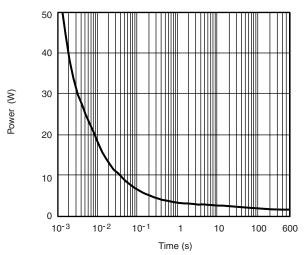
On-Resistance vs. Gate-to-Source Voltage

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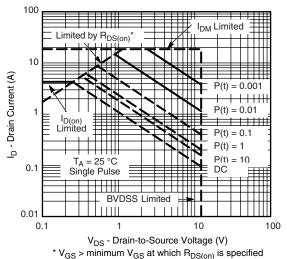
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

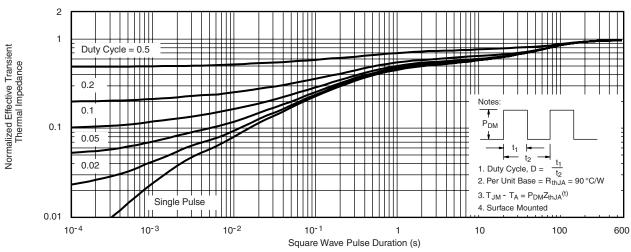




Single Pulse Power



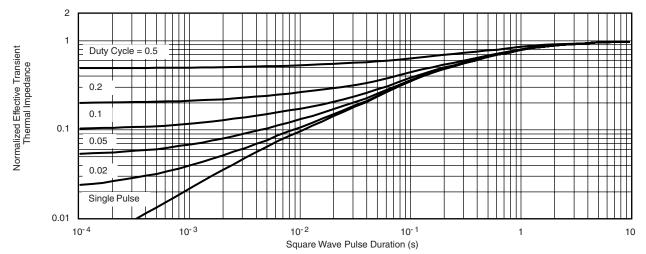
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

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