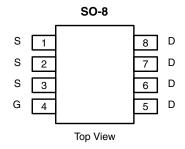




Vishay Siliconix

# N-Channel 100 V (D-S) MOSFET

PRODUCT SUMMARY						
V <sub>DS</sub> (V)	$R_{DS(on)}(\Omega)$ Max.	I <sub>D</sub> (A) <sup>a</sup>	Q <sub>g</sub> (Typ.)			
	0.0088 at V <sub>GS</sub> = 10 V	18.4				
100	0.0094 at V <sub>GS</sub> = 7.5 V	17.8	20.7 nC			
	0.0120 at V <sub>GS</sub> = 4.5 V	15.8				



### **Ordering Information:**

Si4190ADY-T1-GE3 (Lead (Pb)-free and Halogen-free)

### **FEATURES**

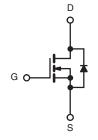
- TrenchFET® Power MOSFET
- 100 %  $R_g$  and UIS Tested
- Material categorization: For definitions of compliance please see www.vishav.com/doc?99912



HALOGEN FREE

### **APPLICATIONS**

- DC/DC Primary Side Switch
- Telecom/Server
- Industrial



N-Channel MOSFET

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V <sub>DS</sub>	100	V
Gate-Source Voltage		V <sub>GS</sub>	± 20	
	T <sub>C</sub> = 25 °C		18.4	
Continuous Proin Correct /T 450 °C)	T <sub>C</sub> = 70 °C		14.6	
Continuous Drain Current (T <sub>J</sub> = 150 °C)	T <sub>A</sub> = 25 °C	- I <sub>D</sub>	13 <sup>b, c</sup>	
	T <sub>A</sub> = 70 °C		10.3 <sup>b, c</sup>	
Pulsed Drain Current (t = 300 μs)		I <sub>DM</sub>	70	Α
0 " 0 0 0 0	T <sub>C</sub> = 25 °C		5.4	
Continuous Source-Drain Diode Current	T <sub>A</sub> = 25 °C	- I <sub>S</sub>	2.7 <sup>b, c</sup>	
Single Pulse Avalanche Current	1 01 mll	I <sub>AS</sub>	30	
Avalanche Energy L = 0.1 mH		E <sub>AS</sub>	45	mJ
	T <sub>C</sub> = 25 °C		6	
Maximum Dawar Dissination	T <sub>C</sub> = 70 °C		3.8	w
Maximum Power Dissipation	T <sub>A</sub> = 25 °C	P <sub>D</sub>	3 <sup>b, c</sup>	
	T <sub>A</sub> = 70 °C		1.9 <sup>b, c</sup>	
Operating Junction and Storage Temperature Range		T <sub>J</sub> , T <sub>stg</sub>	- 55 to 150	°C

THERMAL RESISTANCE RATINGS						
Parameter		Symbol	Typical	Maximum	Unit	
Maximum Junction-to-Ambient <sup>b, d</sup>	t ≤ 10 s	R <sub>thJA</sub>	33	42	°C/W	
Maximum Junction-to-Foot (Drain)	Steady State	R <sub>thJF</sub>	16	21	O/ VV	

### Notes:

- a. Based on  $T_C = 25$  °C.
- b. Surface mounted on 1" x 1" FR4 board.
- d. Maximum under steady state conditions is 85 °C/W.

Document Number: 63826 S12-0541-Rev. A, 12-Mar-12 For technical support, please contact: pmostechsupport@vishav.com

# Si4190ADY

## Vishay Siliconix



Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static							
Drain-Source Breakdown Voltage	$V_{DS}$	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	100			V	
V <sub>DS</sub> Temperature Coefficient	$\Delta V_{DS}/T_{J}$			64		mV/°C	
V <sub>GS(th)</sub> Temperature Coefficient	$\Delta V_{GS(th)}/T_{J}$	- I <sub>D</sub> = 250 μA		- 5.8			
Gate-Source Threshold Voltage	V <sub>GS(th)</sub>	$V_{DS} = V_{GS}$ , $I_{D} = 250 \mu A$	1.5		2.8	V	
Gate-Source Leakage	I <sub>GSS</sub>	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA	
		V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V			1	μΑ	
Zero Gate Voltage Drain Current	I <sub>DSS</sub>	V <sub>DS</sub> = 100 V, V <sub>GS</sub> = 0 V, T <sub>J</sub> = 55 °C			10		
On-State Drain Current <sup>a</sup>	I <sub>D(on)</sub>	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	30			Α	
	, ,	V <sub>GS</sub> = 10 V, I <sub>D</sub> = 15 A		0.0073	0.0088		
Drain-Source On-State Resistance <sup>a</sup>	R <sub>DS(on)</sub>	V <sub>GS</sub> = 7.5 V, I <sub>D</sub> = 12 A		0.0078	0.0094	Ω	
	- (- /	V <sub>GS</sub> = 4.5 V, I <sub>D</sub> = 10 A		0.0096	0.0120		
Forward Transconductance <sup>a</sup>	9 <sub>fs</sub>	V <sub>DS</sub> = 15 V, I <sub>D</sub> = 15 A		54		S	
Dynamic <sup>b</sup>				L			
Input Capacitance	C <sub>iss</sub>			1970			
Output Capacitance	C <sub>oss</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 0 \text{ V}, f = 1 \text{ MHz}$		695		pF	
Reverse Transfer Capacitance	C <sub>rss</sub>			62			
·	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$	$V_{DS} = 50 \text{ V}, V_{GS} = 10 \text{ V}, I_{D} = 10 \text{ A}$		44.4	67	nC	
Total Gate Charge				20.7	31		
Gate-Source Charge	Q <sub>gs</sub>	$V_{DS} = 50 \text{ V}, V_{GS} = 4.5 \text{ V}, I_{D} = 10 \text{ A}$		6.1			
Gate-Drain Charge	Q <sub>gd</sub>			9.1			
Output Charge	Q <sub>oss</sub>	V <sub>DS</sub> = 50 V, V <sub>GS</sub> = 0 V		56	85		
Gate Resistance	R <sub>g</sub>	f = 1 MHz	0.4	1.1	2.2	Ω	
Turn-On Delay Time	t <sub>d(on)</sub>			15	30		
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_L = 5 \Omega$		11	22		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{GEN} = 7.5 \text{ V}, R_g = 1 \Omega$		31	60		
Fall Time	t <sub>f</sub>			10	20		
Turn-On Delay Time	t <sub>d(on)</sub>			12	24	ns	
Rise Time	t <sub>r</sub>	$V_{DD} = 50 \text{ V}, R_L = 5 \Omega$		10	20		
Turn-Off Delay Time	t <sub>d(off)</sub>	$I_D \cong 10 \text{ A}, V_{GEN} = 10 \text{ V}, R_g = 1 \Omega$		34	65		
Fall Time	t <sub>f</sub>			10	20		
Drain-Source Body Diode Characteristi	cs						
Continuous Source-Drain Diode Current	I <sub>S</sub>	T <sub>C</sub> = 25 °C			5.4		
Pulse Diode Forward Current <sup>a</sup>	I <sub>SM</sub>				70	Α	
Body Diode Voltage	V <sub>SD</sub>	I <sub>S</sub> = 5 A		0.76	1.1	V	
Body Diode Reverse Recovery Time	t <sub>rr</sub>			42	80	ns	
Body Diode Reverse Recovery Charge	Q <sub>rr</sub>	1 10 A di/dt 100 A/v- T 05 00		40	80	nC	
Reverse Recovery Fall Time	t <sub>a</sub>	$I_F = 10 \text{ A}, \text{ di/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		19			
Reverse Recovery Rise Time	t <sub>b</sub>			23		ns	

### Notes:

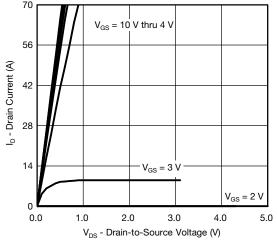
- a. Pulse test; pulse width  $\leq$  300  $\mu s,$  duty cycle  $\leq$  2 %
- b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

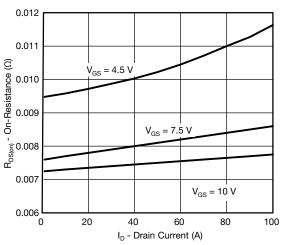


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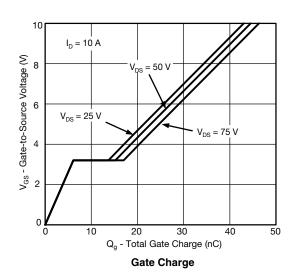
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

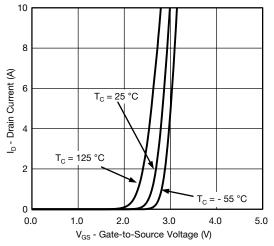


### **Output Characteristics**

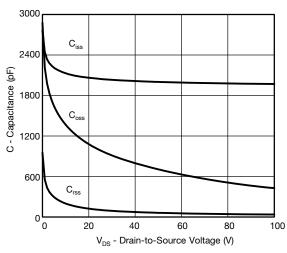


On-Resistance vs. Drain Current

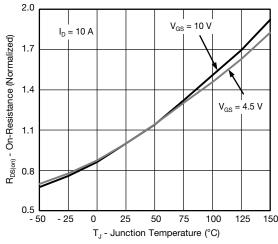




**Transfer Characteristics** 



Capacitance

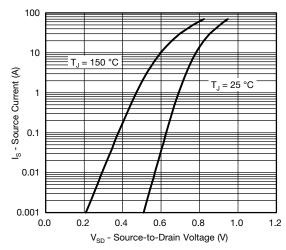


On-Resistance vs. Junction Temperature

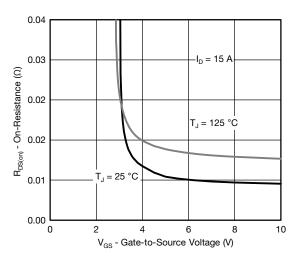
## Si4190ADY

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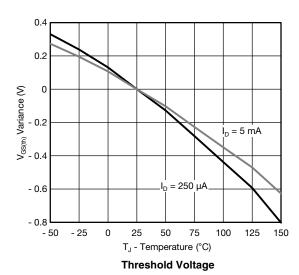
## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

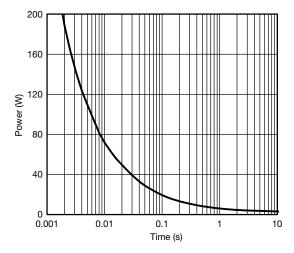


Source-Drain Diode Forward Voltage

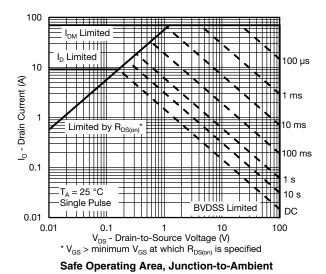


On-Resistance vs. Gate-to-Source Voltage





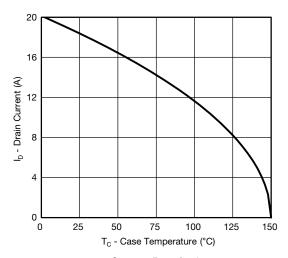
Single Pulse Power, Junction-to-Ambient



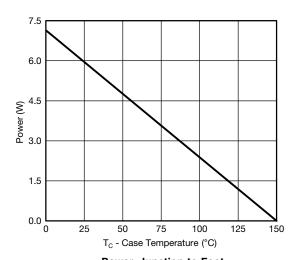


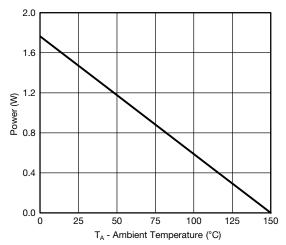
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## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



### **Current Derating\***





Power, Junction-to-Foot

Power, Junction-to-Ambient

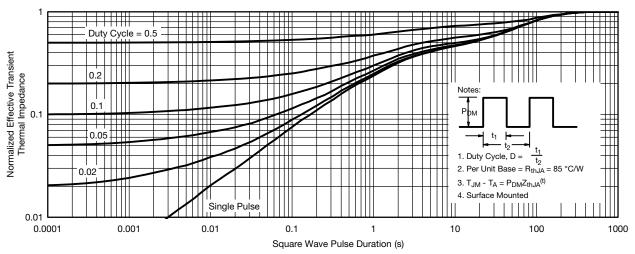
<sup>\*</sup> The power dissipation  $P_D$  is based on  $T_{J(max)} = 150$  °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

## Si4190ADY

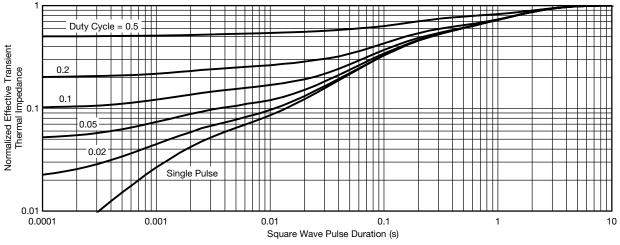
## Vishay Siliconix



## TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?63826



SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012







	MILLIM	IETERS	INCHES			
DIM	Min	Max	Min	Max		
Α	1.35	1.75	0.053	0.069		
A <sub>1</sub>	0.10	0.20	0.004	0.008		
В	0.35	0.51	0.014	0.020		
С	0.19	0.25	0.0075	0.010		
D	4.80	5.00	0.189	0.196		
Е	3.80	4.00	0.150	0.157		
е	1.27 BSC		0.050	0.050 BSC		
Н	5.80	6.20	0.228	0.244		
h	0.25	0.50	0.010	0.020		
L	0.50	0.93	0.020	0.037		
q	0°	8°	0°	8°		
S	0.44	0.64	0.018	0.026		
ECN: C-06527-Rev. I. 11-Sep-06						

DWG: 5498

Document Number: 71192 www.vishay.com 11-Sep-06



## **RECOMMENDED MINIMUM PADS FOR SO-8**



Recommended Minimum Pads Dimensions in Inches/(mm)

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IPS70R2K0CEAKMA1 BUK954R8-60E DMN3404LQ-7 NTE6400 SQJ402EP-T1-GE3 2SK2614(TE16L1,Q) 2N7002KW-FAI

DMN1017UCP3-7 EFC2J004NUZTDG ECH8691-TL-W FCAB21350L1 P85W28HP2F-7071 DMN1053UCP4-7 NTE221 NTE2384

NTE2903 NTE2941 NTE2945 NTE2946 NTE2960 NTE2967 NTE2969 NTE2976 NTE455 NTE6400A NTE2910 NTE2916 NTE2956

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