New Product



Si4228DY

RoHS

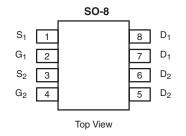
COMPLIANT HALOGEN

FREE

Vishay Siliconix

Dual N-Channel 25 V (D-S) MOSFET

PRODUCT SUMMARY						
V _{DS} (V)	R_{DS(on)} (Ω)	I _D (A) ^{a, e}	Q _g (Typ.)			
	0.018 at V _{GS} = 10 V	8				
25	0.020 at V _{GS} = 4.5 V	8	7.8 nC			
	0.024 at V _{GS} = 2.5 V	7.5				



FEATURES

- Halogen-free According to IEC 61249-2-21
 Definition
- TrenchFET[®] Power MOSFET
- 100 % R_g and UIS Tested
- Compliant to RoHS Directive 2002/95/EC

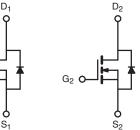
APPLICATIONS

•

- Synchronous Buck Converter
 - DC/DC Converter

N-Channel MOSFET

G



N-Channel MOSFET

Ordering Information: Si4228DY-T1-GE3 (Lead (Pb)-free and Halogen-free)

Parameter	Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	25	v
Gate-Source Voltage		V _{GS}	± 12	v
	T _C = 25 °C		8 ^e	
	T _C = 70 °C		8 ^e	
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	8 ^{b, c, e}	
	T _A = 70 °C		6.9 ^{b, c}	•
Pulsed Drain Current		I _{DM}	50	A
Continuous Source-Drain Diode Current	T _C = 25 °C	- I _S	2.6	
	T _A = 25 °C		1.7 ^{b, c}	
Single Pulse Avalanche Current		I _{AS}	15	
Avalanche Energy	L = 0.1 mH	E _{AS}	11.25	mJ
	T _C = 25 °C		3.1	
Maximum Dawar Dissinction	T _C = 70 °C		2	w
Maximum Power Dissipation	T _A = 25 °C	P _D	2 ^{b, c}	VV
	T _A = 70 °C		1.3 ^{b, c}	
Operating Junction and Storage Temperature Range		T _J , T _{stg}	- 55 to 150	°C

THERMAL RESISTANCE RATINGS							
Parameter		Symbol	Typical	Maximum	Unit		
Maximum Junction-to-Ambient ^{b, d}	t ≤ 10 s	R _{thJA}	52	62.5	°C/W		
Maximum Junction-to-Foot (Drain)	Steady State	R _{thJF}	30	40	0/11		

Notes:

a. Based on $T_C = 25 \ ^{\circ}C$.

b. Surface mounted on 1" x 1" FR4 board.

c. t = 10 s.

d. Maximum under steady state conditions is 110 $^{\circ}\text{C/W}.$

e. Package limited.

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit	
Static				•	•		
Drain-Source Breakdown Voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = 250 \mu\text{A}$	25			V	
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	– Ι _D = 250 μΑ		20		mV/°C	
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	I _D = 250 μA		- 3.2		- mv/-c	
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}$, $I_D = 250 \ \mu A$	0.6		1.4	V	
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 12 V$			± 100	nA	
Zara Cata Valtaga Drain Current		$V_{DS} = 25 \text{ V}, V_{GS} = 0 \text{ V}$			1		
Zero Gate Voltage Drain Current	IDSS	V_{DS} = 25 V, V_{GS} = 0 V, T_{J} = 55 °C			10	μA	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \ge 5 \text{ V}, V_{GS} = 10 \text{ V}$	20			А	
		$V_{GS} = 10 \text{ V}, \text{ I}_{D} = 7 \text{ A}$		0.015	15 0.018		
Drain-Source On-State Resistance ^a	R _{DS(on)}	$V_{GS} = 4.5 \text{ V}, \text{ I}_{D} = 7 \text{ A}$		0.016	0.020		
		$V_{GS} = 2.5 \text{ V}, \text{ I}_{D} = 5 \text{ A}$		0.020	0.024		
Forward Transconductance ^a	9 _{fs}	V _{DS} = 15 V, I _D = 7 A		68		S	
Dynamic ^b							
Input Capacitance	C _{iss}			790		pF	
Output Capacitance	C _{oss}	V_{DS} = 12.5 V, V_{GS} = 0 V, f = 1 MHz		146			
Reverse Transfer Capacitance	C _{rss}			76			
Total Gate Charge		V_{DS} = 12.5 V, V_{GS} = 10 V, I_{D} = 8.6 A		16.5	25	nC	
	Qg			7.8	12		
Gate-Source Charge	Q _{gs}	V_{DS} = 12.5 V, V_{GS} = 4.5 V, I_{D} = 8.6 A		1.6			
Gate-Drain Charge	Q _{gd}			1.7			
Gate Resistance	R _g	f = 1 MHz	0.5	2.5	5	Ω	
Turn-On Delay Time	t _{d(on)}			7	14		
Rise Time	t _r	V_{DD} = 12.5 V, R_{L} = 1.8 Ω		12	18	- ns	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong$ 6.9 A, V_{GEN} = 4.5 V, R_g = 1 Ω		21	30		
Fall Time	t _f			10	20		
Turn-On Delay Time	t _{d(on)}			4	8		
Rise Time	t _r	V_{DD} = 12.5 V, R_L = 1.8 Ω		9	18		
Turn-Off Delay Time	t _{d(off)}	$\text{I}_\text{D}\cong$ 6.9 A, V_GEN = 10 V, R_g = 1 Ω		20	30		
Fall Time	t _f			7	14		
Drain-Source Body Diode Characterist	ics						
Continuous Source-Drain Diode Current	۱ _S	T _C = 25 °C			2.6	^	
Pulse Diode Forward Currenta	I _{SM}			T	50	A	
Body Diode Voltage	V _{SD}	I _S = 6.9 A		0.82	1.2	V	
Body Diode Reverse Recovery Time	t _{rr}			15	23	ns	
Body Diode Reverse Recovery Charge	Q _{rr}			6	12	nC	
Reverse Recovery Fall Time	t _a	$I_F = 6.9 \text{ A}, \text{ dl/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 \text{ °C}$		8			
Reverse Recovery Rise Time	t _b			7		ns	

Notes

a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

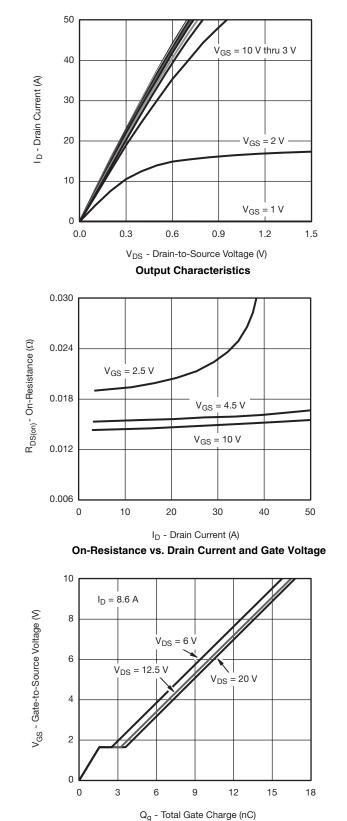
b. Guaranteed by design, not subject to production testing.

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

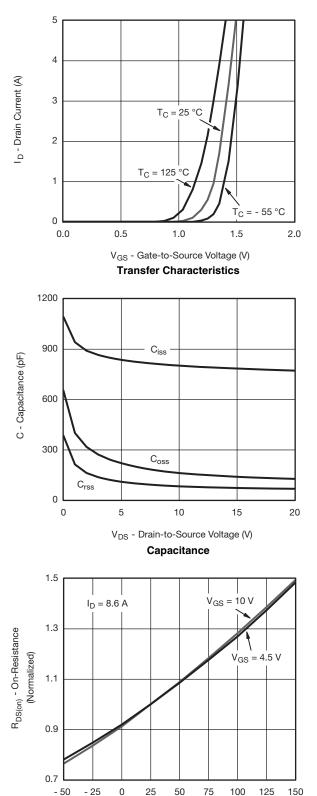


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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Gate Charge



T_J - Junction Temperature (°C) **On-Resistance vs. Junction Temperature**

75

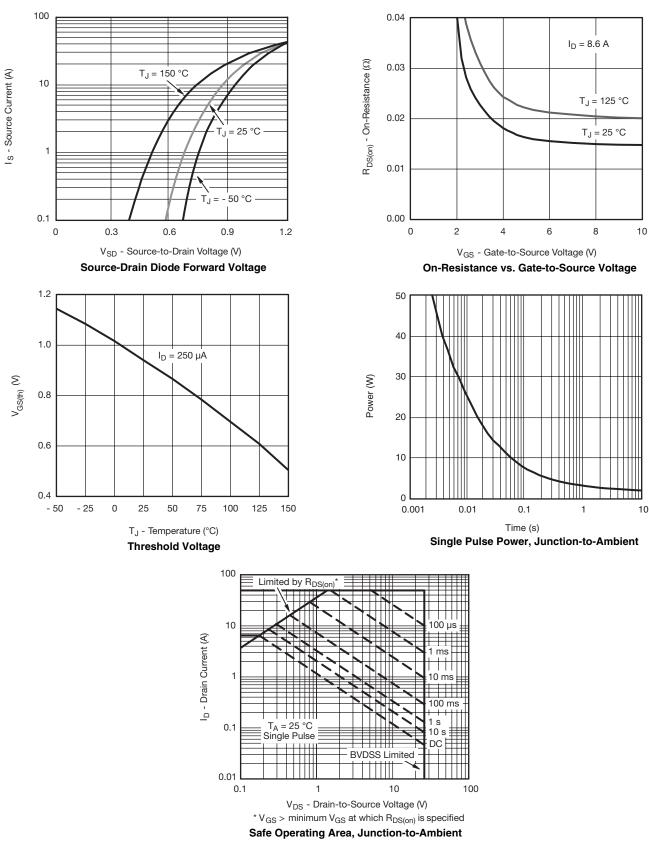
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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

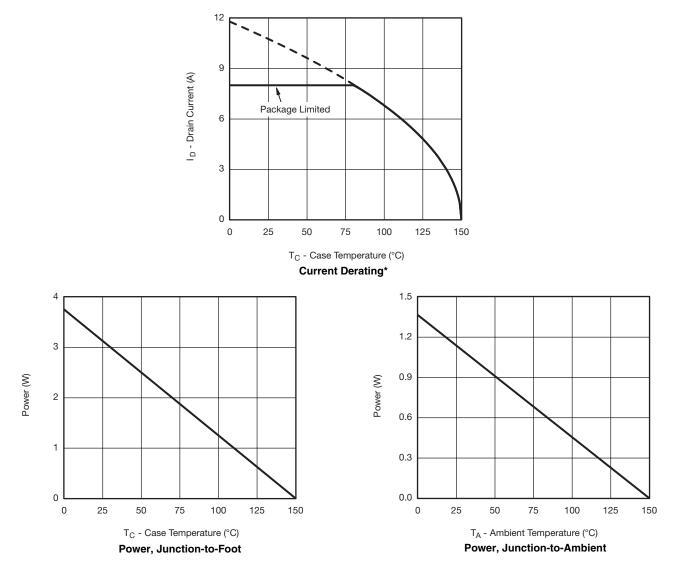


New Product



Si4228DY Vishay Siliconix

TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted

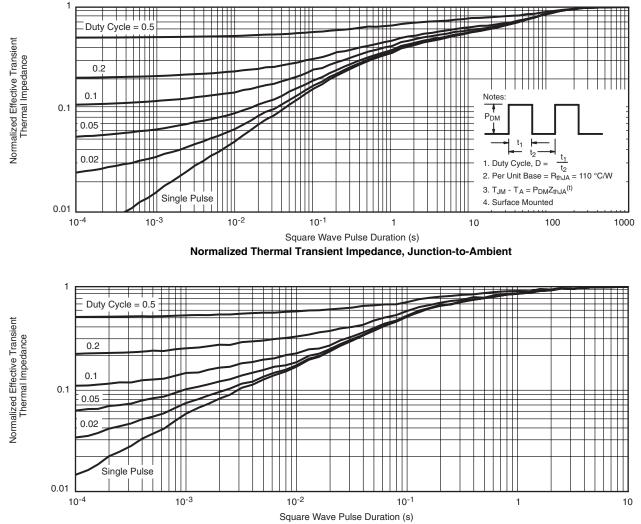


* The power dissipation P_D is based on $T_{J(max)} = 150$ °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS 25 °C, unless otherwise noted



Normalized Thermal Transient Impedance, Junction-to-Foot

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?66591.



Package Information

Vishay Siliconix

SOIC (NARROW): 8-LEAD JEDEC Part Number: MS-012





	MILLIM	IETERS	INC	HES	
DIM	Min	Мах	Min	Max	
A	1.35	1.75	0.053	0.069	
A ₁	0.10	0.20	0.004	0.008	
В	0.35	0.51	0.014	0.020	
С	0.19	0.25	0.0075	0.010	
D	4.80	5.00	0.189	0.196	
E	3.80	4.00	0.150	0.157	
е	1.27	BSC	0.050 BSC		
н	5.80	6.20	0.228	0.244	
h	0.25	0.50	0.010	0.020	
L	0.50	0.93	0.020	0.037	
q	0°	8°	0°	8°	
S	0.44	0.64	0.018	0.026	
ECN: C-06527-Rev. I, 11-Sep-06 DWG: 5498					

Application Note 826

Vishay Siliconix



RECOMMENDED MINIMUM PADS FOR SO-8



Recommended Minimum Pads Dimensions in Inches/(mm)

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