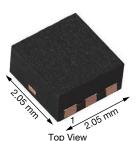
SiA4263DJ

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Vishay Siliconix

P-Channel 20 V (D-S) MOSFET

PowerPAK[®] SC-70-6L Single





Marking code: KB

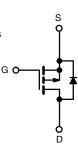
PRODUCT SUMMARY					
V _{DS} (V)	-20				
$R_{DS(on)}$ max. (Ω) at V_{GS} = -4.5 V	0.0199				
$R_{DS(on)}$ max. (Ω) at V_{GS} = -2.5 V	0.0285				
$R_{DS(on)}$ max. (Ω) at V_{GS} = -1.8 V	0.0482				
Q _g typ. (nC)	19.8				
I _D (A) ^{a, d}	-12				
Configuration	Single				

FEATURES

- TrenchFET[®] Gen III p-channel power MOSFET
- R_{DS(on)} rating at V_{GS} = -1.8 V
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

APPLICATIONS

- Battery management in mobile devices
- Battery switch
- Load switch
- PA switch



P-Channel MOSFET

ORDERING INFORMATION

Package	PowerPAK SC-70
Lead (Pb)-free and halogen-free	SiA4263DJ-T1-GE3

PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	-20	Ň	
Gate-source voltage		V _{GS}	± 8	V	
	T _C = 25 °C		-12 ^a		
	T _C = 70 °C	1. [-12 ^a		
Continuous drain current ($T_J = 150 \ ^{\circ}C$)	T _A = 25 °C		-7.5 ^{b, c}		
	T _A = 70 °C	1	-6 ^{b, c}		
Pulsed drain current (t = 100 µs)		I _{DM}	-32	— A	
Continuous source-drain diode current	T _C = 25 °C		-12 °		
	T _A = 25 °C	I _S	-2.74 ^{b, c}		
Single pulse avalanche current	1 0.1 mll	I _{AS}	-10		
Single pulse avalanche energy $L = 0.1 \text{ mH}$		E _{AS}	5	mJ	
	T _C = 25 °C		15.6		
	T _C = 70 °C		10		
Maximum power dissipation	T _A = 25 °C	P _D	3.29 ^{b, c}	W	
	T _A = 70 °C	1 –	2.10 ^{b, c}		
Operating junction and storage temperature range		T _J , T _{stg}	-55 to +150	°C	

THERMAL RESISTANCE RATING	HERMAL RESISTANCE RATINGS						
PARAMETER	SYMBOL	TYPICAL	MAXIMUM	UNIT			
Maximum junction-to-ambient ^b	t ≤ 5 s	R _{thJA}	30	38	°C/W		
Maximum junction-to-case (drain)	Steady state	R _{thJC}	6.5	8	0/10		

Notes

a. Package limited

b. Surface mounted on 1" x 1" FR4 board

c. t = 5 s

d. Maximum under steady state conditions is 80 °C/W

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COMPLIANT

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PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNIT
Static	•					
Drain-source breakdown voltage	V _{DS}	V _{GS} = 0 V, I _D = -250 μA	-20	-	-	V
V _{DS} temperature coefficient	$\Delta V_{DS}/T_{J}$	- I _D = -250 μA -	-	-12	-	mV/°C
V _{GS(th)} temperature coefficient	$\Delta V_{GS(th)}/T_J$		-	2.5	-	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D =-250 μA	-0.4	-	-1	V
Gate-source leakage	I _{GSS}	$V_{DS} = 0 V, V_{GS} = \pm 8 V$	-	-	± 100	nA
7		V _{DS} = -20 V, V _{GS} = 0 V	-	-	-1	μA
Zero gate voltage drain current	I _{DSS}	V _{DS} = -20 V, V _{GS} = 0 V, T _J = 70 °C	-	-	-10	
		V _{GS} = -4.5 V, I _D = -7.5 A	-	0.017	0.022	
Drain-source on-state resistance ^a	R _{DS(on)}	V _{GS} = -2.5 V, I _D = -6.4 A	-	0.023	0.030	Ω
	. ,	V _{GS} = -1.8 V, I _D = -2 A	-	0.035	0.0511	
Forward transconductance ^a	g _{fs}	V _{DS} = -10 V, I _D = -7.5 A	-	30	-	S
Dynamic ^b			1	•	•	1
Input capacitance	C _{iss}	V _{DS} = -10 V, V _{GS} = 0 V, f = 1 MHz	-	1825	-	pF
Output capacitance	C _{oss}		-	210	-	
Reverse transfer capacitance	C _{rss}		-	200	-	
-		V _{DS} = -10 V, V _{GS} = -8 V, I _D = -7.5 A	-	34.8	52.2	nC
Total gate charge	Qg		-	19.8	30	
Gate-source charge	Q _{gs}	V _{DS} = -10 V, V _{GS} = -4.5 V, I _D = -7.5 A	-	2.6	-	
Gate-drain charge	Q _{ad}		-	3	-	
Gate resistance	R _q	f = 1 MHz	2.12	10.6	21.2	Ω
Turn-on delay time	t _{d(on)}		-	25	38	ns
Rise time	t _r	$V_{DD} = -10 \text{ V}, \text{ R}_{L} = 1.67 \Omega, \text{ I}_{D} \cong -6 \text{ A},$	-	30	45	
Turn-off delay time	t _{d(off)}	$V_{\text{GEN}} = -4.5 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	-	95	145	
Fall time	t _f		-	40	60	
Turn-on delay time	t _{d(on)}		-	8	16	
Rise time	t _r	V_{DD} = -10 V, R_L = 1.67 Ω , $I_D \cong$ -6 A,	-	20	30	
Turn-off delay time	t _{d(off)}	$V_{\text{GEN}} = -8 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$	-	115	173	
Fall time	t _f		-	40	60	
Drain-Source Body Diode Characteristi	cs		1	1	1	
Continuous source-drain diode current	Is	T _C = 25 °C	-	-	-8	
Pulse diode forward current	I _{SM}	-	-	-	-32	A
Body diode voltage	V _{SD}	I _S = -6 A, V _{GS} = 0 V	-	-0.8	-1.2	V
Body diode reverse recovery time	t _{rr}		-	21	32	ns
Body diode reverse recovery charge	Q _{rr}	I _F = -6 A, di/dt = 100 A/µs,	-	9	18	nC
Reverse recovery fall time	ta	$T_{\rm J} = 25 \ ^{\circ}{\rm C}$	-	9	-	
Reverse recovery rise time	t _b		_	12	-	ns

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

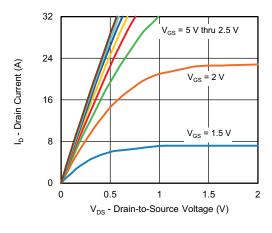
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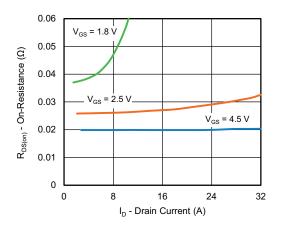
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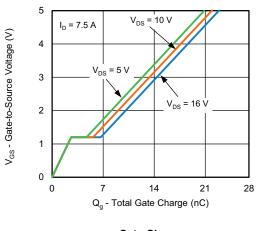
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



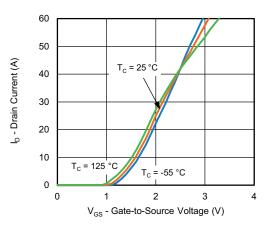
Output Characteristics



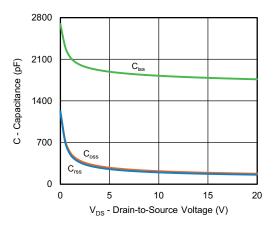
On-Resistance vs. Drain Current and Gate Voltage



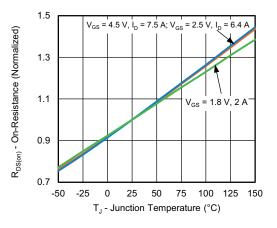
Gate Charge



Transfer Characteristics



Capacitance



On-Resistance vs. Junction Temperature

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3

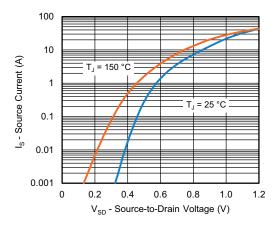
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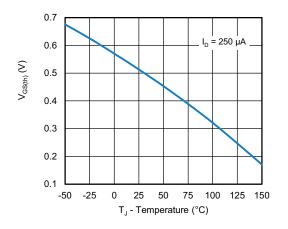


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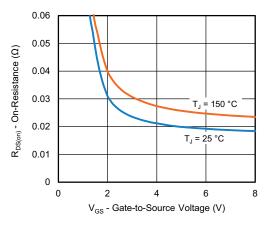
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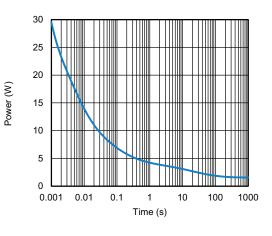
Source-Drain Diode Forward Voltage



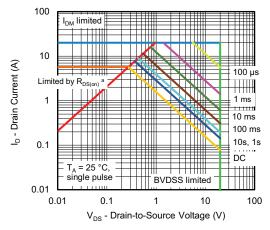
Threshold Voltage



On-Resistance vs. Gate-to-Source Voltage



Single Pulse Power, Junction-to-Ambient



Safe Operating Area, Junction-to-Ambient

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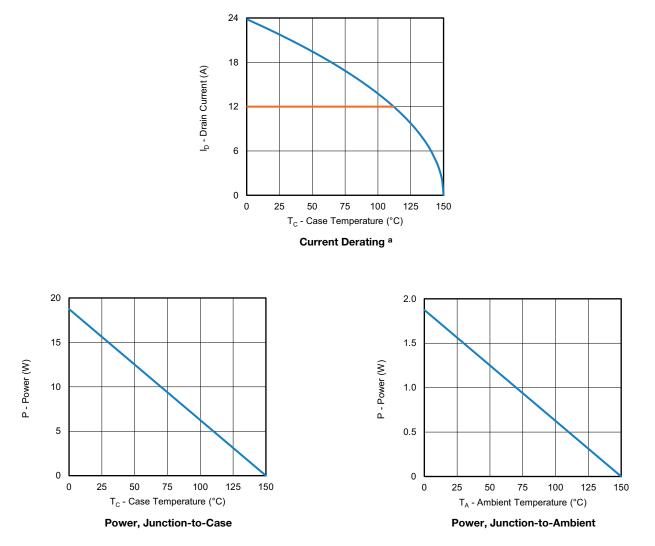
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Note

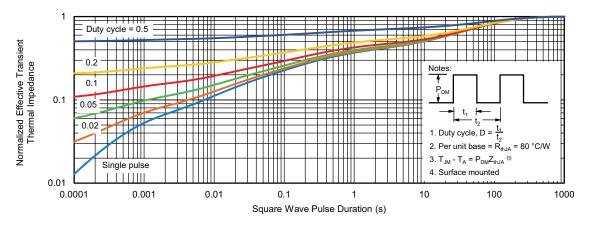
a. The power dissipation P_D is based on T_J max. = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit



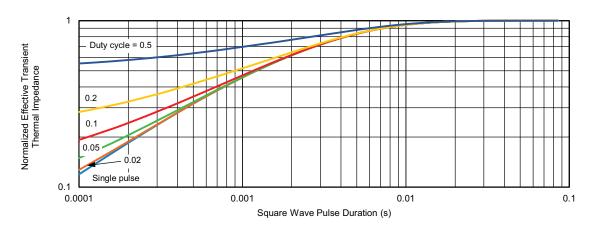
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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

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