

Vishay Siliconix

P-Channel 30 V (D-S) MOSFET

PRODUCT SUMMARY									
V _{DS} (V)	$R_{DS(on)}(\Omega)$ (Max.)	I _D (A)	Q _g (Typ.)						
- 30	0.021at V _{GS} = - 10 V	- 12 ^a	21 nC						
	0.030 at $V_{GS} = -4.5 \text{ V}$	- 12 ^a	21110						

PowerPAK SC-70-6L-Single D D D 3 G G C S S 2.05 mm Ordering Information: SiA483DJ-T1-GE3 (Lead (Pb)-free and Halogen-free)

FEATURES

- TrenchFET[®] Power MOSFET
- Thermally Enhanced PowerPAK® SC-70 Package
 - Small Footprint Area
 - Low On-Resistance
 - 100 % R_g Tested
- Material categorization:

For definitions of compliance please see www.vishay.com/doc?99912

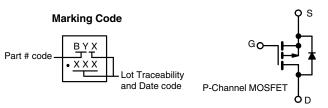


ROHS COMPLIANT HALOGEN

FREE

APPLICATIONS

- · Smart Phones, Tablet PCs, Mobile Computing:
 - Battery Switches
 - Load Switches
 - Power Management
 - DC/DC Converters



Parameter		Symbol	Limit	Unit	
Drain-Source Voltage		V _{DS}	- 30	V	
Gate-Source Voltage		V _{GS}	± 20	v	
	T _C = 25 °C		- 12 ^a		
Continuous Drain Current (T = 150 °C)	T _C = 70 °C		- 12 ^a		
Continuous Drain Current (T _J = 150 °C)	T _A = 25 °C	I _D	- 10 ^{b, c}		
	T _A = 70 °C		- 8 ^{b, c}	A	
Pulsed Drain Current (t = 300 μs)	·	I _{DM} - 40			
Continuous Source-Drain Diode Current	T _C = 25 °C	1	- 12 ^a		
Continuous Source-Diam Diode Current	T _A = 25 °C	I _S	- 2.9 ^{b, c}		
	T _C = 25 °C		19		
Maximum Power Dissipation	T _C = 70 °C	ь	12	w	
Maximum Fower Dissipation	T _A = 25 °C	P _D	3.5 ^{b, c}		
	T _A = 70 °C		2.2 ^{b, c}		
Operating Junction and Storage Temperature Ra	ange	T _J , T _{stg}	- 55 to 150	°C	
Soldering Recommendations (Peak Temperature	e) ^{d, e}		260		

THERMAL RESISTANCE RATINGS										
Parameter		Symbol	Typical	Maximum	Unit					
Maximum Junction-to-Ambient ^{b, f}	t ≤ 5 s	R _{thJA}	28	36	°C/W					
Maximum Junction-to-Case (Drain)	Steady State	R _{th.IC}	5.3	6.5	C/VV					

Notes:

- a. Package limited
- b. Surface mounted on 1" x 1" FR4 board.
- c. t = 5 s.
- d. See solder profile (www.vishay.com/doc?73257). The PowerPAK SC-70 is a leadless package. The end of the lead terminal is exposed copper (not plated) as a result of the singulation process in manufacturing. A solder fillet at the exposed copper tip cannot be guaranteed and is not required to ensure adequate bottom side solder interconnection.
- e. Rework conditions: manual soldering with a soldering iron is not recommended for leadless components.
- f. Maximum under steady state conditions is 80 °C/W.

Document Number: 62779 S12-2394-Rev. A, 15-Oct-12 For technical questions, contact: pmostechsupport@vishay.com

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Parameter	Symbol	Test Conditions	Min.	Тур.	Max.	Unit
Static	1				•	
Drain-Source Breakdown Voltage	V_{DS}	$V_{GS} = 0$, $I_D = -250 \mu A$	- 30			V
V _{DS} Temperature Coefficient	$\Delta V_{DS}/T_{J}$	J 050 A		- 21		\//00
V _{GS(th)} Temperature Coefficient	$\Delta V_{GS(th)}/T_J$	$I_{D} = -250 \mu\text{A}$		4.6		mV/°C
Gate-Source Threshold Voltage	V _{GS(th)}	$V_{DS} = V_{GS}, I_{D} = -250 \mu A$	- 1		- 2.2	V
Gate-Source Leakage	I _{GSS}	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$			± 100	nA
Zara Cata Valtara Drain Current		V _{DS} = - 30 V, V _{GS} = 0 V			- 1	μΑ
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = - 30 V, V _{GS} = 0 V, T _J = 55 °C			- 10	
On-State Drain Current ^a	I _{D(on)}	$V_{DS} \le -5 \text{ V}, V_{GS} = -10 \text{ V}$	- 10			Α
Dunin Course On Chata Basistanasi		V _{GS} = - 10 V, I _D = - 5 A		0.016	0.021	Ω
Drain-Source On-State Resistance ^a	R _{DS(on)}	V _{GS} = - 4.5 V, I _D = - 3 A		0.024	0.030	
Forward Transconductance ^a	9 _{fs}	V _{DS} = - 10 V, I _D = - 5 A		23		S
Dynamic ^b						
Input Capacitance	C _{iss}			1550		pF
Output Capacitance	C _{oss}	V _{DS} = - 15 V, V _{GS} = 0 V, f = 1 MHz		175		
Reverse Transfer Capacitance	C _{rss}			150		
T. 10 . 0	Qg	V _{DS} = - 15 V, V _{GS} = - 10 V, I _D = - 10 A		29	45	nC
Total Gate Charge				14	21	
Gate-Source Charge	Q_{gs}	V _{DS} = - 15 V, V _{GS} = - 4.5 V, I _D = - 10 A		4.4		
Gate-Drain Charge	Q _{gd}			4.8		
Gate Resistance	R_{g}	f = 1 MHz	0.7	3.7	7.4	Ω
Turn-On Delay Time	t _{d(on)}			37	80	-
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 1.9 \Omega$		30	60	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -8 \text{ A}, V_{GEN} = -4.5 \text{ V}, R_g = 1 \Omega$		25	50	
Fall Time	t _f			8	20	
Turn-On Delay Time	t _{d(on)}			10	10	ns -
Rise Time	t _r	$V_{DD} = -15 \text{ V}, R_{L} = 1.9 \Omega$		10	20	
Turn-Off Delay Time	t _{d(off)}	$I_D \cong -8 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		27	55	
Fall Time	t _f			9	20	
Drain-Source Body Diode Characterist						
Continuous Source-Drain Diode Current	I _S	T _C = 25 °C			- 12	
Pulse Diode Forward Current	I _{SM}				- 40	Α
Body Diode Voltage	V_{SD}	I _S = -8 A, V _{GS} = 0		- 0.8	- 1.2	V
Body Diode Reverse Recovery Time t _{rr}				17	40	ns
Body Diode Reverse Recovery Charge	Q _{rr}			10	20	nC
Reverse Recovery Fall Time	ta	$I_F = -8 \text{ A}, \text{ dI/dt} = 100 \text{ A/}\mu\text{s}, T_J = 25 ^{\circ}\text{C}$		10		
Reverse Recovery Rise Time	t _b			7	Ì	ns

Notes:

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

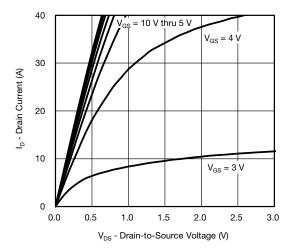
a. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %.

b. Guaranteed by design, not subject to production testing.

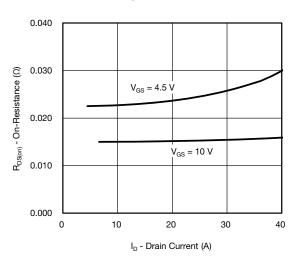


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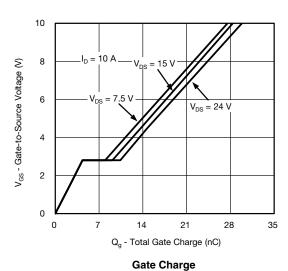
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

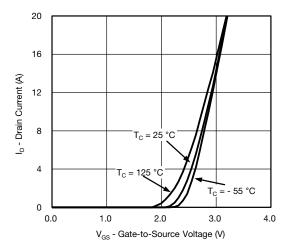


Output Characteristics

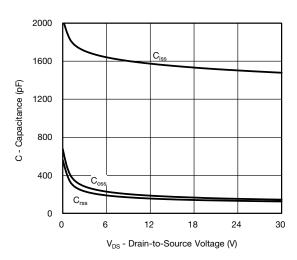


On-Resistance vs. Drain Current and Gate Voltage

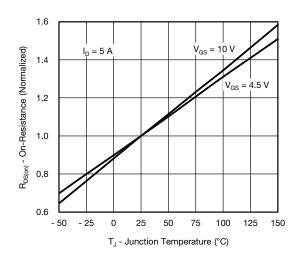




Transfer Characteristics



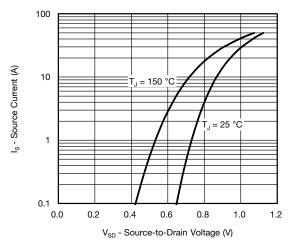
Capacitance

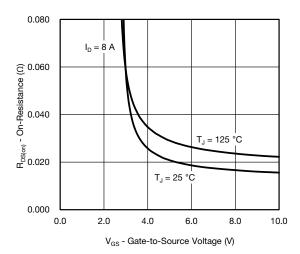


On-Resistance vs. Junction Temperature

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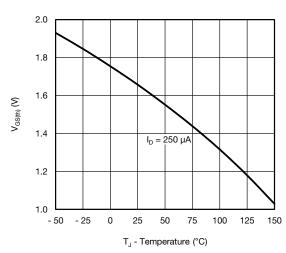
TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)

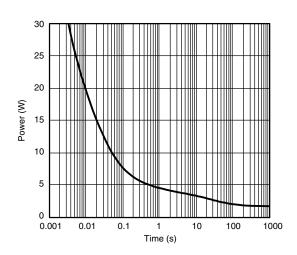




Soure-Drain Diode Forward Voltage

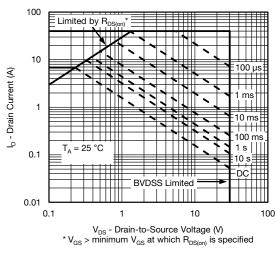
On-Resistance vs. Gate-to-Source Voltage





Threshold Voltage

Single Pulse Power, Junction-to-Ambient

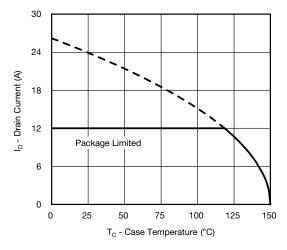


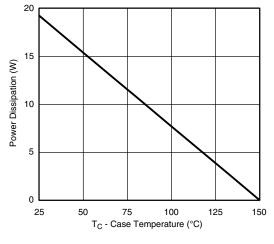
Safe Operating Area, Junction-to-Ambient



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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)





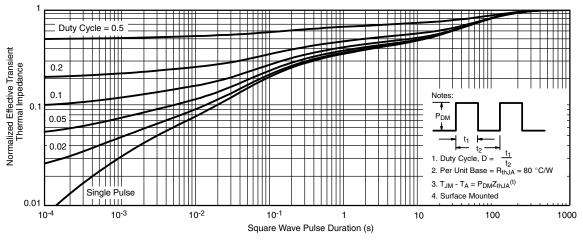
Current Derating*

Power Derating

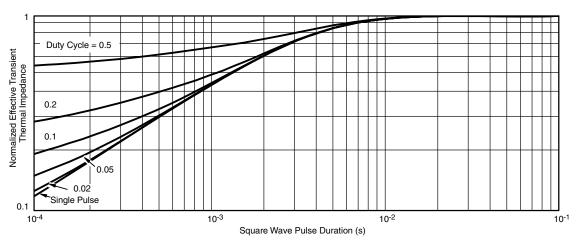
 $^{^{\}star}$ The power dissipation P_D is based on $T_{J(max.)}$ = 150 °C, using junction-to-case thermal resistance, and is more useful in settling the upper dissipation limit for cases where additional heatsinking is used. It is used to determine the current rating, when this rating falls below the package limit.

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TYPICAL CHARACTERISTICS (25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Vishay Siliconix maintains worldwide manufacturing capability. Products may be manufactured at one of several qualified locations. Reliability data for Silicon Technology and Package Reliability represent a composite of all qualified locations. For related documents such as package/tape drawings, part marking, and reliability data, see www.vishay.com/ppg?62779.





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PowerPAK® SC70-6L





BACKSIDE VIEW OF SINGLE

BACKSIDE VIEW OF DUAL



- All dimensions are in millimeters
 Package outline exclusive of mold flash and metal burr
 Package outline inclusive of plating

	SINGLE PAD						DUAL PAD					
DIM	MILLIMETERS			INCHES			MILLIMETERS			INCHES		
	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max
Α	0.675	0.75	0.80	0.027	0.030	0.032	0.675	0.75	0.80	0.027	0.030	0.032
A 1	0	-	0.05	0	-	0.002	0	-	0.05	0	-	0.002
b	0.23	0.30	0.38	0.009	0.012	0.015	0.23	0.30	0.38	0.009	0.012	0.015
С	0.15	0.20	0.25	0.006	0.008	0.010	0.15	0.20	0.25	0.006	0.008	0.010
D	1.98	2.05	2.15	0.078	0.081	0.085	1.98	2.05	2.15	0.078	0.081	0.085
D1	0.85	0.95	1.05	0.033	0.037	0.041	0.513	0.613	0.713	0.020	0.024	0.028
D2	0.135	0.235	0.335	0.005	0.009	0.013						
E	1.98	2.05	2.15	0.078	0.081	0.085	1.98	2.05	2.15	0.078	0.081	0.085
E1	1.40	1.50	1.60	0.055	0.059	0.063	0.85	0.95	1.05	0.033	0.037	0.041
E2	0.345	0.395	0.445	0.014	0.016	0.018						
E3	0.425	0.475	0.525	0.017	0.019	0.021						
е		0.65 BSC			0.026 BSC	;	0.65 BSC			0.026 BSC		
K		0.275 TYP	١		0.011 TYP		0.275 TYP			0.011 TYP		
K1		0.400 TYP	١	0.016 TYP			0.320 TYP			0.013 TYP		
K2		0.240 TYP 0.009 TYP			0.252 TYP 0.010 TYP			1				
К3		0.225 TYP	1	0.009 TYP								
K4		0.355 TYP		0.014 TYP								
L	0.175	0.275	0.375	0.007	0.011	0.015	0.175	0.275	0.375	0.007	0.011	0.015
Т							0.05	0.10	0.15	0.002	0.004	0.006
FCN: C-07431 – Bey C. 06-Aug-07												

DWG: 5934

Document Number: 73001 06-Aug-07



RECOMMENDED PAD LAYOUT FOR PowerPAK® SC70-6L Single



Dimensions in mm/(Inches)

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ATTLICATION NOT



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Revision: 02-Oct-12 Document Number: 91000

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IPS70R2K0CEAKMA1 BUK954R8-60E DMN3404LQ-7 NTE6400 SQJ402EP-T1-GE3 2SK2614(TE16L1,Q) 2N7002KW-FAI

DMN1017UCP3-7 EFC2J004NUZTDG ECH8691-TL-W FCAB21350L1 P85W28HP2F-7071 DMN1053UCP4-7 NTE221 NTE2384

NTE2903 NTE2941 NTE2945 NTE2946 NTE2960 NTE2967 NTE2969 NTE2976 NTE455 NTE6400A NTE2910 NTE2916 NTE2956

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