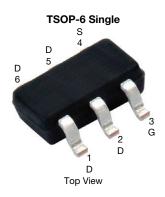
Vishay Siliconix

Automotive P-Channel 20 V (D-S) 175 °C MOSFET



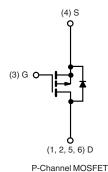
PRODUCT SUMMARY					
V _{DS} (V)	-20				
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.036				
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.064				
I _D (A)	-8				
Configuration	Single				

FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified c
- 100 % R_q and UIS tested
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>







ORDERING INFORMATION	
Package	TSOP-6
Lead (Pb)-free and halogen-free	SQ3469EV (for detailed order number please see www.vishay.com/doc?79771)

ABSOLUTE MAXIMUM RATING	GS (T _C = 25 °C, unles	s otherwise noted	d)	
PARAMETER	SYMBOL	LIMIT	UNIT	
Drain-source voltage	V_{DS}	-20	V	
Gate-source voltage	V_{GS}	± 20	V	
Continuous drain current	T _C = 25 °C ^a	1	-8	
Continuous drain current	T _C = 125 °C	- I _D	-5	
Continuous source current	Is	-6	Α	
Pulsed drain current ^a	I _{DM}	-32		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	-17	
Single pulse avalanche energy	L = 0.1 IIII	E _{AS}	14	mJ
Maximum power dissipation ^a	T _C = 25 °C	D	5	W
	T _C = 125 °C	P_{D}	1.6	VV
Operating junction and storage temperatur	e range	T _J , T _{stg}	-55 to +175	°C

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction to ambient	PCB mount b	R_{thJA}	110	°C/W	
Junction to foot (drain)		R_{thJF}	30	C/VV	

Notes

- a. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- b. When mounted on 1" square PCB (FR-4 material)
- c. Parametric verification ongoing



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PARAMETER	SYMBOL	TES	MIN.	TYP.	MAX.	UNIT		
Static	-				·			
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 \text{ V}, I_D = -250 \mu\text{A}$		- 20	-	-	V	
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	$V_{DS} = V_{GS}, I_D = -250 \mu A$		- 2.0	- 2.5	V	
Gate-source leakage	I _{GSS}	V _{DS} =	$V_{DS} = 0 \text{ V}, V_{GS} = \pm 20 \text{ V}$		-	± 100	nA	
		V _{GS} = 0 V	V _{DS} = - 20 V	-	-	- 1		
Zero gate voltage drain current	I _{DSS}	V _{GS} = 0 V	V _{DS} = - 20 V, T _J = 125 °C	-	-	- 50	μΑ	
		V _{GS} = 0 V	V _{DS} = - 20 V, T _J = 175 °C	-	-	- 150	1	
On-state drain current ^a	I _{D(on)}	V _{GS} = - 10 V	V _{DS} ≤ - 5 V	- 10	-	-	Α	
Drain actives on state resistance 3		V _{GS} = - 10 V	I _D = - 6.7 A	-	0.029	0.036		
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = - 4.5 V	I _D = - 2 A	-	0.052	0.064	Ω	
Forward transconductance b	9fs	V _{DS} = - 15 V, I _D = - 6 A		-	12	-	S	
Dynamic ^b								
Input capacitance	C _{iss}			-	816	1020		
Output capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = - 10 V, f = 1 MHz	-	294	367	pF	
Reverse transfer capacitance	C _{rss}			-	180	225		
Total gate charge ^c	Q_g			-	17.7	27		
Gate-source charge ^c	Q _{gs}	V _{GS} = - 10 V	$V_{DS} = -10 \text{ V}, I_{D} = -6.7 \text{ A}$	-	2.9		nC	
Gate-drain charge ^c	Q _{gd}			-	3.9			
Gate resistance	R_g		f = 1 MHz		-	12	Ω	
Turn-on delay time ^c	t _{d(on)}			-	13	20		
Rise time ^c	t _r	$V_{DD} = -10 \text{ V}, R_L = 10 \Omega$ $I_D \cong -1 \text{ A}, V_{GEN} = -10 \text{ V}, R_g = 1 \Omega$		-	10	15	ns	
Turn-off delay time ^c	t _{d(off)}			-	32	48		
Fall time ^c	t _f			-	10	15		
Source-Drain Diode Ratings and Chara	cteristics b							
Pulsed current ^a	I _{SM}			-	-	- 32	Α	
Forward voltage	V_{SD}	I _F = - 5 A, V _{GS} = 0 V		-	- 0.86	- 1.2	V	

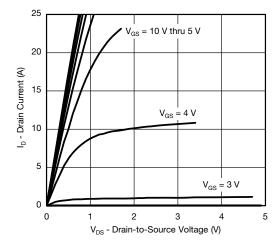
Notes

- a. Pulse test; pulse width $\leq 300~\mu s,~duty~cycle \leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

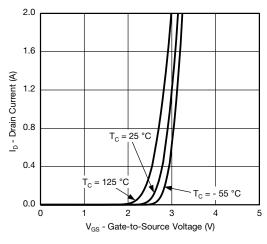
Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.



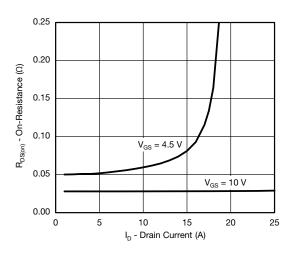
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



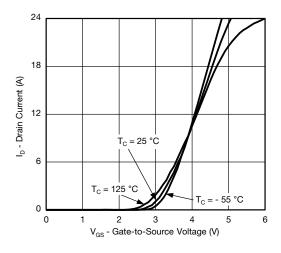
Output Characteristics



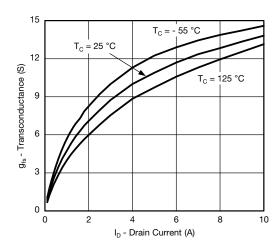
Transfer Characteristics



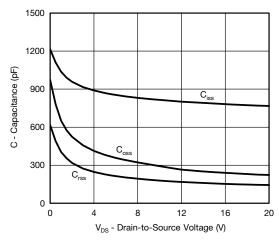
On-Resistance vs. Drain Current



Transfer Characteristics



Transconductance

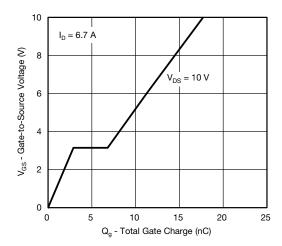


Capacitance

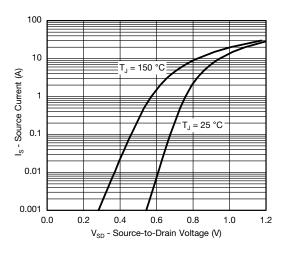
For technical questions, contact: automostechsu



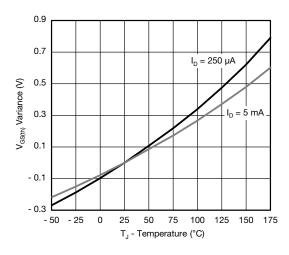
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



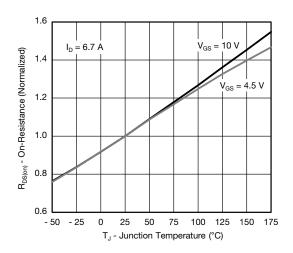
Gate Charge



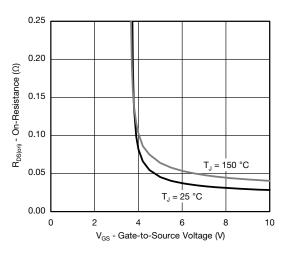
Source-Drain Diode Forward Voltage



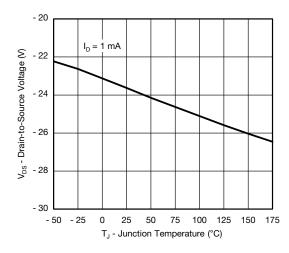
Threshold Voltage



On-Resistance vs. Junction Temperature



On-Resistance vs. Gate-to-Source Voltage

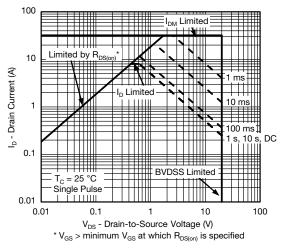


Drain Source Breakdown vs. Junction Temperature

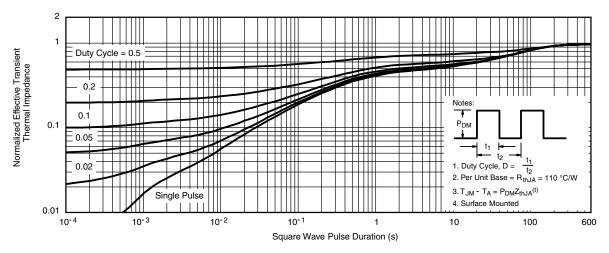
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THERMAL RATINGS ($T_A = 25$ °C, unless otherwise noted)



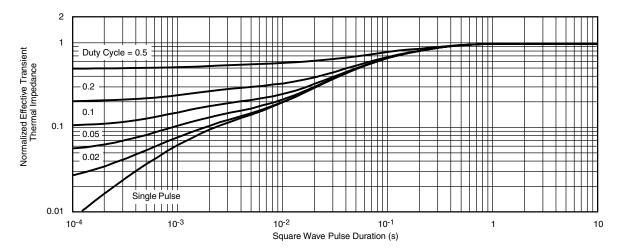
Safe Operating Area



Normalized Thermal Transient Impedance, Junction-to-Ambient



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions.

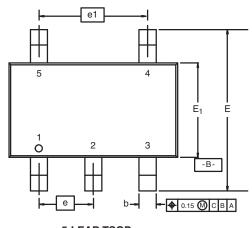
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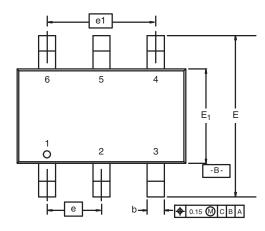




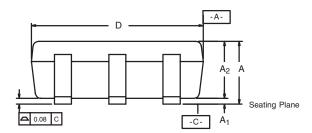
TSOP: 5/6-LEAD

JEDEC Part Number: MO-193C

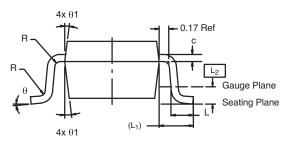




5-LEAD TSOP







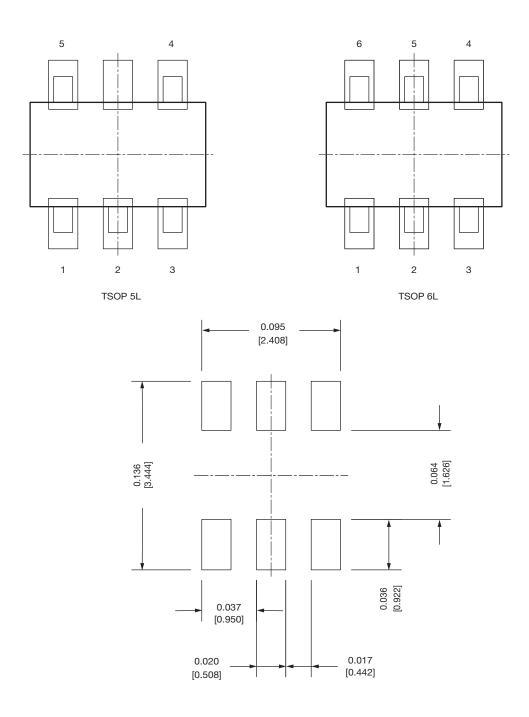
	MIL	LIMETER	RS	INCHES			
Dim	Min	Nom	Max	Min	Nom	Max	
Α	0.91	-	1.10	0.036	-	0.043	
A ₁	0.01	-	0.10	0.0004	-	0.004	
A ₂	0.90	-	1.00	0.035	0.038	0.039	
b	0.30	0.32	0.45	0.012	0.013	0.018	
С	0.10	0.15	0.20	0.004	0.006	0.008	
D	2.95	3.05	3.10	0.116	0.120	0.122	
E	2.70	2.85	2.98	0.106	0.112	0.117	
E ₁	1.55	1.65	1.70	0.061	0.065	0.067	
е		0.95 BSC		0.0374 BSC			
e ₁	1.80	1.90	2.00	0.071	0.075	0.079	
L	0.32	-	0.50	0.012	-	0.020	
L ₁	0.60 Ref			0.024 Ref			
L ₂	0.25 BSC			0.010 BSC			
R	0.10	-	-	0.004	-	-	
θ	0°	4°	8°	0°	4°	8°	
θ_1	7° Nom			7° Nom			
ECN: C-06593-Rev. I, 18-Dec-06 DWG: 5540							

Document Number: 71200

18-Dec-06



Recommended Land Pattern For TSOP-5L / TSOP-6L



Note

• All dimensions are in inches (millimeter)

ECN: C22-0860-Rev. B, 24-Oct-2022 DWG: 3010



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DMN1017UCP3-7 EFC2J004NUZTDG P85W28HP2F-7071 DMN1053UCP4-7 NTE2384 DMC2700UDMQ-7 DMN2080UCB4-7
DMN61D9UWQ-13 US6M2GTR DMN31D5UDJ-7 DMP22D4UFO-7B IPS60R3K4CEAKMA1 DMN1006UCA6-7 DMN16M9UCA6-7
STF5N65M6 IRF40H233XTMA1 STU5N65M6 DMN6022SSD-13 DMN13M9UCA6-7 DMTH10H4M6SPS-13 IPS60R360PFD7SAKMA1
DMN2990UFB-7B SSM3K35CT,L3F IPLK60R1K0PFD7ATMA1 2N7002W-G MCAC30N06Y-TP IPWS65R035CFD7AXKSA1
MCQ7328-TP SSM3J143TU,LXHF PJMF280N65E1_T0_00201 PJMF380N65E1_T0_00201
PJMF600N65E1_T0_00201 PJMF900N65E1_T0_00201 PJMF900N60E1_T0_00201