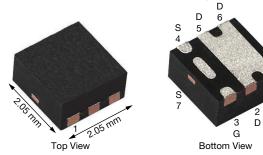


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Vishay Siliconix

Automotive P-Channel 30 V (D-S) 175 °C MOSFET

PowerPAK® SC-70-6L Single



Marking Code: QFXXXX

PRODUCT SUMMARY									
V _{DS} (V)	-30								
$R_{DS(on)}(\Omega)$ at $V_{GS} = -10 \text{ V}$	0.020								
$R_{DS(on)}(\Omega)$ at $V_{GS} = -4.5 \text{ V}$	0.033								
I _D (A)	-10								
Configuration	Single								
Package	PowerPAK SC-70								

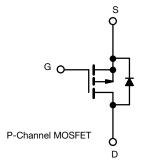
FEATURES

- TrenchFET® power MOSFET
- AEC-Q101 qualified d
- 100 % R_g and UIS tested
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912





ROHS COMPLIANT HALOGEN FREE



PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V_{DS}	-30	V	
Gate-source voltage		V _{GS}	± 20	V	
Continuous drain current	T _C = 25 °C	1	-10		
Continuous drain current	T _C = 125 °C	I _D	-10	7	
Continuous source current (diode conduc	tion) ^a	I _S	10	Α	
Pulsed drain current ^b		I _{DM}	-40		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	15		
Single pulse avalanche energy	L=U.IIIII	E _{AS}	11.25	mJ	
Maximum power dissipation ^b	T _C = 25 °C	Р	13.6	— w	
iviaximum power dissipation 5	T _C = 125 °C	P_{D}	4.5		
Operating junction and storage temperatu	re range	T _J , T _{stq}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS									
PARAMETER		SYMBOL	LIMIT	UNIT					
Junction-to-ambient	PCB mount c	R_{thJA}	90	°C/W					
Junction-to-case (drain)		R_{thJF}	11	G/ VV					

Notes

- a. Package limited
- b. Pulse test; pulse width \leq 300 μ s, duty cycle \leq 2 %
- c. When mounted on 1" square PCB (FR4 material)
- d. Parametric verification ongoing



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Static Drain-source breakdown voltage V _{DS} V _{GS} = 0, I _D = -250 μA -30 - - Gate-source threshold voltage V _{GS} (th) V _{DS} = V _{GS} , I _D = -250 μA -1.5 -2 -2.5 Gate-source leakage I _{GSS} V _{DS} = 0 V, V _{GS} = ±20 V - - ± 100 Zero gate voltage drain current I _{DSS} V _{GS} = 0 V V _{DS} = -30 V, T _J = 125 °C - - - -50 On-state drain current a I _{D(on)} V _{GS} = 0 V V _{DS} = -30 V, T _J = 125 °C - - - -50 - -50 - - -50 - - -50 - - -50 - - -50 - - -50 - - -50 - - -50 - - -50 - - - -50 - - - - - - - - - - - - - - - - - - - <td< th=""><th>UNIT</th></td<>	UNIT	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		
	V	
	7 V	
$ \begin{tabular}{ c c c c c c c c c c c c c c c c c c c$	nA	
$ V_{GS} = 0 \ V V_{DS} = -30 \ V, \ T_J = 175 \ ^{\circ} C \qquad - \qquad - \qquad - \qquad - 150 \\ On-state drain current \ ^{a} \qquad I_{D(on)} \qquad V_{GS} = -10 \ V \qquad V_{DS} \ge 5 \ V \qquad - 8 \qquad - \qquad$		
$ \begin{array}{c} \text{On-state drain current}^{ a} & \text{ID}_{\text{(on)}} & \text{V}_{\text{GS}} = -10 \ \text{V} & \text{V}_{\text{DS}} \ge 5 \ \text{V} & -8 & - & - \\ & & & & & & & & & & & & & & &$	μΑ	
$ P_{Drain-source on-state resistance a} \begin{tabular}{c c c c c c c c c c c c c c c c c c c $	1	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	Α	
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$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$	nC	
$ \begin{array}{c ccccccccccccccccccccccccccccccccccc$		
$\begin{array}{c ccccccccccccccccccccccccccccccccccc$	Ω	
$ \begin{array}{cccccccccccccccccccccccccccccccccccc$		
G(OII)	-	
	ns	
Fall time c t _f - 8 12	1	
Source-Drain Diode Ratings and Characteristics		
Pulsed current ^a I _{SM} 40	Α	
Forward voltage V_{SD} $I_F = -5 \text{ A}, V_{GS} = 0$ - -0.8 -1.2	V	

Notes

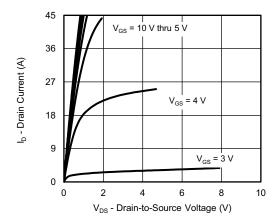
- a. Pulse test; pulse width $\leq 300~\mu s,$ duty cycle $\leq 2~\%$
- b. Guaranteed by design, not subject to production testing
- c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

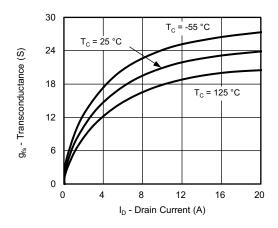


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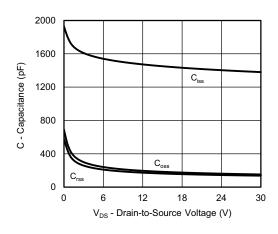
TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



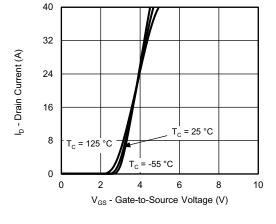
Output Characteristics



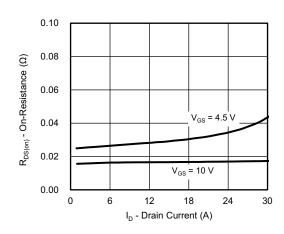
Transconductance



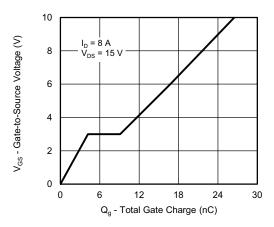
Capacitance



Transfer Characteristics



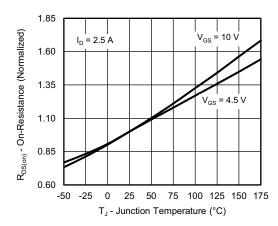
On-Resistance vs. Drain Current



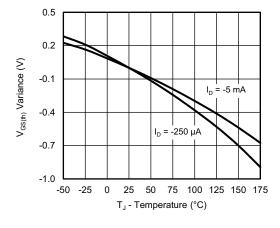
Gate Charge



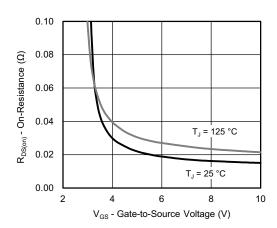
TYPICAL CHARACTERISTICS ($T_A = 25$ °C, unless otherwise noted)



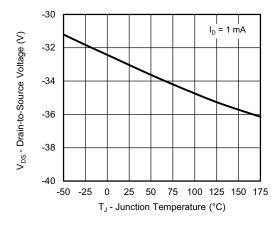
On-Resistance vs. Junction Temperature



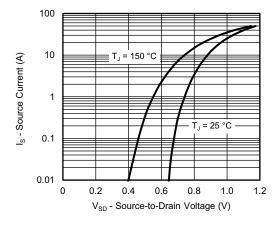
Threshold Voltage



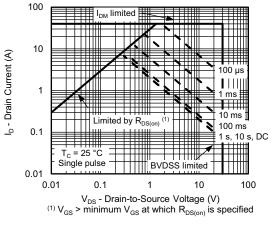
On-Resistance vs. Gate-to-Source Voltage



Drain Source Breakdown vs. Junction Temperature



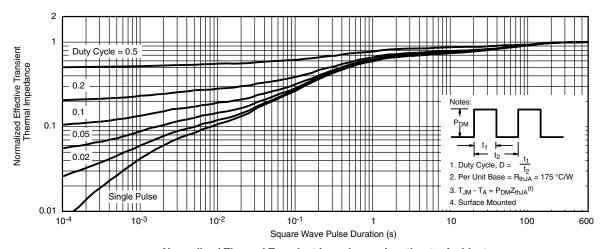
Source-Drain Diode Forward Voltage



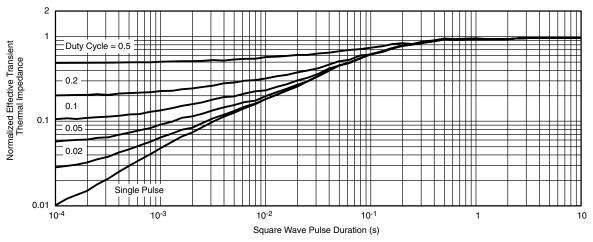
Safe Operating Area



THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Foot

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
 - Normalized Transient Thermal Impedance Junction-to-Foot (25 °C)

are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

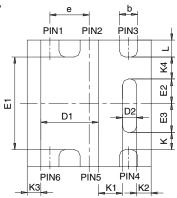
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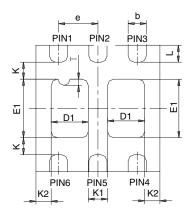
Package Information



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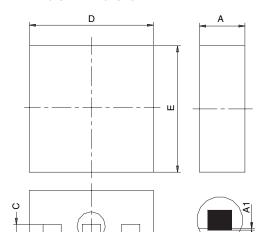
PowerPAK® SC70-6L





BACKSIDE VIEW OF SINGLE

BACKSIDE VIEW OF DUAL



- Notes:

 1. All dimensions are in millimeters

 2. Package outline exclusive of mold flash and metal burr

 3. Package outline inclusive of plating

	SINGLE PAD						DUAL PAD						
DIM	М	ILLIMETER	RS		INCHES		MILLIMETERS			INCHES			
	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max	Min	Nom	Max	
Α	0.675	0.75	0.80	0.027	0.030	0.032	0.675	0.75	0.80	0.027	0.030	0.032	
A 1	0	-	0.05	0	-	0.002	0	-	0.05	0	-	0.002	
b	0.23	0.30	0.38	0.009	0.012	0.015	0.23	0.30	0.38	0.009	0.012	0.015	
С	0.15	0.20	0.25	0.006	0.008	0.010	0.15	0.20	0.25	0.006	0.008	0.010	
D	1.98	2.05	2.15	0.078	0.081	0.085	1.98	2.05	2.15	0.078	0.081	0.085	
D1	0.85	0.95	1.05	0.033	0.037	0.041	0.513	0.613	0.713	0.020	0.024	0.028	
D2	0.135	0.235	0.335	0.005	0.009	0.013							
E	1.98	2.05	2.15	0.078	0.081	0.085	1.98	2.05	2.15	0.078	0.081	0.085	
E1	1.40	1.50	1.60	0.055	0.059	0.063	0.85	0.95	1.05	0.033	0.037	0.041	
E2	0.345	0.395	0.445	0.014	0.016	0.018							
E3	0.425	0.475	0.525	0.017	0.019	0.021							
е		0.65 BSC			0.026 BSC			0.65 BSC			0.026 BSC		
K		0.275 TYP			0.011 TYP			0.275 TYP			0.011 TYP		
K1		0.400 TYP			0.016 TYP			0.320 TYP			0.013 TYP		
K2		0.240 TYP		0.009 TYP			0.252 TYP			0.010 TYP			
К3		0.225 TYP		0.009 TYP									
K4		0.355 TYP		0.014 TYP									
L	0.175	0.275	0.375	0.007	0.011	0.015	0.175	0.275	0.375	0.007	0.011	0.015	
Т							0.05	0.10	0.15	0.002	0.004	0.006	
ECNI: C O	7401 Day	, C 06 Au	- 07										

DETAIL Z

ECN: C-07431 - Rev. C, 06-Aug-07

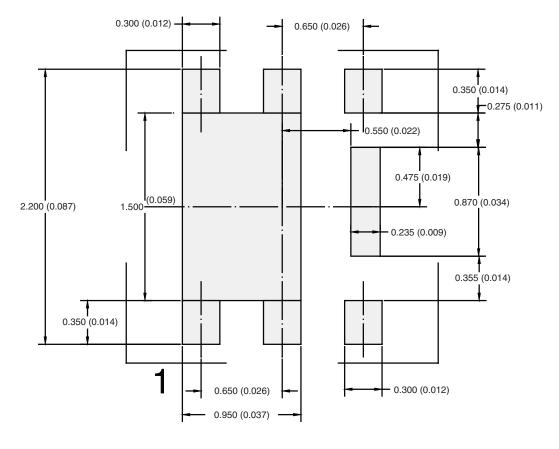
DWG: 5934

Document Number: 73001 06-Aug-07

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RECOMMENDED PAD LAYOUT FOR PowerPAK® SC70-6L Single



Dimensions in mm/(Inches)

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