SQR40020ER

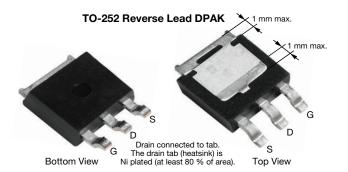
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COMPLIANT

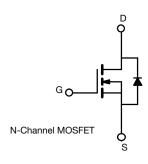
Automotive N-Channel 40 V (D-S) 175 °C MOSFET



PRODUCT SUMMARY				
V _{DS} (V)	40			
$R_{DS(on)} (\Omega)$ at $V_{GS} = 10 V$	0.00233			
I _D (A)	100			
Configuration	Single			
Package	TO-252 reverse lead DPAK			

FEATURES

- TrenchFET[®] power MOSFET
- · Package with low thermal resistance
- Ni plated drain tab area (heatsink) for top side cooling
- 100 % R_g and UIS tested
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>



ABSOLUTE MAXIMUM RATINGS (To	_C = 25 °C, unles	s otherwise notec)		
PARAMETER		SYMBOL	LIMIT	UNIT	
Drain-source voltage		V _{DS}	40	V	
Gate-source voltage		V _{GS}	± 20	v	
Continuous drain current	$T_{C} = 25 \ ^{\circ}C \ ^{a}$	I-	100		
Continuous drain current	T _C = 125 °C	۱ _D	87.5		
Continuous source current (diode conduction)		I _S	97	А	
Pulsed drain current ^b		I _{DM}	280		
Single pulse avalanche current	L = 0.1 mH	I _{AS}	46		
Single pulse avalanche energy		E _{AS}	105.8	mJ	
Maximum power dissipation ^b	T _C = 25 °C	PD	107	W	
	T _C = 125 °C	۳D	35	VV	
Operating junction and storage temperature range)	T _J , T _{stg}	-55 to +175	°C	

THERMAL RESISTANCE RATINGS					
PARAMETER		SYMBOL	LIMIT	UNIT	
Junction-to-ambient	PCB mount ^c	R _{thJA}	50	°C/W	
Junction-to-case (drain)		R _{thJC}	1.4	0/10	

Notes

a. Package limited

b. Pulse test; pulse width \leq 300 $\mu s,$ duty cycle \leq 2 %

c. When mounted on 1" square PCB (FR4 material)

SQR40020ER

SPECIFICATIONS ($T_C = 25 \ ^{\circ}C$,	unless other	wise noted)					
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNIT
Static							
Drain-source breakdown voltage	V _{DS}	$V_{GS} = 0 V, I_D = 250 \mu A$		40	-	-	v
Gate-source threshold voltage	V _{GS(th)}	V _{DS} =	= V _{GS} , I _D = 250 μΑ	2.5	3.0	3.5	v
Gate-source leakage	I _{GSS}	V _{DS} =	0 V, $V_{GS} = \pm 20 V$	-	-	± 100	nA
		$V_{GS} = 0 V$	V _{DS} = 40 V	-	-	1	
Zero gate voltage drain current	I _{DSS}	$V_{GS} = 0 V$	V _{DS} = 40 V, T _J = 125 °C	-	-	50	μA
		$V_{GS} = 0 V$	V _{DS} = 40 V, T _J = 175 °C	-	-	500	μA
On-state drain current ^a	I _{D(on)}	V _{GS} = 10 V	$V_{DS} \ge 5 V$	50	-	-	Α
		V _{GS} = 10 V	I _D = 20 A	-	0.00190	0.00233	
Drain-source on-state resistance a	R _{DS(on)}	V _{GS} = 10 V	I _D = 20 A, T _J = 125 °C	-	-	0.00390	Ω
	. ,	V _{GS} = 10 V	I _D = 20 A, T _J = 175 °C	-	-	0.00470	
Forward transconductance ^b	g _{fs}	V _{DS}	= 15 V, I _D = 20 A	-	84	-	S
Dynamic ^b					•	•	
Input capacitance	C _{iss}			-	5405	8000	
Output capacitance	C _{oss}	$V_{GS} = 0 V$	V _{DS} = 25 V, f = 1 MHz	-	1942	2700	pF
Reverse transfer capacitance	C _{rss}			-	175	250	
Total gate charge ^c	Qg			-	84	130	
Gate-source charge ^c	Q _{gs}	V _{GS} = 10 V	= 10 V V _{DS} = 20 V, I _D = 50 A		29.5	-	nC
Gate-drain charge ^c	Q _{gd}			-	19.5	-	
Gate resistance	Rg	f = 1 MHz		1	2	3	Ω
Turn-on delay time ^c	t _{d(on)}			-	17	30	
Rise time ^c	t _r	- V _{DD} =	= 20 V, R _I = 0.4 Ω	-	17	30	
Turn-off delay time ^c	t _{d(off)}	$I_D \cong 50 \text{ A}, V_{\text{GEN}} = 10 \text{ V}, \text{ R}_{\text{g}} = 1 \Omega$		-	34	60	ns
Fall time ^c	t _f			-	18	35	
Source-Drain Diode Ratings and Chara	cteristics b				<u> </u>	•	
Pulsed current ^a	I _{SM}			-	-	280	А
Forward voltage	V _{SD}	I _F = 25 A, V _{GS} = 0 V		-	0.8	1.5	V
Body diode reverse recovery time	t _{rr}			-	41	85	ns
Body diode reverse recovery charge	Q _{rr}	- I _F = 50 A, di/dt = 100 A/μs		-	28	60	nC
Reverse recovery fall time	t _a			-	24	-	
Reverse recovery rise time	t _b	1		-	17	-	ns
Body diode peak reverse recovery current	I _{RM(REC)}			-	-1.36	-	А

Notes

a. Pulse test; pulse width \leq 300 µs, duty cycle \leq 2 %

b. Guaranteed by design, not subject to production testing

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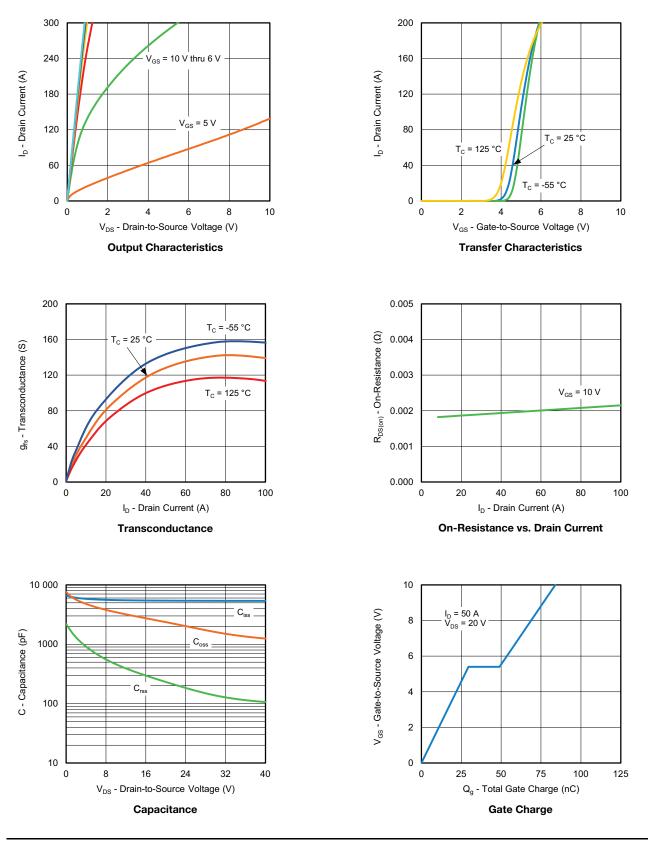
c. Independent of operating temperature

Stresses beyond those listed under "Absolute Maximum Ratings" may cause permanent damage to the device. These are stress ratings only, and functional operation of the device at these or any other conditions beyond those indicated in the operational sections of the specifications is not implied. Exposure to absolute maximum rating conditions for extended periods may affect device reliability.

2



TYPICAL CHARACTERISTICS (T_A = 25 °C, unless otherwise noted)



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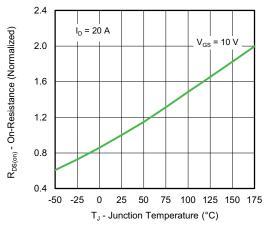
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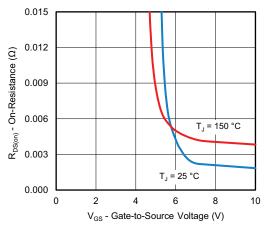
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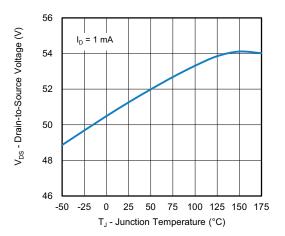
TYPICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$, unless otherwise noted)



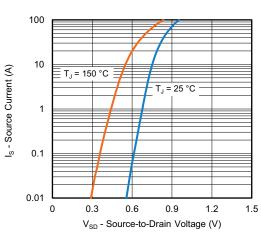
On-Resistance vs. Junction Temperature



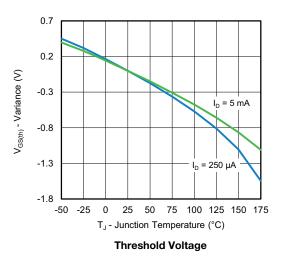
On-Resistance vs. Gate-to-Source Voltage

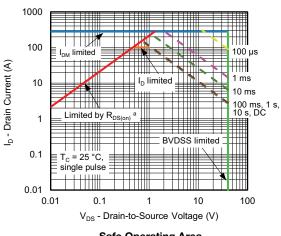


Drain Source Breakdown vs. Junction Temperature

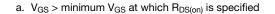


Source Drain Diode Forward Voltage





Safe Operating Area



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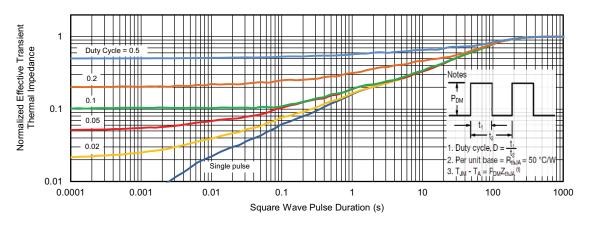
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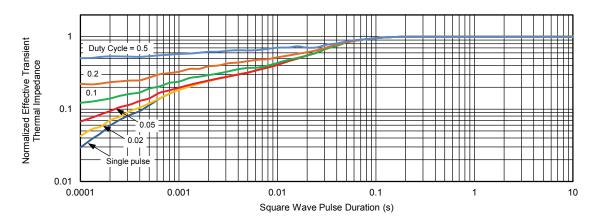
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THERMAL RATINGS (T_A = 25 °C, unless otherwise noted)



Normalized Thermal Transient Impedance, Junction-to-Ambient



Normalized Thermal Transient Impedance, Junction-to-Case

Note

- The characteristics shown in the two graphs
 - Normalized Transient Thermal Impedance Junction-to-Ambient (25 °C)
- Normalized Transient Thermal Impedance Junction-to-Case (25 °C)

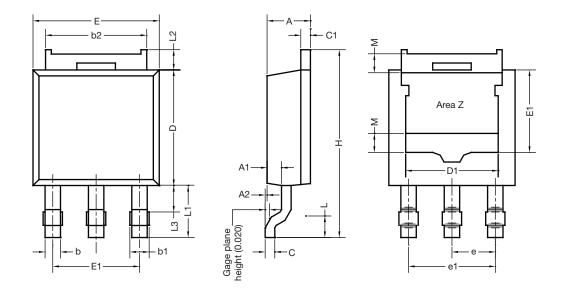
are given for general guidelines only to enable the user to get a "ball park" indication of part capabilities. The data are extracted from single pulse transient thermal impedance characteristics which are developed from empirical measurements. The latter is valid for the part mounted on printed circuit board - FR4, size 1" x 1" x 0.062", double sided with 2 oz. copper, 100 % on both sides. The part capabilities can widely vary depending on actual application parameters and operating conditions

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TO-252 Reverse Lead Case Outline



Notes

Dimension L3 for reference only

• Area Z: unplated area more than 80 % heatsink area and for partial plating part only

DIM.	MIL	LIMETERS	INCHES		
	MIN.	MAX.	MIN.	MAX.	
А	2.23	2.33	0.088	0.092	
A1	0.64	0.89	0.025	0.035	
A2	0.03	0.18	0.001	0.007	
b	0.71	0.88	0.028	0.035	
b1	0.76	1.14	0.030	0.045	
b2	5.23	5.44	0.206	0.214	
С	0.46	0.58	0.018	0.023	
C1	0.46	0.58	0.018	0.023	
D	5.97	6.22	0.235	0.245	
D1	4.49	5.00	0.177	0.197	
E	6.48	6.73	0.255	0.265	
E1	4.32	-	0.170	-	
е	2	2.28 BSC	0.090 BSC		
e1	4	I.57 BSC	0.180 BSC		
Н	9.65	10.41	0.380	0.410	
L	1.40	1.78	0.055	0.070	
L1	2.74 BSC		0.108 BSC		
L2	0.89	1.27	0.035	0.050	
L3	1.15	1.52	0.040	0.060	
М	-	1.00 (reference only)	-	0.039 (reference only	



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