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Vishay General Semiconductor

High Current Density Surface-Mount Schottky Barrier Rectifier



LINKS TO ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS				
I _{F(AV)}	1.0 A			
V_{RRM}	50 V, 60 V			
I _{FSM}	50 A			
E _{AS}	11.25 mJ			
V _F at I _F = 1.0 A	0.43 V			
T _J max.	150 °C			
Package	SMP (DO-220AA)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 1.0 mm
- Ideal for automated placement
- Low forward voltage drop, low power losses
- · High efficiency
- · Low thermal resistance
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMP (DO-220AA)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and automotive grade

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

 $\mbox{M3}$ suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes the cathode end

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	SS1P5L	SS1P6L	UNIT	
Device marking code		15L	16L		
Maximum repetitive peak reverse voltage	V _{RRM}	50	60	V	
Maximum average forward rectified current (fig. 1)	I _{F(AV)}	1.0		Α	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	50		А	
Operating junction and storage temperature range	T _J , T _{STG}	-55 to +150		°C	





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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Maximum instantaneous forward voltage	I 1 0 A	$T_A = 25 ^{\circ}\text{C}$ $T_A = 125 ^{\circ}\text{C}$	V _F ⁽¹⁾	0.52	0.59	- V
	$I_F = 1.0 A$	T _A = 125 °C		0.43	0.52	
Maximum reverse current	Rated V _R	T _A = 25 °C	I _R ⁽²⁾	-	100	μΑ
		T _A = 125 °C		1.6	10	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	80	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width \leq 40 ms

THERMAL CHARACTERISTICS (T _A = 25 °c unless otherwise noted)						
PARAMETER	SYMBOL	SS1P5L	SS1P6L	UNIT		
Typical thermal resistance	R _{θJA} ⁽¹⁾	125		°C/W		
Typical trieffial resistance	R ₀ JL (1)	25				

Note

 $^{(1)}$ Thermal resistance from junction to ambient and junction to lead mounted on PCB with 5.0 mm x 5.0 mm copper pad areas. $R_{\theta JL}$ - is measured at the terminal of cathode band.

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
SS1P6L-M3/84A	0.024	84A	3000	7" diameter plastic tape and reel		
SS1P6L-M3/85A	0.024	85A	10 000	13" diameter plastic tape and reel		
SS1P6LHM3/84A ⁽¹⁾	0.024	84A	3000	7" diameter plastic tape and reel		
SS1P6LHM3/85A (1)	0.024	85A	10 000	13" diameter plastic tape and reel		

Note

(1) Automotive grade

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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °c unless otherwise noted)

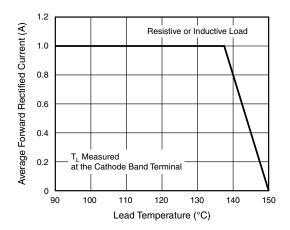


Fig. 1 - Maximum Forward Current Derating Curve

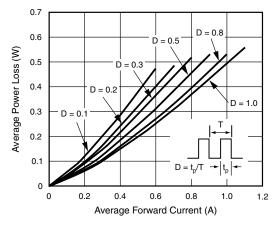


Fig. 2 - Forward Power Loss Characteristics

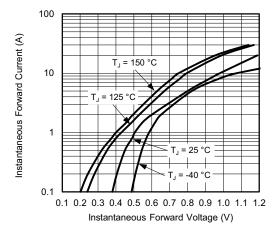


Fig. 3 - Typical Instantaneous Forward Characteristics

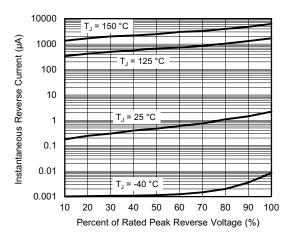


Fig. 4 - Typical Reverse Leakage Characteristics

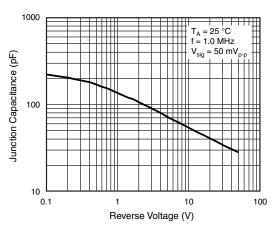


Fig. 5 - Typical Junction to Capacitance

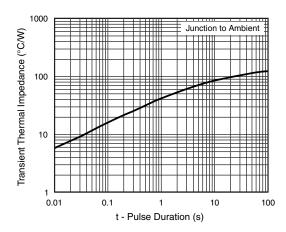


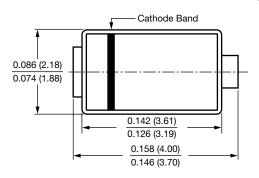
Fig. 6 - Typical Transient Thermal Impedance

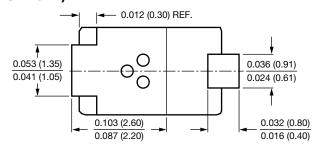


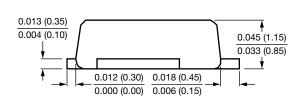
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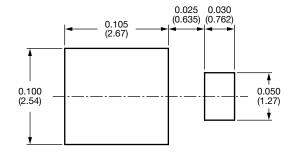
PACKAGE OUTLINE DIMENSIONS in inches (millimeters)

SMP (DO-220AA)











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