

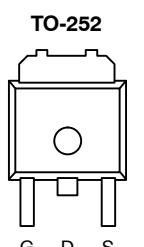
N-Channel 80-V (D-S) 175°C MOSFET

PRODUCT SUMMARY

V_{DS} (V)	$r_{DS(on)}$ (Ω)	I_D (A)
80	0.016 @ $V_{GS} = 10$ V	40

FEATURES

- TrenchFET® Power MOSFET
- 175°C Maximum Junction Temperature
- 100% R_g Tested

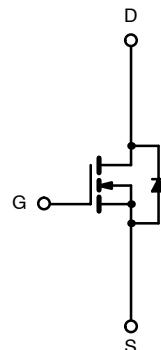


Drain Connected to Tab

Top View

Ordering Information:

SUD40N08-16
SUD40N08-16-E3 (Lead Free)



N-Channel MOSFET

ABSOLUTE MAXIMUM RATINGS ($T_A = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Limit	Unit
Drain-Source Voltage	V_{DS}	80	V
Gate-Source Voltage	V_{GS}	± 20	
Continuous Drain Current ($T_J = 175^\circ\text{C}$) ^b	I_D	40	
		30	
Pulsed Drain Current	I_{DM}	60	A
Continuous Source Current (Diode Conduction)	I_S	40	
Avalanche Current	I_{AR}	40	
Repetitive Avalanche Energy (Duty Cycle $\leq 1\%$)	E_{AR}	80	
Maximum Power Dissipation	P_D	136 ^b	W
		3 ^a	
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 to 175	°C

THERMAL RESISTANCE RATINGS

Parameter	Symbol	Typical	Maximum	Unit
Junction-to-Ambient ^a	R_{thJA}	15	18	°C/W
		40	50	
Junction-to-Case	R_{thJC}	0.85	1.1	

Notes

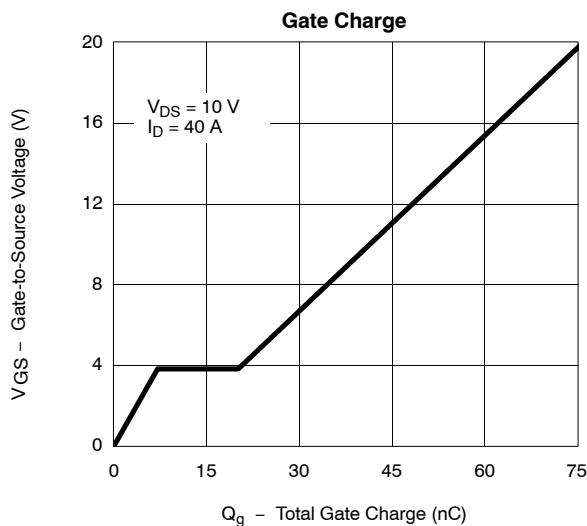
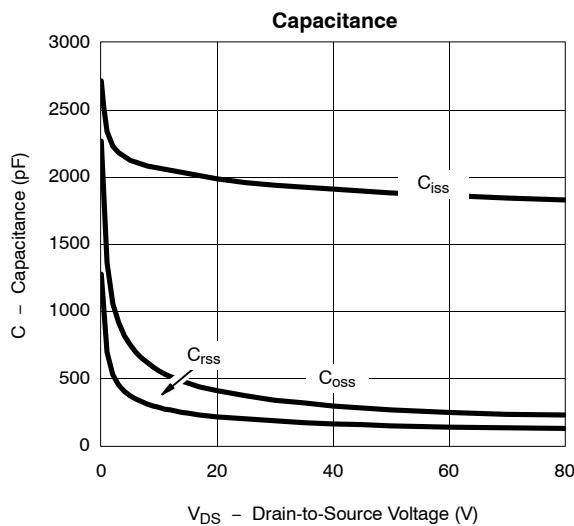
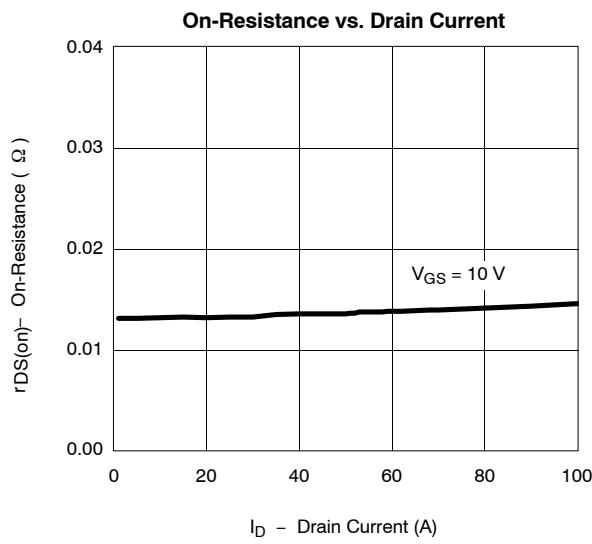
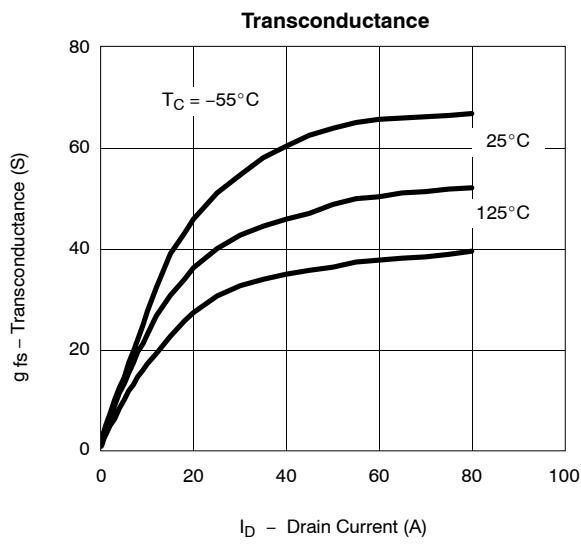
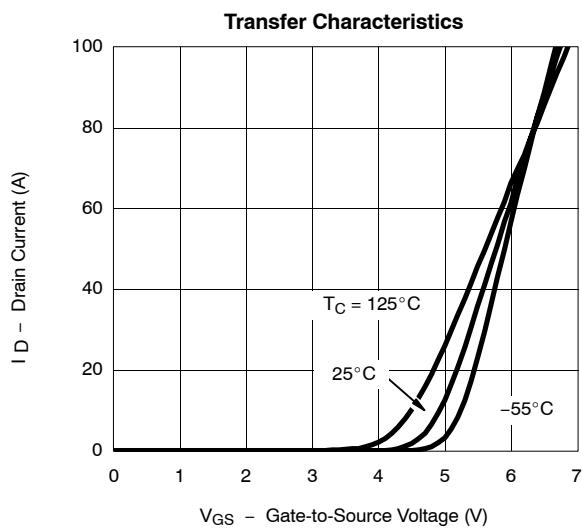
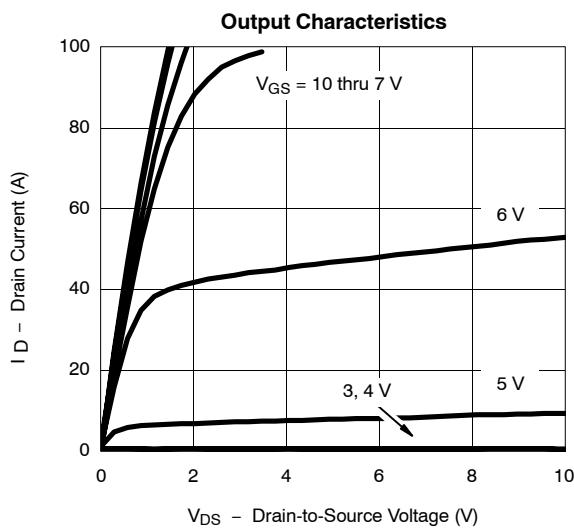
- a. Surface Mounted on 1" x1" FR4 Board.
- b. See SOA curve for voltage derating.

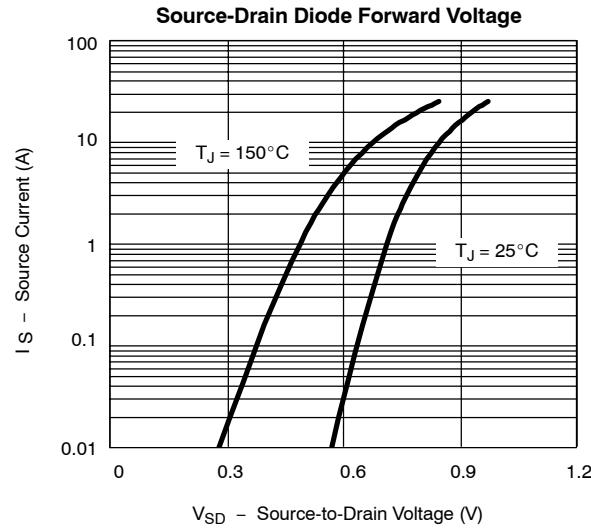
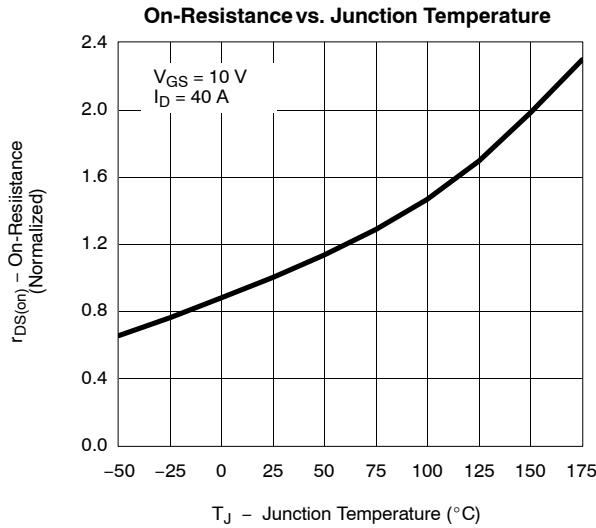
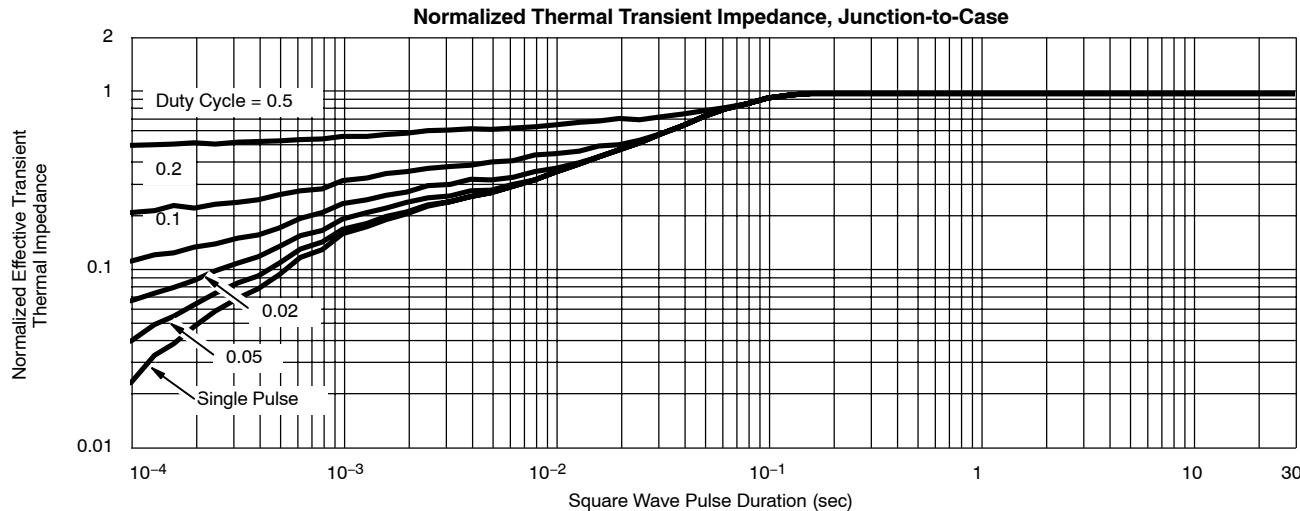
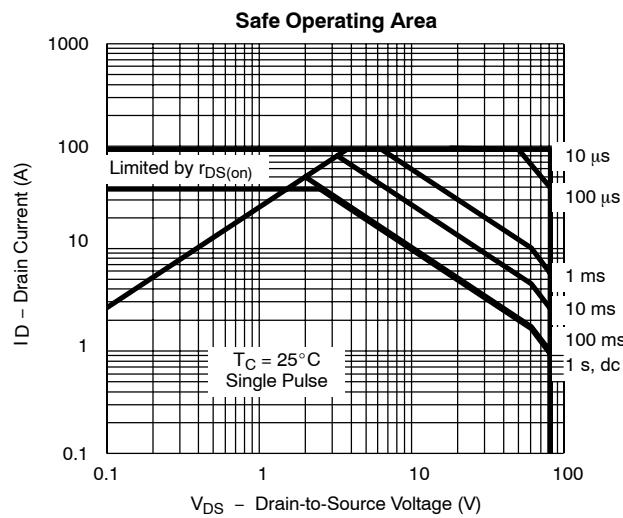
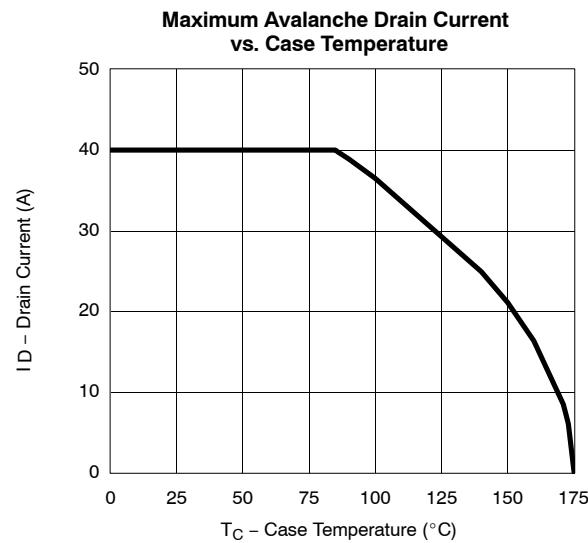
SPECIFICATIONS ($T_J = 25^\circ\text{C}$ UNLESS OTHERWISE NOTED)

Parameter	Symbol	Test Condition	Min	Typ ^a	Max	Unit
Static						
Drain-Source Breakdown Voltage	$V_{(\text{BR})\text{DSS}}$	$V_{\text{GS}} = 0 \text{ V}, I_D = 250 \mu\text{A}$	80			V
Gate Threshold Voltage	$V_{\text{GS}(\text{th})}$	$V_{\text{DS}} = V_{\text{GS}}, I_D = 250 \mu\text{A}$	2.0		4.0	
Gate-Body Leakage	I_{GSS}	$V_{\text{DS}} = 0 \text{ V}, V_{\text{GS}} = \pm 20 \text{ V}$			± 100	nA
Zero Gate Voltage Drain Current	I_{DSS}	$V_{\text{DS}} = 80 \text{ V}, V_{\text{GS}} = 0 \text{ V}$			1	μA
		$V_{\text{DS}} = 80 \text{ V}, V_{\text{GS}} = 0 \text{ V}, T_J = 125^\circ\text{C}$			50	
		$V_{\text{DS}} = 80 \text{ V}, V_{\text{GS}} = 0 \text{ V}, T_J = 175^\circ\text{C}$			250	
On-State Drain Current ^b	$I_{\text{D}(\text{on})}$	$V_{\text{DS}} = 5 \text{ V}, V_{\text{GS}} = 10 \text{ V}$	60			A
Drain-Source On-State Resistance ^b	$r_{\text{DS}(\text{on})}$	$V_{\text{GS}} = 10 \text{ V}, I_D = 40 \text{ A}$		0.013	0.016	Ω
		$V_{\text{GS}} = 10 \text{ V}, I_D = 40 \text{ A}, T_J = 125^\circ\text{C}$			0.027	
		$V_{\text{GS}} = 10 \text{ V}, I_D = 40 \text{ A}, T_J = 175^\circ\text{C}$			0.037	
Forward Transconductance ^b	g_{fs}	$V_{\text{DS}} = 15 \text{ V}, I_D = 40 \text{ A}$		45		S
Dynamic^a						
Input Capacitance	C_{iss}	$V_{\text{GS}} = 0 \text{ V}, V_{\text{DS}} = 25 \text{ V}, f = 1 \text{ MHz}$		1960		pF
Output Capacitance	C_{oss}			370		
Reverse Transfer Capacitance	C_{rss}			200		
Total Gate Charge ^c	Q_g	$V_{\text{DS}} = 40 \text{ V}, V_{\text{GS}} = 10 \text{ V}, I_D = 40 \text{ A}$		42	60	nC
Gate-Source Charge ^c	Q_{gs}			7		
Gate-Drain Charge ^c	Q_{gd}			13		
Gate Resistance	R_g	$V_{\text{DD}} = 40 \text{ V}, R_L = 1.0 \Omega$ $I_D \approx 40 \text{ A}, V_{\text{GEN}} = 10 \text{ V}, R_g = 2.5 \Omega$	0.5		2.7	Ω
Turn-On Delay Time ^c	$t_{\text{d}(\text{on})}$			12	20	ns
Rise Time ^c	t_r			52	80	
Turn-Off Delay Time ^c	$t_{\text{d}(\text{off})}$			25	38	
Fall Time ^c	t_f			10	15	
Source-Drain Diode Ratings and Characteristic ($T_C = 25^\circ\text{C}$)						
Pulsed Current	I_{SM}				60	A
Diode Forward Voltage ^b	V_{SD}	$I_F = 40 \text{ A}, V_{\text{GS}} = 0 \text{ V}$		1.0	1.5	V
Source-Drain Reverse Recovery Time	t_{rr}	$I_F = 40 \text{ A}, di/dt = 100 \text{ A}/\mu\text{s}$		45	70	ns

Notes

- a. Guaranteed by design, not subject to production testing.
- b. Pulse test; pulse width $\leq 300 \mu\text{s}$, duty cycle $\leq 2\%$.
- c. Independent of operating temperature.

TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)


TYPICAL CHARACTERISTICS (25°C UNLESS NOTED)

THERMAL RATINGS




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