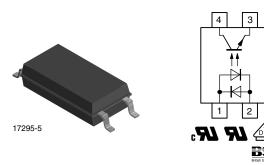


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## Optocoupler, Phototransistor Output, AC Input, SOP-4L, Long Mini-Flat Package



### DESCRIPTION

The TCLT1600 consists of a phototransistor optically coupled to 2 gallium arsenide infrared-emitting diodes in an SOP 4-pin wide body package.

#### AGENCY APPROVALS

- UL1577, file no. E76222
- CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-2 (VDE 0884)/DIN EN 60747-5-5 (pending), available with option 1
- BSI IEC 60950; IEC 60065

### FEATURTES

- Low profile package
- Extra low coupling capacity typical 0.2 pF
- High common mode rejection
- AC input
- Creepage current resistance according to VDE 0303/IEC 60112 comparative tracking index: CTI ≥ 175
- Pb-free e3

- Creepage distance > 8 mm
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### APPLICATIONS

- Switch-mode power supplies
- Line receiver
- Computer peripheral interface
- Microprocessor system interface
- Reinforced isolation provides circuit protection against electrical shock (safety class II)
- Circuits for safe protective separation against electrical shock according to safety class II (reinforced isolation):
   for appl. class I to IV at mains voltage ≤ 300 V
   for appl. class I to III at mains voltage ≤ 600 V
   according to DIN EN 60747-5-2 (VDE 0884)

ORDERING INFORMATIONS										
Т		С	L	Т	1	6	0	0	SOP-4L	
				PART N	UMBER				▲ 10.2 mm	
AGENCY CERTIFIED/PACKAGE								CTR (	%)	
UL, cUL, VDE, BSI							80 to 300			
SOP-4L, miniflat, long							TCLT1600			

RoHS COMPLIANT HALOGEN

HALOGEN FREE <u>GREEN</u> (5-2008)



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**TCLT1600** 

<b>ABSOLUTE MAXIMUM RATINGS</b> ( $T_{amb} = 25 \degree C$ , unless otherwise specified)										
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT						
INPUT										
Forward current		I <sub>F</sub>	± 60	mA						
Forward surge current	$t_p \le 10 \ \mu s$	I <sub>FSM</sub>	± 1.5	А						
Power dissipation		P <sub>diss</sub>	100	mW						
Junction temperature		Tj	125	°C						
OUTPUT	OUTPUT									
Collector emitter voltage		V <sub>CEO</sub>	70	V						
Emitter collector voltage		V <sub>ECO</sub>	7	V						
Collector current		Ι <sub>C</sub>	50	mA						
Collector peak current	$t_p/T=0.5,t_p\leq 10\;ms$	I <sub>CM</sub>	100	mA						
Power dissipation		P <sub>diss</sub>	150	mW						
Junction temperature		Tj	125	°C						
COUPLER										
Total power dissipation		P <sub>tot</sub>	250	mW						
Operating ambient temperature range		T <sub>amb</sub>	-55 to +100	°C						
Storage temperature range		T <sub>stg</sub>	-55 to +125	°C						
Soldering temperature <sup>(1)</sup>		T <sub>sld</sub>	260	°C						

Notes

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability.

<sup>(1)</sup> Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (<u>www.vishay.com/doc?80054</u>).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
INPUT									
Forward voltage	$I_F = \pm 50 \text{ mA}$	V <sub>F</sub>	-	1.25	1.6	V			
Junction capacitance	$V_R = 0 V, f = 1 MHz$	Cj	-	50	-	pF			
OUTPUT									
Collector emitter voltage	I <sub>C</sub> = 1 mA	V <sub>CEO</sub>	70	-	-	V			
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7	-	-	V			
Collector ermitter leakage current	$V_{CE} = 20 \text{ V}, \text{ I}_{F} = 0 \text{ A}$	I <sub>CEO</sub>	-	10	100	nA			
COUPLER									
Collector emitter saturation voltage	$I_{F} = \pm 10 \text{ mA}, I_{C} = 1 \text{ mA}$	V <sub>CEsat</sub>	-	-	0.3	V			
Cut-off frequency	$\label{eq:VCE} \begin{split} V_{CE} &= 5 \text{ V}, \text{ I}_{\text{F}} = \pm \text{ 10 mA}, \\ \text{ R}_{\text{L}} &= \text{ 100 } \Omega \end{split}$	f <sub>c</sub>	-	110	-	kHz			
Coupling capacitance	f = 1 MHz	C <sub>k</sub>	-	0.3	-	pF			

Note

 Minimum and maximum values are tested requierements. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)									
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT			
I <sub>C</sub> /I <sub>F</sub>	$V_{CE} = 5 \text{ V}, \text{ I}_{F} = \pm 5 \text{ mA}$	CTR	80	-	300	%			

2



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**TCLT1600** 

SAFETY AND INSULATION RATINGS								
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT				
Partial discharge test voltage - routine test	100 %, t <sub>test</sub> = 1 s	V <sub>pd</sub>	2	kV				
Partial discharge test voltage -	t <sub>Tr</sub> = 60 s, t <sub>test</sub> = 10 s,	V <sub>IOTM</sub>	8	kV				
lot test (sample test)	(see figure 2)	V <sub>pd</sub>	1.68	kV				
Isolation test voltage (RMS)		V <sub>ISO</sub>	5000	V <sub>RMS</sub>				
	V <sub>IO</sub> = 500 V	R <sub>IO</sub>	10 <sup>12</sup>	Ω				
Insulation resistance	$V_{IO} = 500 \text{ V}, \text{ T}_{amb} = 100 ^{\circ}\text{C}$	R <sub>IO</sub>	10 <sup>11</sup>	Ω				
	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 150 °C (construction test only)	R <sub>IO</sub>	10 <sup>9</sup>	Ω				
Forward current		I <sub>si</sub>	130	mA				
Power dissipation		P <sub>SO</sub>	265	mW				
Rated impulse voltage		VIOTM	8	kV				
Safety temperature		T <sub>si</sub>	150	°C				
Clearance distance			8.00	mm				
Creepage distance			8.00	mm				
Insulation distance (internal)			0.40	mm				

#### Note

 According to DIN EN 60747-5-2 (VDE 0884) (see figure 2). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.

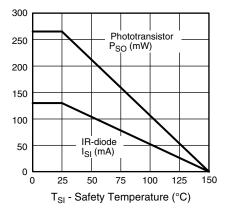


Fig. 1 - Derating Diagram

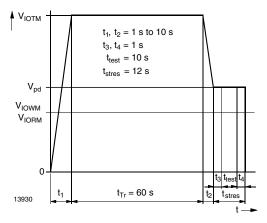


Fig. 2 - Test Pulse Diagram for Sample Test according to DIN EN 60747-5-2; IEC60747-5-5

SWITCHING CHARACTERISTICS ( $T_{amb} = 25 \text{ °C}$ , unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Delay time	$V_{S}$ = 5 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ , (see figure 3)	t <sub>d</sub>	-	3	-	μs		
Rise time	$V_{S}$ = 5 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ , (see figure 3)	t <sub>r</sub>	-	3	-	μs		
Turn-on time	$V_{S}$ = 5 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ , (see figure 3)	t <sub>on</sub>	-	6	-	μs		
Storage time	$V_{S}$ = 5 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ , (see figure 3)	t <sub>s</sub>	-	0.3	-	μs		
Fall time	$V_{S} = 5 \text{ V}, \text{ I}_{C} = 2 \text{ mA}, \text{ R}_{L} = 100 \Omega,$ (see figure 3)	t <sub>f</sub>	-	4.7	-	μs		
Turn-off time	$V_{S}$ = 5 V, I <sub>C</sub> = 2 mA, R <sub>L</sub> = 100 $\Omega$ , (see figure 3)	t <sub>off</sub>	-	5	-	μs		
Turn-on time	$V_S = 5 \text{ V}, \text{ I}_F = 10 \text{ mA}, \text{ R}_L = 1 \text{ k}\Omega,$ (see figure 4)	t <sub>on</sub>	-	9	-	μs		
Turn-off time	$V_{S}$ = 5 V, I <sub>F</sub> = 10 mA, R <sub>L</sub> = 1 k $\Omega$ , (see figure 4)	t <sub>off</sub>	-	10	-	μs		

Rev. 2.5, 13-Oct-15

3

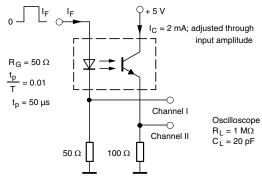
Document Number: 83517

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TCLT1600

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Fig. 3 - Test Circuit, Non-Saturated Operation

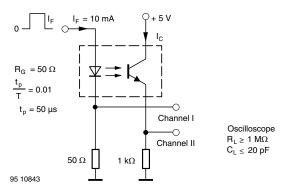
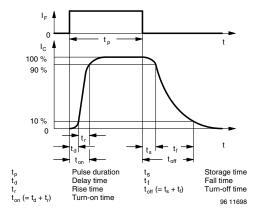


Fig. 4 - Test Circuit, Saturated Operation





Rev. 2.5, 13-Oct-15

4

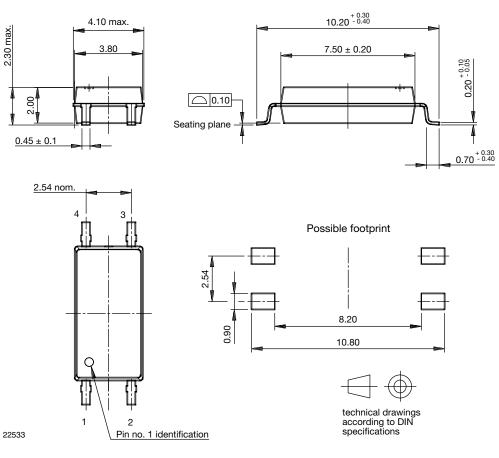
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### **Vishay Semiconductors**

#### **PACKAGE DIMENSIONS** (in millimeters)



#### PACKAGE MARKING



### **TAPE AND REEL DIMENSIONS** (in millimeters)

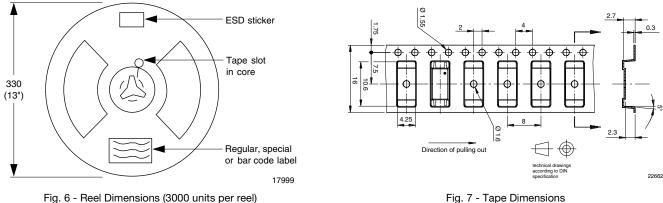


Fig. 7 - Tape Dimensions

5



## **TCLT1600**

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HANDLING AND STORAGE CONDITIONS

Moisture sensitivity level 1, according to J-STD-020

ESD level: HBM class 2

Conditions:  $T_{amb} < 30\ ^\circ C,\ RH < 85\ \%$ 

Floor life: unlimited

#### SOLDER PROFILE

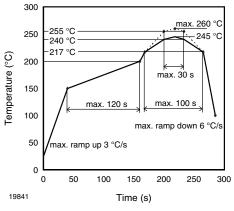


Fig. 8 - Lead (Pb)-free Reflow Solder Profile according to J-STD-020

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