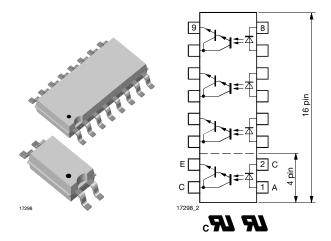


Vishay Semiconductors

# Optocoupler, Photodarlington Output, High Gain, Single/Quad Channel, Half Pitch Mini-Flat Package



#### **DESCRIPTION**

The TCMD1000, TCMD4000 consist of a photodarlington optically coupled to a gallium arsenide infrared-emitting diodes in either a 4 pin or 16 pin miniflat package.

The elements provide a fixed distance between input and output for highest safety requirements.

#### **FEATURES**

- Low profile package (half pitch)
- AC isolation test voltage 3750 V<sub>RMS</sub>
- · Low coupling capacitance of typical 0.3 pF
- Low temperature coefficient of CTR
- Wide ambient temperature range
- Material categorization:
   For definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>





COMPLIANT

GREEN

#### **APPICLATIONS**

- Programmable logic
- Modems
- Answering machines
- · General applications

#### **AGENCY APPROVALS**

- UL1577, file no. E76222 system code M, double protection
- CSA 22.2 bulletin 5A, double protection
- DIN EN 60747-5-5 (VDE 0884-5)
- FIMKO
- BSI

ORDERING	ORDERING INFORMATION								
Т	С	M	D	#	0	0	0	SOP-#	
			PART N	UMBER				<b>7 mm</b> →	
AGENCY CERTIFIED/PACKAGE					CTR (%)				
UL, cUL, FIMKO, BSI, VDE				> 600					
SOP-4	SOP-4					TCMD1000			
SOP-16, quad channel				TCMD4000					

<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER TEST CONDITION SYMBOL VALUE UNIT							
INPUT							
Reverse voltage		V <sub>R</sub>	6	V			
Forward current		I <sub>F</sub>	60	mA			
Forward surge current	t <sub>P</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	А			
Power dissipation		P <sub>diss</sub>	100	mW			
Junction temperature		Tj	125	°C			



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ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
OUTPUT							
Collector emitter voltage		V <sub>CEO</sub>	35	V			
Emitter collector voltage		V <sub>ECO</sub>	7	V			
Collector current		I <sub>C</sub>	80	mA			
Collector peak current	$t_P/T = 0.5, t_P \le 10 \text{ ms}$	Ісм	100	mA			
Power dissipation		P <sub>diss</sub>	150	mW			
Junction temperature		T <sub>j</sub>	125	°C			
COUPLER							
AC isolation test voltage (RMS)		V <sub>ISO</sub> (1)	3750	$V_{RMS}$			
Total power dissipation		P <sub>tot</sub>	250	mW			
Operating ambient temperature range		T <sub>amb</sub>	- 40 to + 100	°C			
Storage temperature range	Storage temperature range		- 40 to + 125	°C			
Soldering temperature (2)		T <sub>sld</sub>	260	°C			

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Related to standard climate 23/50 DIN 50014.
- (2) Wave soldering three cycles are allowed. Also refer to "Assembly Instruction" (www.vishay.com/doc?80054).

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
input							
Forward voltage	$I_F = 50 \text{ mA}$	V <sub>F</sub>		1.25	1.6	V	
Junction capacitance	V <sub>R</sub> = 0 V, f = 1 MHz	C <sub>j</sub>		50		pF	
output							
Collector emitter voltage	I <sub>C</sub> = 100 μA	$V_{CEO}$	35			V	
Emitter collector voltage	I <sub>E</sub> = 100 μA	V <sub>ECO</sub>	7			V	
Collector dark current	$V_{CE} = 10 \text{ V}, I_F = 0, E = 0$	I <sub>CEO</sub>			100	nA	
coupler							
Collector emitter saturation voltage	$I_F = 20 \text{ mA}, I_C = 5 \text{ mA}$	V <sub>CEsat</sub>			1	V	
Cut-off frequency	$I_{F} = 10 \text{ mA, } V_{CE} = 5 \text{ V,}$ $R_{L} = 100 \Omega$	f <sub>c</sub>		10		kHz	
Coupling capacitance	f = 1 MHz	C <sub>k</sub>		0.3		pF	

#### Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>	$V_{CF} = 2 \text{ V}, I_{F} = 1 \text{ mA}$	TCMD1000	CTR 600 800		%		
	v <sub>CE</sub> = 2 v, i <sub>F</sub> = 1 IIIA	TCMD4000	CTR	600	800		%

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Rise time	$V_{CE}$ = 2 V, $I_{C}$ = 10 mA, $R_{L}$ = 100 $\Omega$ (see figure 1)	t <sub>r</sub>		300		μs	
Turn-off time	$V_{CE}$ = 2 V, $I_{C}$ = 10 mA, $R_{L}$ = 100 $\Omega$ (see figure 1)	t <sub>off</sub>		250		μs	

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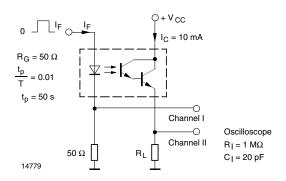


Fig. 1 - Test Circuit, Non-Saturated Operation

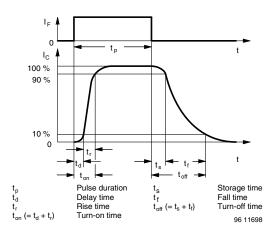


Fig. 2 - Switching Times

SAFETY AND INSULATION RATINGS						
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT
Climatic classification	IEC 68 part 1			40/110/21		
Comparative tracking index		CTI	175		399	
V <sub>IOTM</sub>			6000			V
V <sub>IORM</sub>			707			V
P <sub>SO</sub>					265	mW
I <sub>SI</sub>					130	mA
T <sub>SI</sub>					150	°C
Creepage distance			5			mm
Clearance distance			5			mm
Insulation thickness, reinforce rated	per IEC 60950 2.10.5.1		0.4			mm

#### Note

• As per IEC 60747-5-2, § 7.4.3.8.1, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

#### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

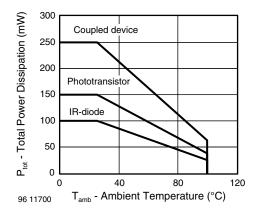


Fig. 3 - Forward Voltage vs. Ambient Temperature

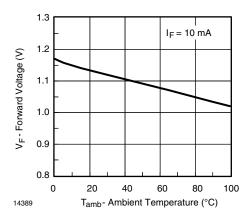


Fig. 4 - Forward Voltage vs. Ambient Temperature



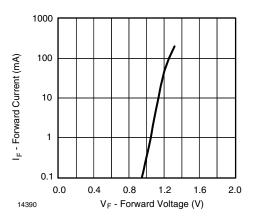


Fig. 5 - Forward Current vs. Forward Voltage

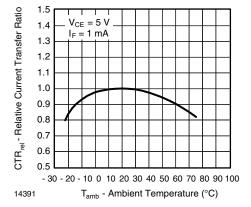


Fig. 6 - Relative Current Transfer Ratio vs. Ambient Temperature

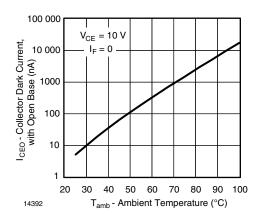


Fig. 7 - Collector Dark Current vs. Ambient Temperature

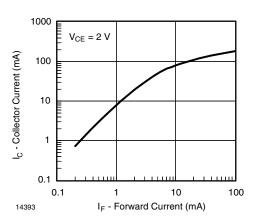


Fig. 8 - Collector Current vs. Forward Current

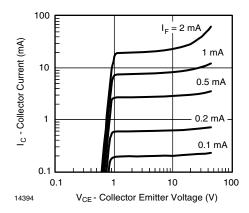


Fig. 9 - Collector Current vs. Collector Emitter Voltage

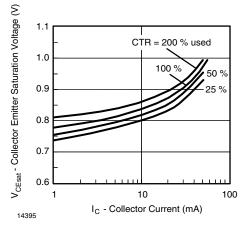


Fig. 10 - Collector Emitter Saturation Voltage vs. Collector Current



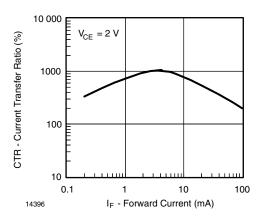
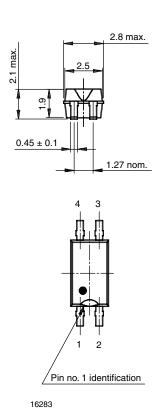
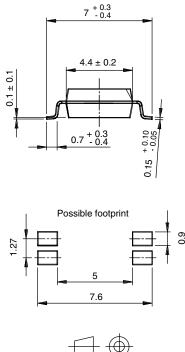


Fig. 11 - Current Transfer Ratio vs. Forward Current

#### **PACKAGE DIMENSIONS** in millimeters

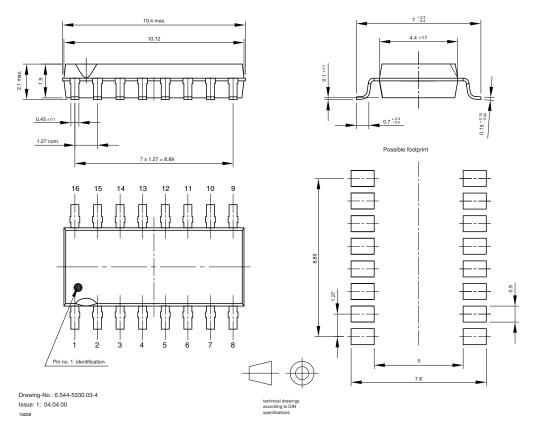






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#### **PACKAGE MARKING**





# **Footprint and Schematic Information**

Vishay Semiconductors

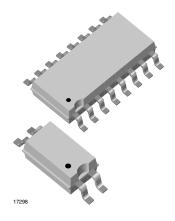
# Footprint and Schematic Information for TCMD1000, TCMD4000

The footprint and schematic symbols for the following parts can be accessed using the associated links. They are available in Eagle, Altium, KiCad, OrCAD / Allegro, Pulsonix, and PADS.

Note that the 3D models for these parts can be found on the Vishay product page.

PART NUMBER	FOOTPRINT / SCHEMATIC
TCMD1000	www.snapeda.com/parts/TCMD1000/Vishay/view-part
TCMD4000	www.snapeda.com/parts/TCMD4000/Vishay/view-part

For technical issues and product support, please contact optocoupleranswers@vishav.com.





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