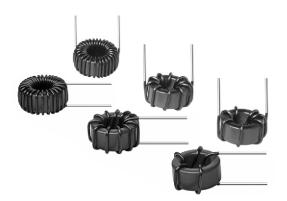


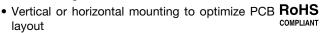


Toroid, High Current, High Temperature, Radial Leaded



FEATURES

- · Printed circuit mounting
- Toroid design reduces EMI



- High temperature rating of 200 °C no aging
- Material categorization: For definitions please see <u>www.vishay.com/doc?99912</u>

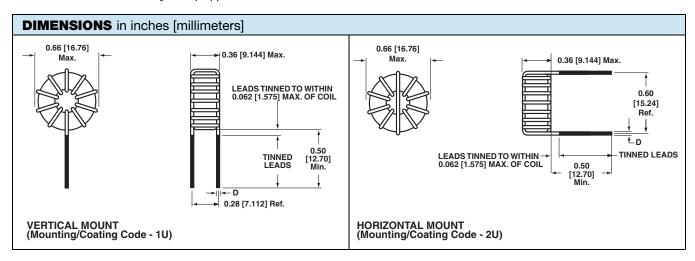
APPLICATIONS

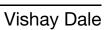
- Switching power supplies
- · EMI/RFI filtering
- · Output chokes

STANI	STANDARD ELECTRICAL SPECIFICATIONS in inches [millimeters]									
		DCR (VERTICAL MOUNT)		DCR (HORIZONTAL MOUNT)		RATED CURRENT VERTICAL	RATED CURRENT HORIZONTAL	SATURATION	LEAD	
IND. L₀ (µH)	TOLERANCE (%)	TYP. (Ω)	MAX. (Ω)	TYP. (Ω)	MAX. (Ω)	MOUNT (A) ⁽¹⁾	MOUNT (A) ⁽¹⁾	CURRENT (A) (2)	DIAMETER D	
0.39	20	0.0014	0.0016	0.0018	0.002	32.0	28.0	23	0.053 [1.346]	
1.2	20	0.002	0.0023	0.0025	0.0028	25.5	22.5	12.5	0.053 [1.346]	
1.5	20	0.0023	0.0026	0.0028	0.003	23.25	21.0	10.5	0.053 [1.346]	
4.7	20	0.0064	0.0072	0.0072	0.008	11.9	11.25	5.9	0.042 [1.067]	
10	20	0.0132	0.0145	0.015	0.0164	7.25	7.0	4.2	0.034 [0.864]	
15	20	0.021	0.023	0.022	0.024	5.6	5.5	3.4	0.031 [0.787]	
22	20	0.024	0.027	0.026	0.029	5.2	5.0	2.5	0.031 [0.787]	
39	20	0.048	0.050	0.050	0.055	3.3	3.3	1.9	0.025 [0.635]	
68	20	0.080	0.086	0.082	0.090	2.5	2.5	1.4	0.022 [0.559]	
100	20	0.099	0.108	0.106	0.118	2.25	2.25	1.15	0.022 [0.559]	

Notes

- Operating temperature (ambient + ΔT): 55 °C to + 200 °C, inductance tested at 0.25 V_{RMS}, 1 kHz, DCR tested at 25 °C ± 5 °C, all material rated at 200 °C
- $^{(1)}\,$ DC current that will cause an approx. ΔT of 40 °C
- $^{(2)}\,$ DC current that will cause L_0 to drop approx. 20 %

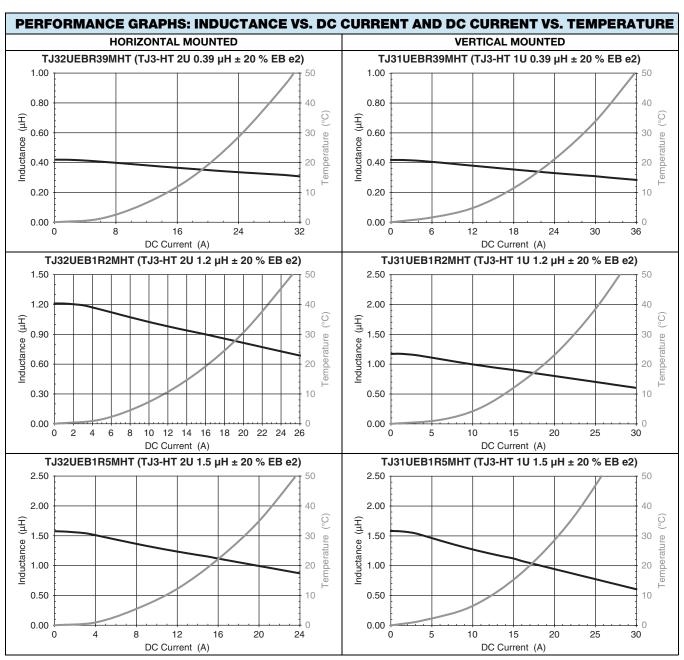




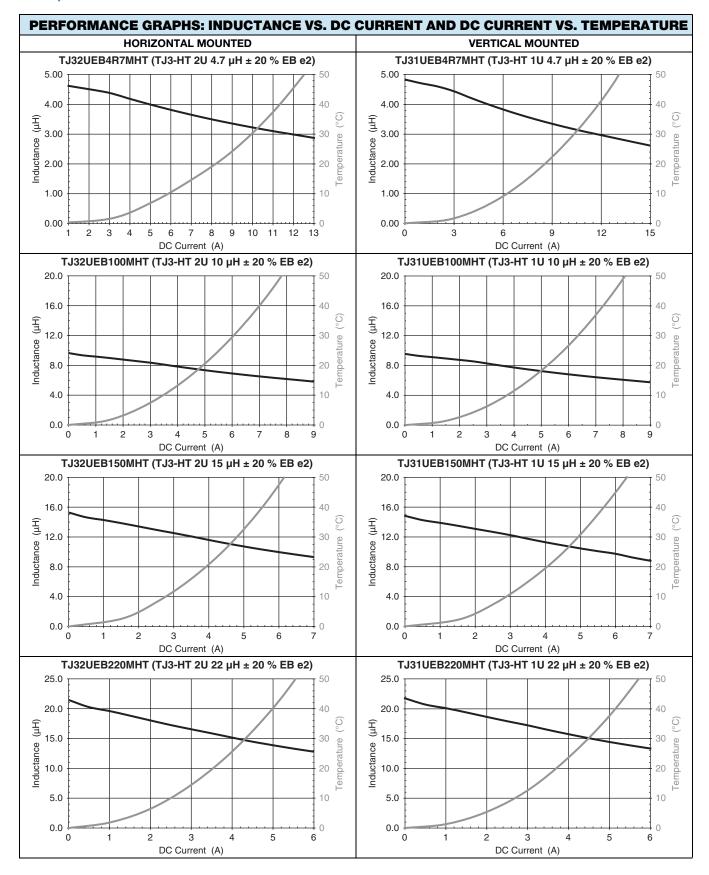


ORDERING INFORMATION								
TJ3-HT	1U	10 μH	± 20 %	EB	e2			
MODEL	MOUNTING/COATING CODE	INDUCTANCE VALUE	INDUCTANCE TOLERANCE	PACKAGE CODE	JEDEC LEAD (Pb)-FREE STANDARD			

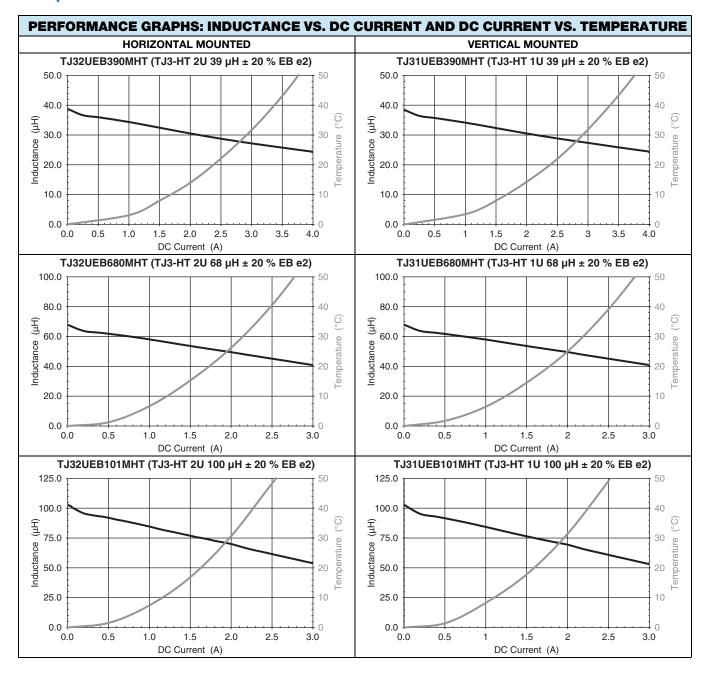
GLOBAL PART NUMBER								
T J 3	MOUNTING/COATING CODE	PACKAGE CODE	1 0 0 INDUCTANCE VALUE	M INDUCTANCE TOLERANCE	H T SERIES			











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Revision: 02-Oct-12 Document Number: 91000

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