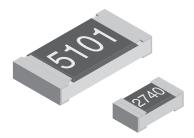


High Stability Thin Film Chip Resistors



TNPW precision thin film flat chip resistors are the perfect choice for most fields of modern electronics where reliability and stability is of major concern. Typical applications include industrial, medical equipment, and those, which are not necessarily subject to the RoHS directive like military and avionics.

FEATURES

- Excellent overall stability at different environmental conditions ≤ 0.05 % (1000 h rated power at 70 °C)
- Low temperature coefficient and tight tolerances (± 0.1 %; ± 10 ppm/K)
- Metal film layer on high quality ceramic
- SnPb termination plating, minimum 6 % Pb

APPLICATIONS

- Avionics
- Military
- Medical equipment
- · Industrial equipment

STANDARD ELECTRICAL SPECIFICATIONS							
	TNPW0402	TNPW0603	TNPW0805	TNPW1206	TNPW1210 (1)	TNPW2010	TNPW2512 (1)
Metric size	RR 1005M	RR 1608M	RR 2012M	RR 3216M	RR 3225M	RR 5025M	RR 6332M
Resistance range	10 Ω to 100 k Ω	10 Ω to 332 k Ω	10 Ω to 1 MΩ	10 Ω to 2 M Ω	10 Ω to 3.01 M Ω	10 Ω to 4.99 M Ω	10 Ω to 8.87 MΩ
Resistance tolerance	± 1 %; ± 0.5 %; ± 0.1 %						
Temperature coefficient	=	± 50 ppm/K; ± 25	5 ppm/K; ± 15 pp	m/K; ± 10 ppm/ł	<	± 50 ppm/K;	± 25 ppm/K
Rated dissipation, $P_{70}^{(2)}$	0.063 W	0.1 W	0.125 W	0.25 W	0.33 W	0.4 W	0.5 W
Operating voltage, <i>U</i> _{max.} AC/DC	50 V	75 V	150 V	200 V	200 V	300 V	300 V
Permissible film temperature, $g_{\rm F}$ max.	125 °C (155 °C)						
Operating temperature range	- 55 °C to 125 °C (155 °C)						
Thermal resistance (3)	870 K/W	550 K/W	440 K/W	220 K/W	170 K/W	140 K/W	110 K/W
Insulation voltage:							
U _{ins} 1 min	75 V	100 V	200 V	300 V	300 V	300 V	300 V
continuous	75 V	75 V	75 V	75 V	75 V	75 V	75 V
Failure rate: FIT _{observed}	≤ 0.3 x 10 ⁻⁹ /h	≤ 0.3 x 10 ⁻⁹ /h	≤ 0.3 x 10 ⁻⁹ /h	≤ 0.3 x 10 ⁻⁹ /h	≤ 0.3 x 10 ⁻⁹ /h	≤ 0.3 x 10 ⁻⁹ /h	≤ 0.3 x 10 ⁻⁹ /h

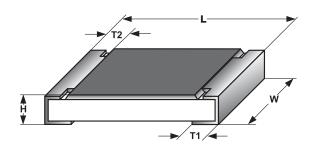
Notes

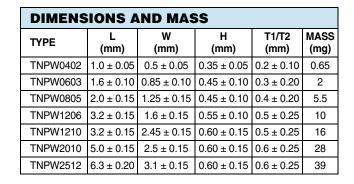
- TNPW 0402 without marking.
- (1) Size not specified in EN 140401-801.
- (2) Rated voltage $\sqrt{P \times R}$. The power dissipation on the resistor generates a temperature rise against the local ambient, depending on the heat flow support of the printed-circuit board (thermal resistance). The rated dissipation applies only if the permitted film temperature is not exceeded.
- (3) Measuring conditions in accordance with EN 140401-801.

High Stability Thin Film Chip Resistors



DIMENSIONS







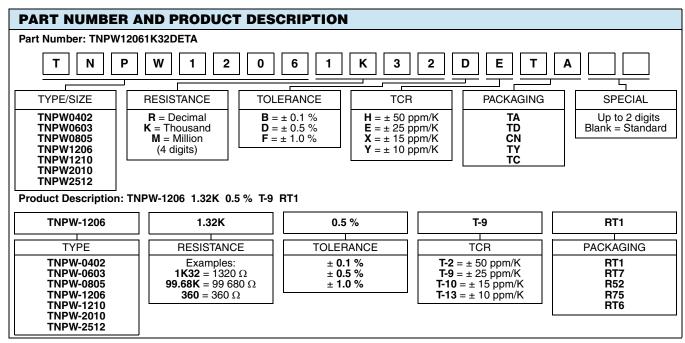
SOLDER PAD DIMENSIONS								
	REFLO	W SOLD	ERING	WAVE SOLDERING				
TYPE	a (mm)	b (mm)	l (mm)	a (mm)	b (mm)	l (mm)		
TNPW0402	0.4	0.6	0.5	-	-	-		
TNPW0603	0.5	0.9	1.0	0.9	0.9	1.0		
TNPW0805	0.7	1.3	1.2	0.9	1.3	1.3		
TNPW1206	0.9	1.7	2.0	1.1	1.7	2.3		
TNPW1210	0.9	2.5	2.0	1.1	2.5	2.3		
TNPW2010	1.0	2.5	3.9	1.2	2.5	3.9		
TNPW2512	1.0	3.2	5.2	1.2	3.2	5.2		

TYPE	TCR	TOLERANCE	RESISTANCE	E-SERIES	
		± 1 %	10R to 100K	24; 96	
	± 50 ppm/K	± 0.5 %	10R to 100K	24; 192	
		± 0.1 %	47R to 100K		
TNPW0402		± 1 %	10R to 100K	24; 96	
TNP WU4U2	± 25 ppm/K	± 0.5 %	10R to 100K		
		± 0.1 %		04.400	
	± 15 ppm/K	± 0.1 %	47R to 100K	24; 192	
	± 10 ppm/K	± 0.1 %		l	
		± 1 %	10R to 332K	24; 96	
	± 50 ppm/K	± 0.5 %	10R to 332K	24; 192	
		± 0.1 %	10H 10 332K		
TNPW0603		± 1 %	10R to 332K	24; 96	
THE WOODS	± 25 ppm/K	± 0.5 %	10R to 332K	- 24; 192	
		± 0.1 %	10H 10 332K		
	± 15 ppm/K	± 0.1 %	47R to 332K		
	± 10 ppm/K	± 0.1 %	47 h 10 332 h		
		± 1 %	10R to 1M0	24; 96	
	± 50 ppm/K	± 0.5 %	10R to 1M0	24; 192	
		± 0.1 %	TOTA TO TIVIO		
TNPW0805		± 1 %	10R to 1M0	24; 96	
TNPW0805	± 25 ppm/K	± 0.5 %	10R to 1M0	24; 192	
		± 0.1 %	TOTA TO TIVIO		
	± 15 ppm/K	± 0.1 %	47R to 1M0		
	± 10 ppm/K	± 0.1 %	4/ n to 1100		



High Stability Thin Film Chip Resistors

TYPE	TCR	TOLERANCE	RESISTANCE	E-SERIES	
		± 1 %	10R to 2M0	24; 96	
	± 50 ppm/K	± 0.5 %	10R to 2M0	24; 192	
		± 0.1 %	TOH TO ZIVIO	24, 192	
TNPW1206		± 1 %	10R to 2M0	24; 96	
1141 44 1200	± 25 ppm/K	± 0.5 %	10R to 2M0		
		± 0.1 %	TOTA TO ZIVIO	- 24; 192	
	± 15 ppm/K	± 0.1 %	47R to 2M0		
	± 10 ppm/K	± 0.1 %			
		± 1 %	10R to 3M01	24; 96	
	± 50 ppm/K	± 0.5 %	10R to 3M01	24; 192	
		± 0.1 %	47R to 2M13		
TNPW1210		± 1 %	10R to 3M01	24; 96	
11VI VV 1210	± 25 ppm/K	± 0.5 %	10R to 3M01	24; 192	
		± 0.1 %			
	± 15 ppm/K	± 0.1 %	47R to 2M13		
	± 10 ppm/K	± 0.1 %			
		± 1 %	10R to 4M99	24; 96	
	± 50 ppm/K	± 0.5 %	10R to 4M99 24;		
TNPW2010		± 0.1 %	47R to 1M0		
		± 1 %	10R to 4M99	24; 96	
	± 25 ppm/K	± 0.5 %	10R to 4M99	24; 192	
		± 0.1 %	47R to 1M0		
TNPW2512		± 1 %	10R to 8M87	24; 96	
	± 50 ppm/K	± 0.5 %	10R to 8M87	24; 192	
		± 0.1 %	47R to 1M0		
		± 1 %	10R to 8M87	24; 96	
	± 25 ppm/K	± 0.5 %	10R to 8M87	24; 192	
		± 0.1 %	47R to 1M0	,	



- The product can be ordered using either the PART NUMBER or the PRODUCT DESCRIPTION.
- For ordering TNPW e3 with lead free terminations please refer to latest edition of datasheet TNPW e3, document number 28758.

Document Number: 31006 Revision: 31-Jan-12

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High Stability Thin Film Chip Resistors



PACKAGING								
TYPE	CODE	QUANTITY	CARRIER TAPE	WIDTH	PITCH	REEL DIAMETER		
TNPW0402	RT7 = TD	10 000		8 mm	2 mm	180 mm/7"		
TNPW0603 TNPW0805 TNPW1206 TNPW1210	R52 = CN	1000 (1)	Paper tape acc. IEC 60286-3	8 mm	4 mm			
	RT1 = TA	5000	Type I					
	RT6 = TC	20 000				330 mm/13"		
TNPW2010 TNPW2512	R75 = TY	1000	Blister tape acc. IEC 60286-3 Type II	12 mm	4 mm	180 mm/7"		

Note

DESCRIPTION

Production is strictly controlled and follows an extensive set instructions established for reproducibility. homogeneous film of metal alloy is deposited on a high grade ceramic substrate and conditioned to achieve the desired temperature coefficient. A special laser is used to achieve the target value by smoothly cutting a meander groove in the resistive layer without damaging the ceramics. A further conditioning is applied in order to stabilize the trimming result. The resistor elements are covered by a protective coating designed for electrical, mechanical and climatic protection. The result of the determined production is verified by an extensive testing procedure on 100 % of the individual chip resistors. This includes pulse load screening for the elimination of products with a potential risk of early life failures according to EN 140401-801, 2.1.2.2. Only accepted products are laid directly into the tape in accordance with EN 60286-3.

ASSEMBLY

The resistors are suitable for processing on automatic SMD assembly systems. They are suitable for automatic soldering using wave, reflow or vapour phase as shown in IEC 61760-1. The encapsulation is resistant to all cleaning solvents commonly used in the electronics industry, including alcohols, esters and aqueous solutions. The suitability of conformal coatings, if applied, shall be qualified by appropriate means to ensure the long-term stability of the whole system. Solderability is specified for 2 years after production or re-qualification. The permitted storage time is 20 years.

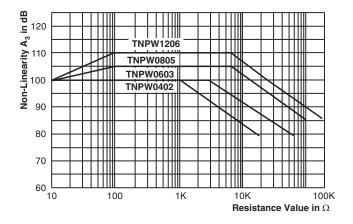
RELATED PRODUCTS

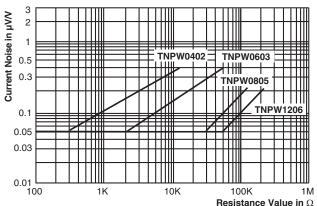
The lead free version TNPW e3 can be used as drop in replacement for the TNPW whenever lead bearing terminations are not mandatory. For ordering TNPW e3 please refer to latest edition of datasheet TNPW e3. document number 28758.

TNPS ESCC high-reliability thin film chip resistors are the premium choice for design and manufacture of equipment, where matured technology and proven reliability are of utmost importance. They are regularly used communication and research satellites and fit equally well into aircraft and military electronic systems. Approval of the TNPS ESCC products is granted by the European Space Components Coordination and registered in the ESCC Qualified Parts List, REP005, document number 28789.

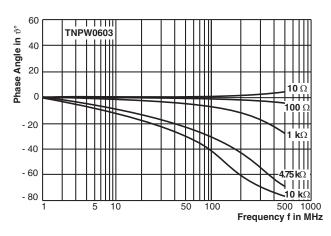
^{(1) 1000} pieces packaging is available only for precision resistors with tolerance ± 0.1 % and temperature coefficient ≤ ± 25 ppm/K.



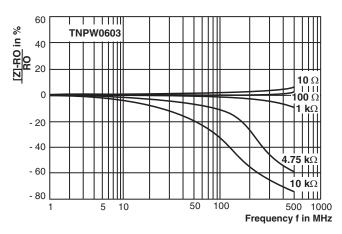




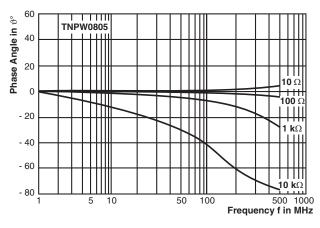
Non-Linearity



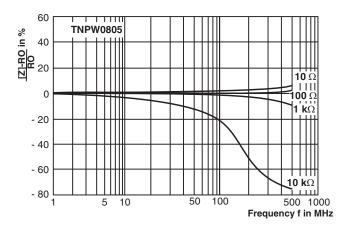
Current Noise



HF Performance



HF Performance



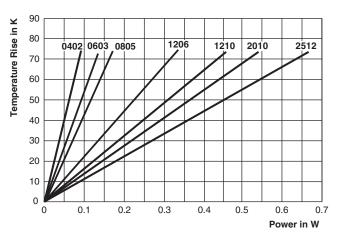
HF Performance

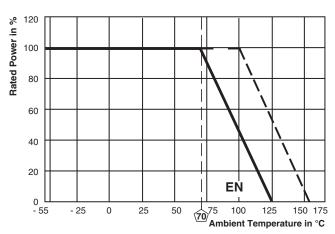
HF Performance

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High Stability Thin Film Chip Resistors

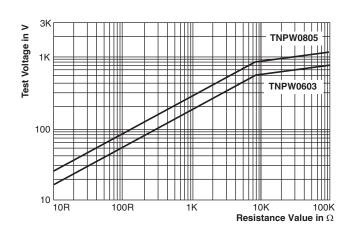


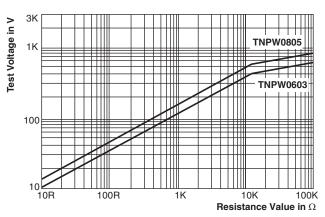




Temperature Rise

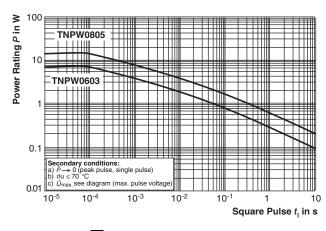
Derating

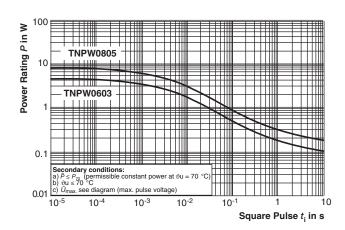




Single-Pulse High Voltage Overload Test 1.2/50 µs EN 140000 4.27

Single-Pulse High Voltage Overload Test 10/700 µs EN 140000 4.27



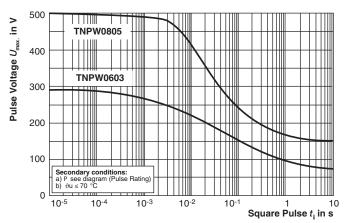


Pulse Rating $\overline{P} \rightarrow 0$

Pulse Rating $\overline{P} \le P_{70}$







Maximum Pulse Voltage

TEST AND REQUIREMENTS

All tests are carried out in accordance with the following specifications:

IEC 60115-1, generic specification (includes tests)

EN 140400, sectional specification (includes schedule for qualification approval)

EN 140401-801, detail specification (includes schedule for conformance inspection)

The testing also covers most of the requirements specified by EIA/IS-703 and JIS-C-5202. The tests are carried out under standard atmospheric conditions in accordance with IEC 60068-1, 5.3. Climatic category LCT/UCT/56 (rated temperature range: Lower category temperature, upper category temperature; damp heat, long term, 56 days) is valid. Unless otherwise specified the following values apply:

Temperature: 15 °C to 35 °C Relative humidity: 45 % to 75 %

Air pressure: 86 kPa to 106 kPa (860 mbar to 1060 mbar).

The components are mounted for testing on boards in accordance with EN 60115-1, 4.31 unless otherwise specified. The parameters stated in the Test Procedures and Requirements table are based on the required tests and permitted limits of EN 140401-801.

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High Stability Thin Film Chip Resistors



TEST PROCEDURES AND REQUIREMENTS								
EN 60115-1 CLAUSE	IEC 60068-2 TEST METHOD	TEST	PROCEDURE	REQUIREMENTS PERMISSIBLE CHANGE (ΔR)				
			Stability for product types:					
			TNPW0402	1	100 Ω to 100 k Ω	10 Ω to 100 k Ω		
			TNPW0603		100 Ω to 332 kΩ	10 Ω to 332 k Ω		
			TNPW0805		100 Ω to 1 MΩ	10 Ω to 1 M Ω		
			TNPW1206	10 Ω to < 100 Ω	100 Ω to 2 M Ω	10 Ω to 2 M Ω		
			TNPW1210]	100 Ω to 3.01 MΩ	10 Ω to 3.01 M Ω		
			TNPW2010]	100 Ω to 4.99 MΩ	10 Ω to 4.99 MΩ		
			TNPW2512]	100 Ω to 8.87 MΩ	10 Ω to 8.87 MΩ		
4.5	-	Resistance		± 0.1 %		± 0.5 %; ± 1.0 %		
4.8.4.2	-	Temperature coefficient	At (20/- 55/20) °C and (20/125/20) °C	± 50 ppm/K; :	K; ± 10 ppm/K			
4.25.1	-	Endurance at 70 °C	$U = \sqrt{P_{70} \times R}$ or \le U _{max} ; 1.5 h on; 0.5 h off; 70 °C; 1000 h	± (0.1 % R + 0.02 Ω)	± (0.05 % R + 0.01 Ω)	± (0.25 % R + 0.05 Ω)		
4.25.3	-	Endurance at upper category temperature	125 °C; 1000 h	± (0.1 % R + 0.02 Ω)	± (0.05 % R + 0.01 Ω)	± (0.5 % R + 0.05 Ω)		
4.13	-	Short time overload	$U = 2.5 \text{ x } \sqrt{P_{70} \text{ x } R}$ $\leq 2 \text{ x } U_{\text{max}}; 5 \text{ s}$	± (0.05 % R + 0.01 Ω)	± (0.02 % R + 0.01 Ω)	± (0.1 % R + 0.02 Ω)		
4.19	14 (Na)	Rapid change of temperature	30 min at LCT: 30 min at UCT; LCT = - 55 °C; UCT = 125 °C; 5 cycles	± (0.05 % R + 0.01 Ω)	± (0.02 % R + 0.01 Ω)	± (0.1 % R + 0.02 Ω)		
4.24	78 (Cab)	Damp heat steady state	(40 ± 2) °C; 56 days; (93 ± 3) % RH	± (0.1 % R + 0.02 Ω)	± (0.05 % R + 0.01 Ω)	± (0.5 % R + 0.05 Ω)		
4.18.2	58 (Td)	Resistance to soldering heat	Solder bath method; (260 ± 5) °C; (10 ± 1) s	± (0.05 % R + 0.01 Ω)	± (0.02 % R + 0.01 Ω)	± (0.1 % R + 0.02 Ω)		



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Revision: 02-Oct-12 Document Number: 91000

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AR03BTC3003 AR03BTC3302 AR03BTC3901 AR03BTC4220 AR03BTC4223N AR03BTC5602 AR03BTC5603 AR03BTC5900

AR03BTC7500 AR03BTC9100 AR03BTC9103 AR03BTC9760 AR05BTC0280 AR05BTC1000 AR05BTC1100 AR05BTC1201

AR05BTC1202 AR05BTC1300 AR05BTC14R3 AR05BTC1500 AR05BTC1523 AR05BTC1620 AR05BTC1623

AR05BTC1760 AR05BTC1800 AR05BTC1823
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