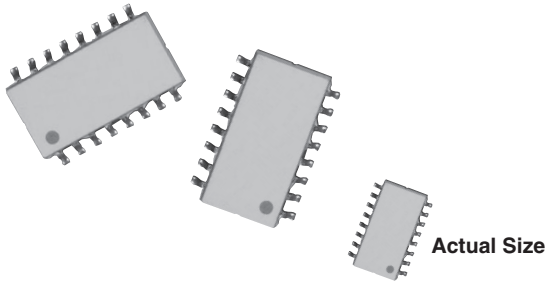
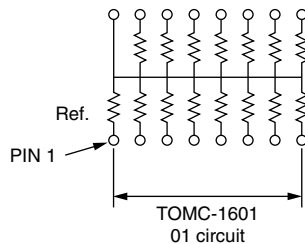


Molded, 50 mil Pitch, Dual-In-Line Thin Film Resistor, Surface Mount Network



Vishay Dale Thin Film offers standard circuits in 16 pins in a medium body molded surface mount package. The networks are available over a resistance range of 100 Ω to 100 k Ω . The network features tight ratio tolerances and close TCR tracking. In addition to the standards shown, custom circuits are available upon request.

SCHEMATIC



The 01 circuit provides 15 nominally equal resistors, each connected between a common lead (16) and a discrete PC board pin.

FEATURES

- 0.090" (2.29 mm) maximum seated height
- Rugged, molded case construction (0.22" wide)
- Highly stable thin film ratio stability ($\Delta R \pm 0.015\%$ at 70 °C for 2000 h)
- Low temperature coefficient, ± 25 ppm/°C (-55 °C to +125 °C)
- Wide resistance range 100 Ω to 100 k Ω
- Isolated/bussed circuits
- Material categorization: For definitions of compliance please see www.vishay.com/doc?99912



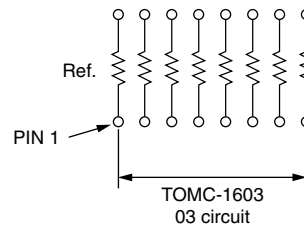
RoHS*
COMPLIANT
HALOGEN
FREE

Note

* Lead (Pb)-containing terminations are not RoHS-compliant. Exemptions may apply.

TYPICAL PERFORMANCE

	ABSOLUTE	TRACKING
TCR	25	5
	ABSOLUTE	RATIO
TOL.	0.1	0.025

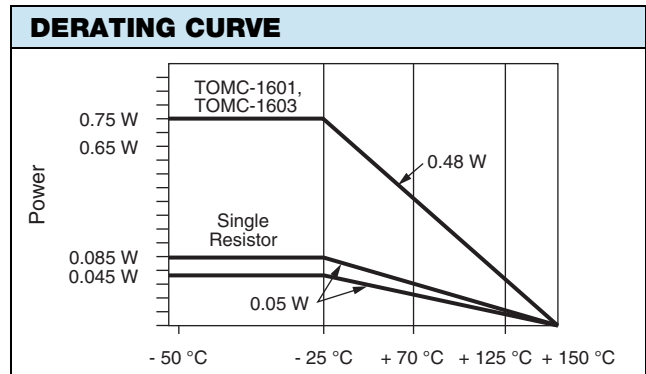


The 03 circuit provides a choice of 8 nominally equal resistors with each resistor isolated from all others and wired directly across.

STANDARD ELECTRICAL SPECIFICATIONS		
TEST	SPECIFICATIONS	CONDITIONS
Material	Passivated nichrome	-
Pin/Lead Number	16	-
Resistance Range	100 Ω to 100 k Ω per resistor	-
TCR: Absolute	± 25 ppm/°C	-55 °C to +125 °C
TCR: Tracking	± 5 ppm/°C	-55 °C to +125 °C
Tolerance: Absolute	$\pm 0.1\%$ to 1%	+25 °C
Tolerance: Ratio	$\pm 0.025\%$ to 0.5%	+25 °C
Power Rating: Resistor	50 mW = PIN 16 common 100 mW = isolated	Maximum at +70 °C
Power Rating: Package	750 mW	Maximum at +70 °C
Stability: Absolute	$\Delta R \pm 0.05\%$	2000 h at +70 °C
Stability: Ratio	$\Delta R \pm 0.015\%$	2000 h at +70 °C
Voltage Coefficient	0.1 ppm/V	-
Working Voltage	100 V max. not to exceed $\sqrt{P \times R}$	-
Operating Temperature Range	-55 °C to +125 °C	-
Storage Temperature Range	-55 °C to +150 °C	-
Noise	< -30 dB	-
Thermal EMF	0.08 μ V/°C	-
Shelf Life Stability: Absolute	$\Delta R \pm 0.01\%$	1 year at +25 °C
Shelf Life Stability: Ratio	$\Delta R \pm 0.002\%$	1 year at +25 °C

DIMENSIONS AND IMPRINTING in inches and millimeters			
	DIMENSION	INCHES	MILLIMETERS
	A	0.350	8.89
	B	0.400	10.16
	C	0.440	11.176
	D	0.050	1.27
	E	0.018	0.457
	F	0.160	4.06
	G	0.08	2.03
	H	0.036	0.914
	J	0.22	5.59
	K	0.244	6.20
	L	0.30	7.52
	M	0.045	1.14
	N	0.003	0.076
	P	0.005	1.27
	Q	0.008	0.203
	R	0.085	2.16
	S	0.003	0.076

MECHANICAL SPECIFICATIONS	
Resistive Element	Passivated nichrome
Substrate Material	Silicon
Body	Molded epoxy
Terminals	Copper alloy
Lead (Pb)-free Option	100 % matte tin
Tin Lead Option	Sn85
Tin Lead and Lead (Pb)-free Finish	Plated



GLOBAL PART NUMBER INFORMATION															
New Global Part Numbering: TOMC16031002BUF															
T	O	M	C	1	6	0	3	1	0	0	2	B	U	F	
T	O	M	C	T	1	6	0	1	1	0	0	3	Z	T	1
GLOBAL MODEL (4 or 5 digits)	PINS	SCHEMATIC	RESISTANCE	TOLERANCE AND RATIO TOLERANCE		PACKAGING									
TOMC (Tin lead)	16	01 = 15 bussed equal resistors	First 3 digits are significant figures and the last digit specifies the number of zeros to follow. Example: 1002 = 10K 1003 = 100K	Abs. Tol.	Ratio	TAPES AND REELS									
TOMCT (Lead (Pb)-free) (e3)		03 = 7 or 8 isolated equal resistors		A = 0.1 % ⁽¹⁾	0.05 %	T0 = 100 min., 100 mult									
				B = 0.1 %	0.1 %	T1 = 1000 min., 1000 mult ⁽³⁾									
				C = 0.25 %	0.1 %	T3 = 300 min., 300 mult									
				D = 0.5 %	0.1 %	T5 = 500 min., 500 mult									
				F = 1 %	0.5 %	TF = Full reel 2000									
				Z = 0.1 % ⁽²⁾	0.025 %	TS = 100 min., 1 mult									
						UF = TUBED									
Historical Part Number example: TOMC16011002Z (for reference purposes only)															
TOMC	16	01	1002	Z											
SERIES	NUMBER OF LEADS	SCHEMATIC	RESISTANCE	TOLERANCE AND RATIO TOLERANCE											

Notes
⁽¹⁾ Tolerance available 250 and up
⁽²⁾ Tolerance available 1K and up
⁽³⁾ Preferred packaging code



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