AUTOMOTIVE

RoHS

HALOGEN

FREE



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Vishay General Semiconductor

High Current Density Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier

Ultra Low $V_F = 0.60 \text{ V}$ at $I_F = 6 \text{ A}$



ADDITIONAL RESOURCES



PRIMARY CHARACTERISTICS			
I _{F(AV)}	12.0 A		
V _{RRM}	150 V		
I _{FSM}	200 A		
V _F at I _F = 12.0 A (T _A = 125 °C)	0.66 V		
T _J max.	175 °C		
Package	SMPC (TO-277A)		
Circuit configuration	Single		

FEATURES

- Very low profile typical height of 1.1 mm
- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available
 - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in low voltage high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test

MAXIMUM RATINGS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V12PM15	UNIT	
Device marking code		12M15		
Maximum repetitive peak reverse voltage	V _{RRM}	150	V	
Maximum average forward restified as went (fig. 1)	I _F ⁽¹⁾	12.0		
Maximum average forward rectified current (fig. 1)	I _F ⁽²⁾	4.7	Α	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	200	А	
Operating junction temperature range	T _J ⁽³⁾ -40 to +175		°C	
Storage temperature range	T _{STG}	-55 to +175	°C	

Notes

- (1) Mounted on 30 mm x 30 mm pad areas aluminum PCB
- (2) Free air, mounted on recommended copper pad area
- $^{(3)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J <1/ $R_{\theta JA}$



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage	I _F = 6.0 A	- T _A = 25 °C	V _E ⁽¹⁾	0.75	-	V	
	I _F = 12.0 A			1.00	1.08		
	I _F = 6.0 A	T _A = 125 °C	'	VF ('')	0.60	-	V
	I _F = 12.0 A		25 0	0.66	0.72		
Reverse current	V _R = 100 V	T _A = 25 °C		0.02	-	mA	
	V _R = 100 V T _A = 125 °C	T _A = 125 °C		2.5	-		
Reverse current	V _R = 150 V	T _A = 25 °C	I _R ⁽²⁾	-	0.25	mA	
	$V_R = 130 \text{ V}$ $T_A = 125 \text{ °C}$	T _A = 125 °C	IR (-)	5.0	16] "/A	
Typical junction capacitance	4.0 V, 1 MHz		CJ	860	-	pF	

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

 $\ensuremath{^{(2)}}$ Pulse test: pulse width $\leq 5~\text{ms}$

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V12PM15	UNIT	
Tunical thormal registance	R ₀ JA (1)(2)	75	°C/W	
Typical thermal resistance	R _{0JM} (3)	4		

- (1) The heat generated must be less than the thermal conductivity from junction to ambient: $dP_D/dT_J < 1/R_{\theta JA}$ (2) Free air mounted on recommended copper pad area; thermal resistance $R_{\theta JA}$ junction to ambient
 (3) Mounted on 30 mm x 30 mm aluminum PCB; thermal resistance $R_{\theta JM}$ junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V12PM15-M3/H	0.10	Н	1500	7" diameter plastic tape and reel	
V12PM15-M3/I	0.10	I	6500	13" diameter plastic tape and reel	
V12PM15HM3/H (1)	0.10	Н	1500	7" diameter plastic tape and reel	
V12PM15HM3/I (1)	0.10	I	6500	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified



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RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)

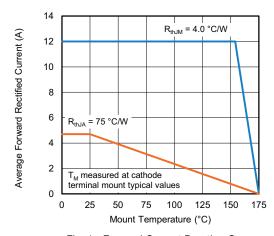


Fig. 1 - Forward Current Derating Curve

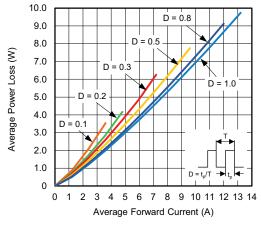


Fig. 2 - Forward Power Loss Characteristics

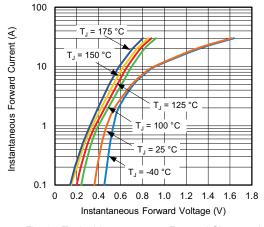


Fig. 3 - Typical Instantaneous Forward Characteristics

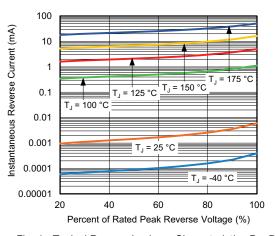


Fig. 4 - Typical Reverse Leakage Characteristics Per Diode

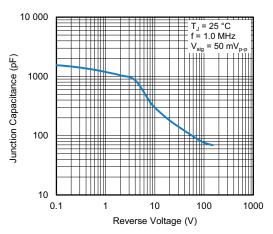


Fig. 5 - Typical Junction Capacitance

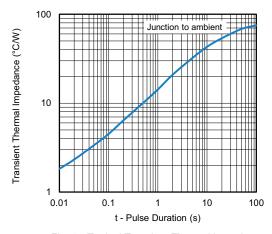


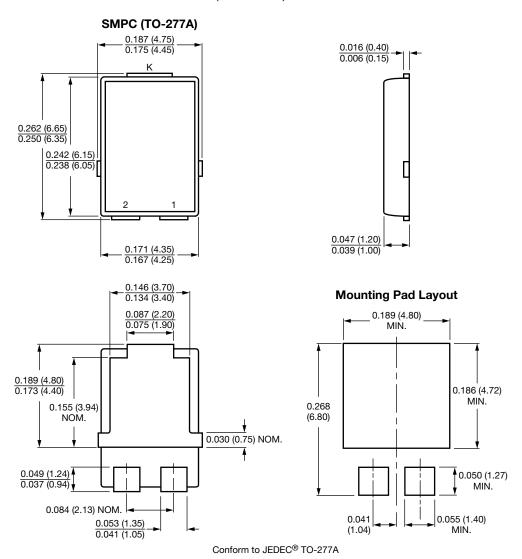
Fig. 6 - Typical Transient Thermal Impedance



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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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