

Vishay General Semiconductor

Dual High Voltage TMBS® (Trench MOS Barrier Schottky) Rectifier





PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10.0 A			
V _{RRM}	170 V			
I _{FSM}	150 A			
V _F at I _F = 10.0 A (T _A = 125 °C)	0.68 V			
T _J max.	175 °C			
Package	SMPD (TO-263AC)			
Circuit configurations	Common cathode			

FEATURES

- Very low profile - typical height of 1.7 mm
- · Low forward voltage drop, low power losses
- High efficiency operation



Available

- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- COMPLIANT HALOGEN FREE

- AEC-Q101 qualified available
- · Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

Base P/NHM3 halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meets JESD 201 class 2 whisker test Polarity: As marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V20D170C	UNIT	
Device marking code			V20D170C		
Maximum repetitive peak reverse voltage		V _{RRM}	170	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)}	20	A	
	per diode		10		
Peak forward surge current 8.3 ms single half superimposed on rated load	sine-wave	I _{FSM}	150	A	
Operating junction temperature range		T _J ⁽¹⁾	-40 to +175	°C	
Storage temperature range		T _{STG}	-55 to +175	°C	

Note

⁽¹⁾ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_1 < 1/R_{0.IA}$



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ELECTRICAL CHARACTERISTICS ($T_A = 25$ °C unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	— V _F ⁽¹⁾	0.75	-	V
	I _F = 10 A			0.83	0.9	
	$I_F = 5 A$	- T _A = 125 °C		0.6	-	
	I _F = 10 A			0.68	0.76	
Reverse current at rated V _R per diode	V _R = 140 V	T _A = 25 °C	I _R ⁽²⁾	0.001	-	mA
		T _A = 125 °C		0.8	-	mA
	V _R = 170 V	T _A = 25 °C		-	0.15	mA
		T _A = 125 °C		1.5	5	mA
Typical junction capacitance	4.0 V, 1 MHz		CJ	520	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 $\,\%$ duty cycle

⁽²⁾ Pulse test: Pulse width \leq 5 ms

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER SYMBOL		V20D170C	UNIT	
Typical thermal resistance per device	R _{0JC} ⁽¹⁾	1.5	°C/W	
	R _{0JA} ⁽²⁾⁽³⁾	58	0/00	

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: $dP_D/dT_J < 1/R_{\theta JA}$ - junction-to-mount

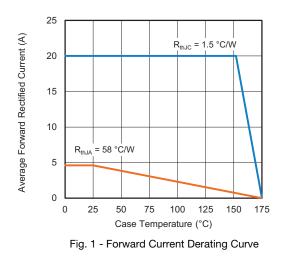
⁽³⁾ Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V20D170C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V20D170CHM3/I (1)	0.55		2000/reel	13" diameter plastic tape and reel		

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



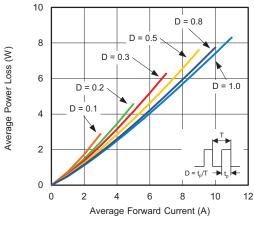


Fig. 2 - Forward Power Loss Characteristics

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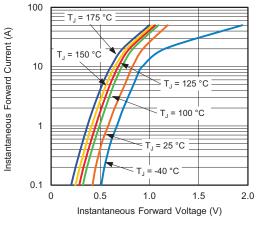


Fig. 3 - Typical Instantaneous Forward Characteristics

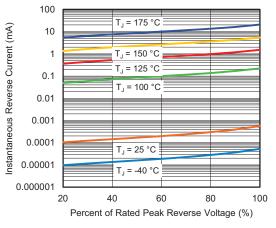


Fig. 4 - Typical Reverse Characteristics

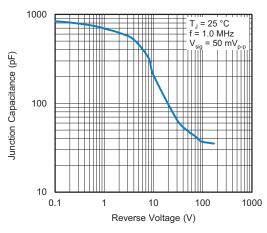


Fig. 5 - Typical Junction Capacitance

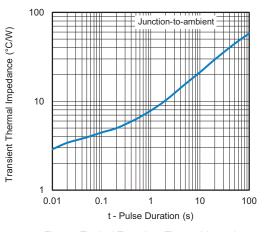


Fig. 6 - Typical Transient Thermal Impedance

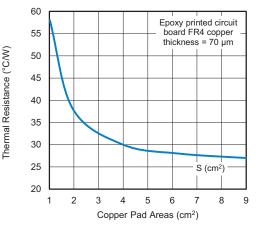


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

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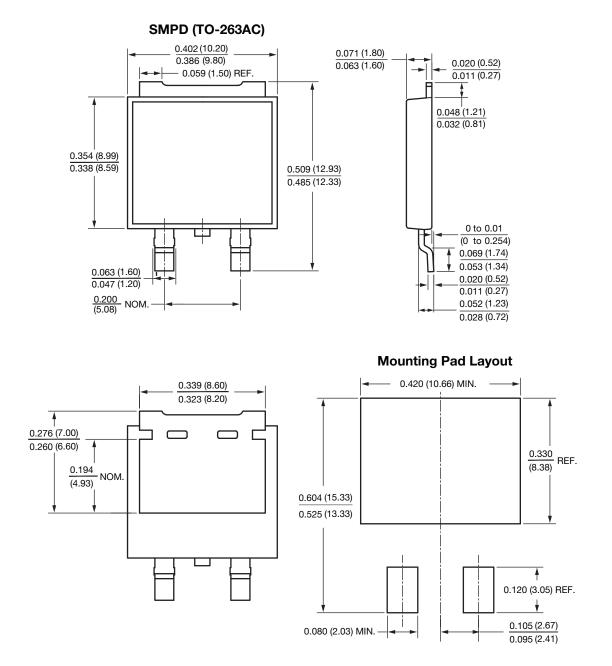
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





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