FEATURES

- Trench MOS Schottky technology
- Very low profile typical height of 1.7 mm
- · Ideal for automated placement
- Low forward voltage drop, low power losses
- High efficiency operation
- Meets MSL J-STD-020. level 1, per LF maximum peak of 260 °C
- AEC-Q101 qualified available: Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

Vishay General Semiconductor

TYPICAL APPLICATIONS

For use in high frequency DC/DC converters, switching power supplies, freewheeling diodes, OR-ing diode, and reverse battery protection in commercial, industrial, and automotive application.

MECHANICAL DATA

Case: SMPD (TO-263AC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant

Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 and HM3 suffix meet JESD 201 class 2 whisker test Polarity: as marked

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)					
PARAMETER		SYMBOL	V20D45C	UNIT	
Device marking code			V20D45C		
Maximum repetitive peak reverse voltage		V _{RRM}	45	V	
Maximum average forward rectified current (fig. 1)	per device	I _{F(AV)} ⁽¹⁾	20	А	
	per diode		10	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load		I _{FSM}	120	A	
Operating junction temperature range		T _J ⁽²⁾	-40 to +150	- °C	
Storage temperature range		T _{STG}	-55 to +150		

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{0,JA}

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Dual Low-Voltage TMBS[®] (Trench MOS Barrier Schottky) Rectifier

Ultra Low V_F = 0.34 V at I_F = 5.0 A

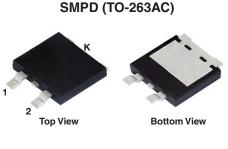
Availabl

RoHS

COMPLIANT

HALOGEN FREE





eSMP[®] Series

Anode 1 O-

Anode 2 O Cathode

DESIGN SUPPORT TOOLS AVAILABLE



PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 10 A			
V _{RRM}	45 V			
I _{FSM}	120 A			
V _F at I _F = 10 A (T _A = 125 °C)	0.40 V			
T _J max.	150 °C			
Package	SMPD (TO-263AC)			
Circuit configuration	Common cathode			



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ELECTRICAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I _F = 5 A	T _A = 25 °C	V _F ⁽¹⁾	0.44	-	V	
	I _F = 10 A			0.49	0.57		
	I _F = 5 A	T _A = 125 °C		0.34	-		
	I _F = 10 A			0.40	0.50		
Reverse current at rated V_R per diode	V _R = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	1	- mA	
		T _A = 125 °C		10	20		
Typical junction capacitance	4.0 V, 1 MHz		CJ	1900	-	pF	

Notes

⁽¹⁾ Pulse test: 300 µs pulse width, 1 % duty cycle

 $^{(2)}\,$ Pulse test: Pulse width $\leq 5\mbox{ ms}$

THERMAL CHARACTERISTICS (T _A = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL	V20D45C	UNIT	
Typical thermal resistance per device	R _{0JC} ⁽¹⁾	1.8	°C/W	
	R _{0JA} ⁽²⁾⁽³⁾	48		

Notes

⁽¹⁾ Mounted on infinite heatsink

 $^{(2)}$ The heat generated must be less than the thermal conductivity from junction-to-ambient: dP_D/dT_J < 1/R_{θ JA}

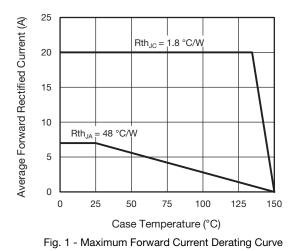
(3) Free air, without heatsink

ORDERING INFORMATION (Example)						
PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
V20D45C-M3/I	0.55	I	2000/reel	13" diameter plastic tape and reel		
V20D45CHM3/I (1)	0.55	I	2000/reel	13" diameter plastic tape and reel		

Note

⁽¹⁾ AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise noted)



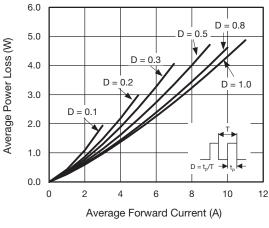
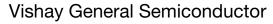


Fig. 2 - Average Power Loss Characteristics

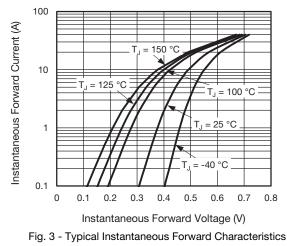
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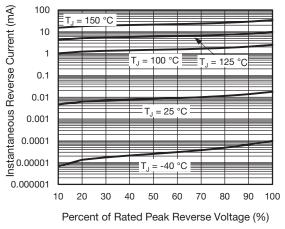
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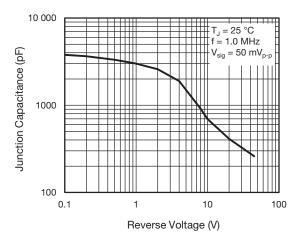


Fig. 5 - Typical Junction Capacitance

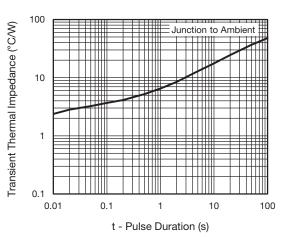


Fig. 6 - Typical Transient Thermal Impedance

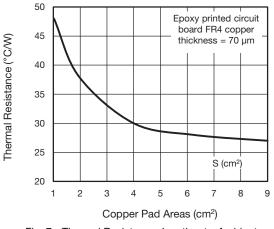


Fig. 7 - Thermal Resistance Junction-to-Ambient vs. Copper Pad Areas

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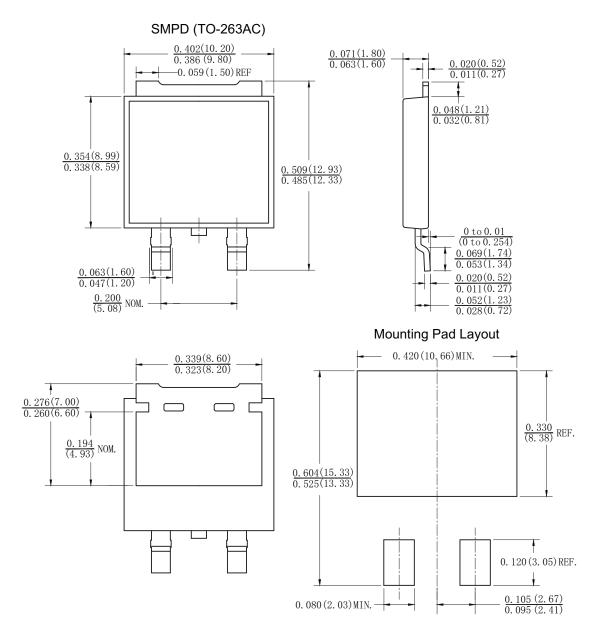
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)





Vishay

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