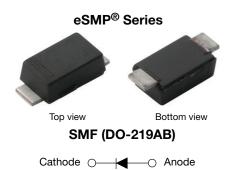
## V2FM10

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## Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifiers



#### **ADDITIONAL RESOURCES**



PRIMARY CHARACTERISTICS			
I <sub>F(AV)</sub>	2.0 A		
V <sub>RRM</sub>	100 V		
I <sub>FSM</sub>	40 A		
$V_F$ at $I_F$ = 2 A ( $T_A$ = 125 °C)	0.62 V		
T <sub>J</sub> max.	175 °C		
Package	SMF (DO-219AB)		
Circuit configuration	Single		

### FEATURES

- Trench MOS Schottky technology
- Low profile package
- Ideal for automated placement
- · Low forward voltage drop, low power losses
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Wave and reflow solderable
- AEC-Q101 qualified available - Automotive ordering code: base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

#### **TYPICAL APPLICATIONS**

For use in high frequency inverters, freewheeling, DC/DC converters, and polarity protection in commercial, industrial, and automotive applications.

#### **MECHANICAL DATA**

**Case:** SMF (DO-219AB) Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant Base P/NHM3 - halogen-free, RoHS-compliant, and AEC-Q101 qualified

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

Document Number: 87562

M3 and HM3 suffix meet JESD 201 class 2 whisker test

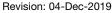
Polarity: color band denotes the cathode end

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)			
PARAMETER	SYMBOL	V2FM10	UNIT
Device marking code		2MB	
Maximum repetitive peak reverse voltage	V <sub>RRM</sub>	100	V
Maximum average forward rectified current (fig.1)	I <sub>F(AV)</sub> <sup>(1)</sup>	2.0	А
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load	I <sub>FSM</sub>	40	А
Operating junction temperature range	T <sub>J</sub> <sup>(2)</sup>	-40 to +175	<b>0°</b>
Storage temperature range	T <sub>STG</sub>	-55 to +175	U

Notes

<sup>(1)</sup> Free air, mounted on FR4 PCB, 2 oz. standard footprint

 $^{(2)}$  The heat generated must be less than the thermal conductivity from junction-to-ambient: dP<sub>D</sub>/dT<sub>J</sub> < 1/R<sub>0JA</sub>



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<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT
Instantaneous forward voltage	I <sub>F</sub> = 1.0 A	- T <sub>A</sub> = 25 °C		0.61	-	- V
	I <sub>F</sub> = 2.0 A		V <sub>F</sub> <sup>(1)</sup>	0.75	0.83	
	I <sub>F</sub> = 1.0 A	- T <sub>A</sub> = 125 °C	VF	0.53	-	
	I <sub>F</sub> = 2.0 A			0.62	0.70	
Reverse current	V <sub>B</sub> = 70 V	T <sub>A</sub> = 25 °C		0.5	-	
	$v_{\rm R} = 70 v$	T <sub>A</sub> = 125 °C	I <sub>R</sub> <sup>(2)</sup>	300	-	
	V <sub>R</sub> = 100 V	$T_A = 25 \text{ °C}$	'R (-/	-	55	μA
		T <sub>A</sub> = 125 °C		500	2000	
Typical junction capacitance	4.0 V, 1 MHz		CJ	150	-	pF

Notes

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 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  5 ms

<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \degree c$ unless otherwise noted)				
PARAMETER	SYMBOL	V2FM10	UNIT	
Typical thermal resistance	R <sub>0JA</sub> (1)(2)	125	°C/W	
	R <sub>0JM</sub> <sup>(2)</sup>	26		

Notes

<sup>(1)</sup> The heat generated must be less than the thermal conductivity from junction-to-ambient:  $dP_D/dT_J < 1/R_{\theta JA}$ 

<sup>(2)</sup> Device mounted on FR4 PCB, 2 oz. standard footprint, thermal resistance  $R_{\theta JA}$  – junction-to-ambient; thermal resistance  $R_{\theta JM}$  – junction-to-mount

ORDERING INFORMATION (Example)				
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE
V2FM10-M3/H	0.015	н	3000	7" diameter plastic tape and reel
V2FM10-M3/I	0.015	I	10 000	13" diameter plastic tape and reel
V2FM10HM3/H <sup>(1)</sup>	0.015	н	3000	7" diameter plastic tape and reel
V2FM10HM3/I <sup>(1)</sup>	0.015		10 000	13" diameter plastic tape and reel

Note

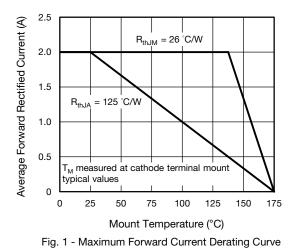
(1) AEC-Q101 qualified

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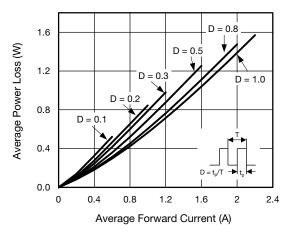


Fig. 2 - Average Power Loss Characteristics

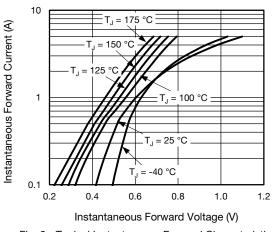
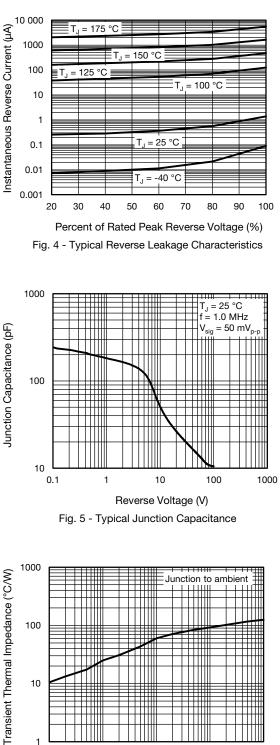


Fig. 3 - Typical Instantaneous Forward Characteristics



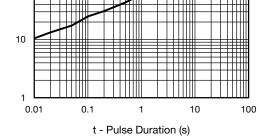


Fig. 6 - Typical Transient Thermal Impedance

Revision: 04-Dec-2019

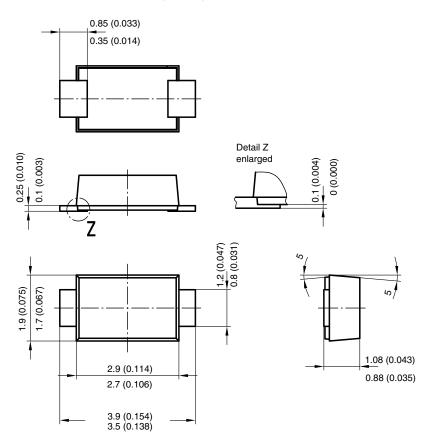
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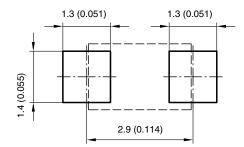
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#### **PACKAGE OUTLINE DIMENSIONS** in millimeters (inches)



Foot print recommendation:



Created - Date: 15. February 2005 Rev. 3 - Date: 13. March 2007 Document no.:S8-V-3915.01-001 (4) 17247

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