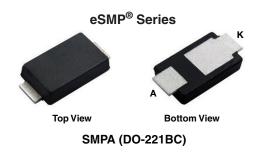
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Surface-Mount TMBS® (Trench MOS Barrier Schottky) Rectifier



Anode O Cathode

LINKS TO ADDITIONAL RESOURCES

3D Models

SHA

PRIMARY CHARACTERISTICS				
I _{F(AV)}	3.0 A			
V _{RRM}	45 V			
I _{FSM}	80 A			
V_F at I_F = 3.0 A (T_A = 125 °C)	0.37 V			
T _J max.	150 °C			
Package	SMPA (DO-221BC)			
Circuit configuration	Single			

FEATURES

- Very low profile typical height of 0.95 mm
- Ideal for automated placement
- Trench MOS Schottky technology
- Low power losses, high efficiency
- Low forward voltage drop
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- AEC-Q101 qualified available - Automotive ordering code; base P/NHM3
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

TYPICAL APPLICATIONS

For use in low voltage, high frequency inverters, freewheeling, DC/DC converters, and polarity protection applications.

MECHANICAL DATA

Case: SMPA (DO-221BC)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade Base P/NHM3_X - halogen-free, RoHS-compliant, and AEC-Q101 gualified

("_X" denotes revision code e.g. A, B,....)

Terminals: matte tin plated leads, solderable per J-STD-002 and JESD22-B102

M3 suffix meets JESD 201 class 2 whisker test, HM3 suffix meets JESD 201 class 2 whisker test

Polarity: color band denotes cathode end

MAXIMUM RATINGS ($T_A = 25 \text{ °C}$ unless otherwise noted)				
PARAMETER	SYMBOL	V3PAL45	UNIT	
Device marking code		3L45		
Maximum repetitive peak reverse voltage	V _{RRM}	45	V	
Maximum DC forward current	I _F ⁽¹⁾	3.0	A	
Peak forward surge current 10 ms single half sine-wave superimposed on rated load	I _{FSM}	80	A	
Operating junction and storage temperature range	T _J , T _{STG}	-40 to +150	°C	

Note

⁽¹⁾ Free air, mounted on recommended copper pad area

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RoHS

COMPLIANT

HALOGEN

FREE



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ELECTRICAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER	TEST CO	TEST CONDITIONS		TYP.	MAX.	UNIT
Instantaneous forward voltage	I _F = 1.5 A	– T _A = 25 °C	V _F ⁽¹⁾	0.41	-	V
	I _F = 3.0 A			0.46	0.54	
	I _F = 1.5 A	– T _A = 125 °C		0.30	-	
	I _F = 3.0 A			0.37	0.46	
Reverse current	V _B = 45 V	T _A = 25 °C	I _R ⁽²⁾	-	450	μA
	v _R = 45 V	T _A = 25 °C T _A = 125 °C		5	15	mA
Typical junction capacitance	4.0 V, 1 MH	4.0 V, 1 MHz		450	-	pF

Notes

 $^{(1)}\,$ Pulse test: 300 μs pulse width, 1 % duty cycle

⁽²⁾ Pulse test: pulse width \leq 5 ms

THERMAL CHARACTERISTICS ($T_A = 25 \text{ °C}$ unless otherwise specified)				
PARAMETER	SYMBOL	V3PAL45	UNIT	
Typical thermal resistance	R _{0JA} ⁽¹⁾	100	°C/W	
	R _{0JM} ⁽¹⁾	9		

Note

⁽¹⁾ Free air, mounted on recommended PCB, 2 oz. pad area; thermal resistance $R_{\theta JA}$ - junction to ambient; $R_{\theta JM}$ - junction to mount

ORDERING INFORMATION (Example)					
PREFERRED P/N	UNIT WEIGHT (g)	PREFERRED PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
V3PAL45-M3/I	0.032	I	14 000	13" diameter plastic tape and reel	
V3PAL45HM3_A/I (1)	0.032		14 000	13" diameter plastic tape and reel	

Note

(1) AEC-Q101 qualified

RATINGS AND CHARACTERISTICS CURVES (T_A = 25 °C unless otherwise specified)

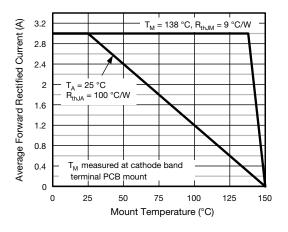


Fig. 1 - Maximum Forward Current Derating Curve

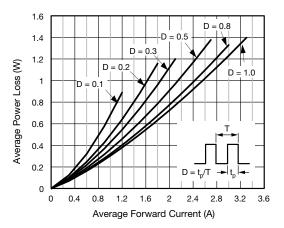
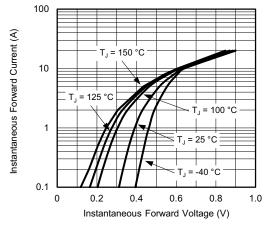


Fig. 2 - Forward Power Loss Characteristics

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Fig. 3 - Typical Instantaneous Forward Characteristics

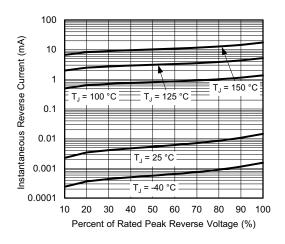


Fig. 4 - Typical Reverse Leakage Characteristics

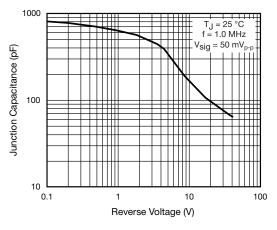


Fig. 5 - Typical Junction Capacitance

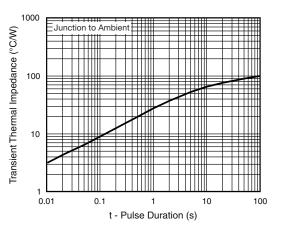


Fig. 6 - Typical Transient Thermal Impedance

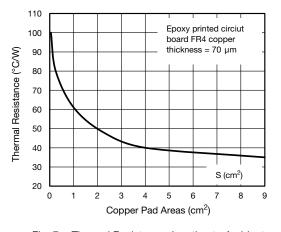


Fig. 7 - Thermal Resistance Junction to Ambient vs. Copper Pad Areas

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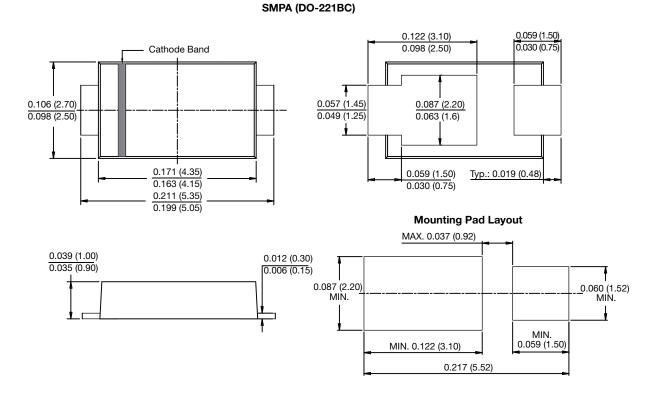
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PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



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