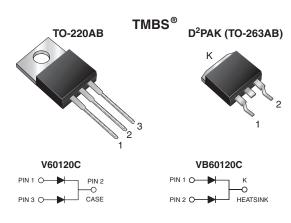


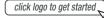
## Vishay General Semiconductor

# **Dual High Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.41 \text{ V}$  at  $I_F = 5 \text{ A}$ 



### **DESIGN SUPPORT TOOLS**





PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 30 A				
V <sub>RRM</sub>	120 V				
I <sub>FSM</sub>	300 A				
V <sub>F</sub> at I <sub>F</sub> = 30 A	0.71 V				
T <sub>J</sub> max.	150 °C				
Package	TO-220AB, D <sup>2</sup> PAK (TO-263AB)				
Circuit configuration	Common cathode				

#### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- · High efficiency operation
- · Low thermal resistance

RoHS COMPLIANT

- Meets MSL level 1, per J-STD-020, COMP LF maximum peak of 245 °C (for TO-263AB package)
- Solder bath temperature 275 °C maximum, 10 s, per JESD 22-B106 (for TO-220AB package)
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

#### TYPICAL APPLICATIONS

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters and reverse battery protection.

#### **MECHANICAL DATA**

Case: TO-220AB and D2PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating

Base P/N-E3 - RoHS-compliant, commercial grade

Terminals: matte tin plated leads, solderable per

J-STD-002 and JESD 22-B102

E3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER		SYMBOL	V60120C	VB60120C	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	120		V	
Maximum average forward rectified current (fig. 1)	per device		60		A	
	per diode	I <sub>F(AV)</sub>	30			
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	300		А	
Non-repetitive avalanche energy at $T_J = 25$ °C, $L = 100$ mH per diode		E <sub>AS</sub>	260		mJ	
Peak repetitive reverse current at $t_p$ = 2 $\mu$ s, 1 kHz, $T_J$ = 38 °C $\pm$ 2 °C per diode		I <sub>RRM</sub>	0.5		А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10	000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to	+150	°C	



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)								
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT		
Breakdown voltage	$I_R = 1.0 \text{ mA}$	T <sub>A</sub> = 25 °C	$V_{BR}$	120 (minimum)	-	V		
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.48	1	V		
	I <sub>F</sub> = 15 A			0.66	-			
	I <sub>F</sub> = 30 A			0.88	0.95			
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.41	ı			
	I <sub>F</sub> = 15 A			0.58	-			
	I <sub>F</sub> = 30 A			0.71	0.75			
Reverse current at rated V <sub>R</sub> per diode	1 V <sub>D</sub> = 90 V	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	14	-	μΑ		
		T <sub>A</sub> = 125 °C		11	-	mA		
	V <sub>P</sub> = 120 V	T <sub>A</sub> = 25 °C		40	500	μΑ		
		T <sub>A</sub> = 125 °C		15	45	mA		

#### **Notes**

(1) Pulse test: 300 µs pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms

THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER	SYMBOL	V60120C	VB60120C	UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	2.2	2.2	°C/W	

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
TO-220AB	V60120C-E3/4W	1.89	4W	50/tube	Tube		
TO-263AB	VB60120C-E3/4W	1.38	4W	50/tube	Tube		
TO-263AB	VB60120C-E3/8W	1.38	8W	800/reel	Tape and reel		

### **RATINGS AND CHARACTERISTICS CURVES** ( $T_A = 25$ °C unless otherwise noted)

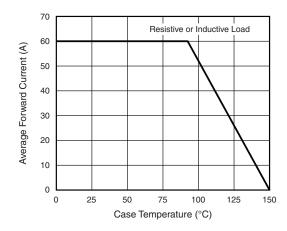


Fig. 1 - Forward Current Derating Curve

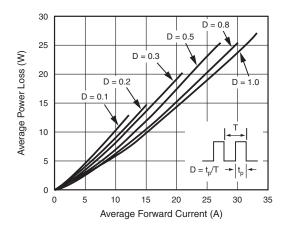


Fig. 2 - Forward Power Loss Characteristics Per Diode

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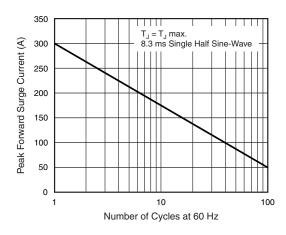


Fig. 3 - Maximum Non-Repetitive Peak Forward Surge Current Per Diode

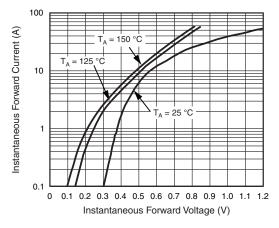


Fig. 4 - Typical Instantaneous Forward Characteristics Per Diode

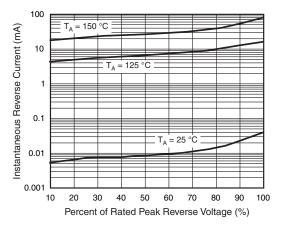


Fig. 5 - Typical Reverse Characteristics Per Diode

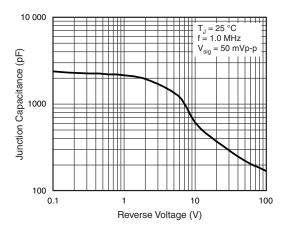


Fig. 6 - Typical Junction Capacitance Per Diode

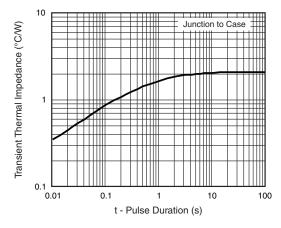
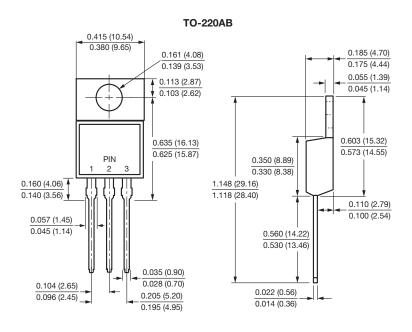


Fig. 7 - Typical Transient Thermal Impedance Per Diode

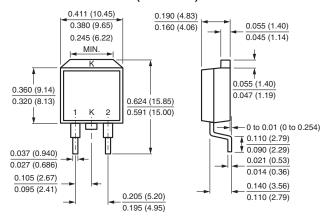


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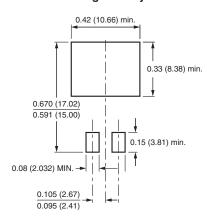
### PACKAGE OUTLINE DIMENSIONS in inches (millimeters)



#### D<sup>2</sup>PAK (TO-263AB)



#### **Mounting Pad Layout**





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