

## Vishay General Semiconductor

# **Dual High-Voltage Trench MOS Barrier Schottky Rectifier**

Ultra Low  $V_F = 0.38 \text{ V}$  at  $I_F = 5 \text{ A}$ 



#### **DESIGN SUPPORT TOOLS**

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PRIMARY CHARACTERISTICS				
I <sub>F(AV)</sub>	2 x 20 A			
$V_{RRM}$	100 V			
I <sub>FSM</sub>	250 A			
V <sub>F</sub> at I <sub>F</sub> = 20 A	0.61 V			
T <sub>J</sub> max.	150 °C			
Package	D <sup>2</sup> PAK (TO-263AB)			
Circuit configuration	cuit configuration Common cathode			

#### **FEATURES**

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation

· Low thermal resistance



- Meets MSL level 1, per J-STD-020, LF maximum peak of 245 °C
- Material categorization: for definitions of compliance please see <a href="https://www.vishay.com/doc?99912">www.vishay.com/doc?99912</a>

### **TYPICAL APPLICATIONS**

For use in high frequency converters, switching power supplies, freewheeling diodes, OR-ing diode, DC/DC converters, and reverse battery protection.

#### **MECHANICAL DATA**

Case: D<sup>2</sup>PAK (TO-263AB)

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 2 whisker test

Polarity: as marked

MAXIMUM RATINGS (T <sub>A</sub> = 25 °C unless otherwise noted)					
PARAMETER		SYMBOL	VB40100C	UNIT	
Maximum repetitive peak reverse voltage		$V_{RRM}$	100	V	
Maximum average forward rectified current (fig. 1)	per device	I <sub>F(AV)</sub>	40	^	
	per diode		20	A	
Peak forward surge current 8.3 ms single half sine-wave superimposed on rated load per diode		I <sub>FSM</sub>	250	А	
Voltage rate of change (rated V <sub>R</sub> )		dV/dt	10 000	V/µs	
Operating junction and storage temperature range		T <sub>J</sub> , T <sub>STG</sub>	-40 to +150	°C	

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	I <sub>F</sub> = 5 A	T <sub>A</sub> = 25 °C	- V <sub>F</sub>	0.47	-	V	
	I <sub>F</sub> = 10 A			0.54	-		
	I <sub>F</sub> = 20 A			0.67	0.73		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.38	-		
	I <sub>F</sub> = 10 A			0.45	-		
	I <sub>F</sub> = 20 A			0.61	0.67		
Reverse current at rated V <sub>R</sub> per diode <sup>(2)</sup>	V <sub>R</sub> = 70 V	T <sub>A</sub> = 25 °C	I <sub>R</sub>	9	-	μΑ	
		T <sub>A</sub> = 125 °C		10	-	mA	
	V <sub>R</sub> = 100 V	T <sub>A</sub> = 25 °C		-	1000	μΑ	
		T <sub>A</sub> = 125 °C		21	45	mA	

#### Notes

 $^{(1)}\,$  Pulse test: 300  $\mu s$  pulse width, 1 % duty cycle

(2) Pulse test: Pulse width ≤ 40 ms



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THERMAL CHARACTERISTICS (T <sub>A</sub> = 25 °C unless otherwise noted)				
PARAMETER	SYMBOL VB40100C		UNIT	
Typical thermal resistance per diode	$R_{ heta JC}$	2.0	°C/W	

ORDERING INFORMATION (Example)						
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE	
TO-263AB	VB40100C-M3/4W	1.39	4W	50/tube	Tube	
TO-263AB	VB40100C-M3/8W	1.39	8W	800/reel	Tape and reel	

## **RATINGS AND CHARACTERISTICS CURVES** (T<sub>A</sub> = 25 °C unless otherwise noted)

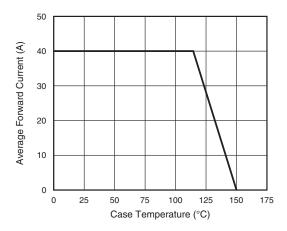


Fig. 1 - Forward Current Derating Curve

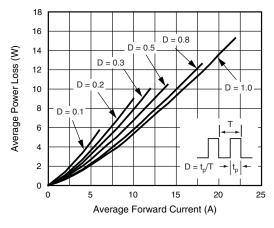


Fig. 2 - Forward Power Loss Characteristics Per Diode

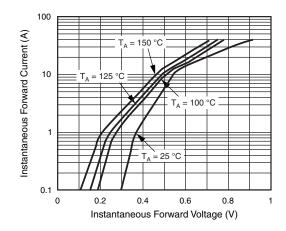


Fig. 3 - Typical Instantaneous Forward Characteristics Per Diode

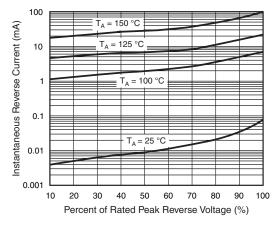


Fig. 4 - Typical Reverse Characteristics Per Diode



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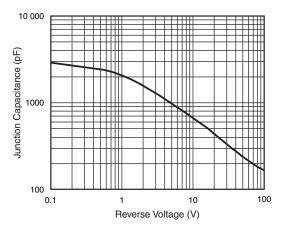


Fig. 5 - Typical Junction Capacitance Per Diode

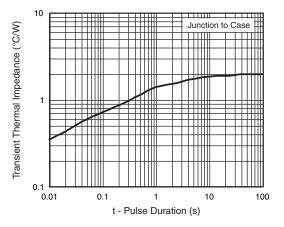
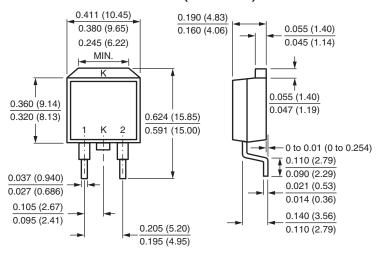


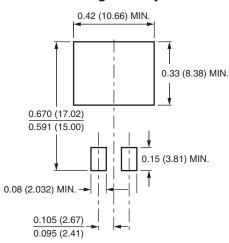
Fig. 6 - Typical Transient Thermal Impedance Per Diode

## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)

## D<sup>2</sup>PAK (TO-263AB)



### **Mounting Pad Layout**





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