Vishay Semiconductors

Bidirectional Asymmetrical (BiAs) Single Line ESD Protection Diode in LLP1006-2L

FEATURES

Ultra compact LLP1006-2LLow package height < 0.4 mm

Low leakage current < 0.1 μA

Low load capacitance typical C_D = 8 pF

• ESD immunity acc. IEC 61000-4-2 ± 25 kV contact discharge ± 30 kV air discharge

• PATENT(S): www.vishay.com/patents

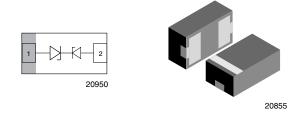
please see www.vishay.com/doc?99912

• Working range -7 V up to +14 V or -14 V up to +7 V

• e4 - precious metal (e.g. Ag, Au, NiPd, NiPdAu) (no Sn)

• Material categorization: for definitions of compliance

• 1-line ESD protection



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MARKING (example only)



Bar = pin 1 marking

Y = type code (see table below)

X = date code

/ISHA\

LINKS TO ADDITIONAL RESOURCES



ORDERING INFORMATION						
PIN PLATING	DEVICE NAME	ORDERING CODE	TAPED UNITS PER REEL (8 mm TAPE ON 7" REEL)	MINIMUM ORDER QUANTITY		
e4	VCUT0714A-HD1	VCUT0714A-HD1-GS08	8k	8k		

PACKAGE DATA							
DEVICE NAME	PACKAGE NAME	PIN PLATING	TYPE CODE	WEIGHT	MOLDING COMPOUND FLAMMABILITY RATING	MOISTURE SENSITIVITY LEVEL	SOLDERING CONDITIONS
VCUT0714A-HD1	LLP1006-2L	e4	В	0.72 mg	UL 94 V-0	MSL level 1 (according J-STD-020)	Peak temperature max. 260 °C

ABSOLUTE MAXIMUM RATINGS							
PARAMETER	TEST CONDITIONS	SYMBOL	VALUE	UNIT			
Peak pulse current	Pin 1 to pin 2, acc. IEC 61000-4-5, 8/20 µs/single shot		5	А			
Feak pulse current	Pin 2 to pin 1, acc. IEC 61000-4-5, 8/20 µs/single shot	IPPM	2	А			
Peak pulse power	Pin 1 to pin 2, acc. IEC 61000-4-5, 8/20 µs/single shot	р	63	W			
	Pin 2 to pin 1, acc. IEC 61000-4-5, 8/20 µs/single shot	P _{PP}	54	W			
ESD immunity	Contact discharge acc. IEC 61000-4-2; 10 pulses	V	± 25	kV			
	Air discharge acc. IEC 61000-4-2; 10 pulses	V _{ESD}	± 30	kV			
Storage temperature		T _{STG}	-55 to +150	°C			
Operating temperature	Junction temperature	ТJ	-40 to +125	°C			

PATENT(S): www.vishay.com/patents

This Vishay product is protected by one or more United States and international patents.

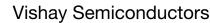
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ROHS

HALOGEN

GREEN (5-2008)

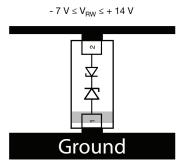


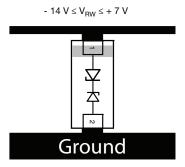
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CUT THE SPIKES

SHAY

The VCUT0714A-HD1 is a bidirectional but asymmetrical (BiAs) ESD protection device which clamps positive and negative overvoltage transients to ground. Connected between the signal or data line and the ground the VCUT0714A-HD1 offers a high isolation (low leakage current, small capacitance) within the specified working range of -7 V to +14 V or -14 V and +7 V. Due to the short leads and small package size of the tiny LLP1006 package the line inductance is very low, so that fast transients like an ESD strike can be clamped with minimal over- or undershoots.





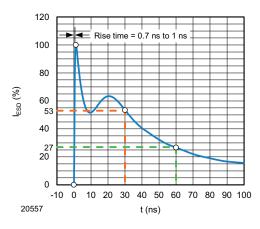
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ELECTRICAL CHARACTERISTICS (pin 2 to pin 1) (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT	
Protection paths	Number of lines which can be protected	N _{channel}	-	-	1	lines	
Reverse stand-off voltage	Max. reverse working voltage	V _{RWM}	-	-	14	V	
Reverse voltage	At I _R = 0.1 μA	V _R	14	-	-	V	
Reverse current	At V _{RWM} = 14 V	I _R	-	-	0.1	μA	
Reverse breakdown voltage	At I _R = 1 mA	V _{BR}	14.5	-	-	V	
Reverse clamping voltage	At I _{PP} = 1 A	V _C	-	-	27	V	
	At $I_{PP} = I_{PPM} = 2 A$	V _C	-	-	30	V	
Capacitance	At $V_R = 0$ V; f = 1 MHz	CD	-	8	8.5	pF	
	At V _R = 7 V; f = 1 MHz	CD	-	4	-	pF	

ELECTRICAL CHARACTERISTICS (pin 1 to pin 2) (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITIONS/REMARKS	SYMBOL	MIN.	TYP.	MAX.	UNIT		
Protection paths	Number of lines which can be protected	N _{channel}	-	-	1	lines		
Reverse stand-off voltage	Max. reverse working voltage	V _{RWM}	-	-	7	V		
Reverse voltage	At I _R = 0.1 μA	V _R	7	-	-	V		
Reverse current	At V _{RWM} = 7 V	I _R	-	-	0.1	μA		
Reverse breakdown voltage	At I _R = 1 mA	V _{BR}	7.3	-	-	V		
Reverse clamping voltage	At I _{PP} = 1 A	V _C	-	-	13	V		
	At $I_{PP} = I_{PPM} = 5 \text{ A}$	V _C	-	-	17	V		
Capacitance	At V = 0 V; f = 1 MHz	CD	-	8	8.5	pF		
	At V = 3.5 V; f = 1 MHz	CD	-	6.4	-	pF		

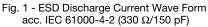
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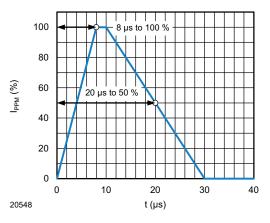


Fig. 2 - 8/20 µs Peak Pulse Current Wave Form acc. IEC 61000-4-5

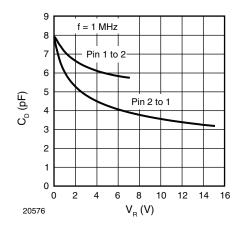


Fig. 3 - Typical Capacitance C_{D} vs. Reverse Voltage V_{R}

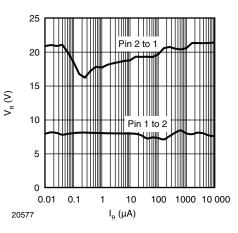


Fig. 4 - Typical Reverse Voltage V_R vs. Reverse Current I_R

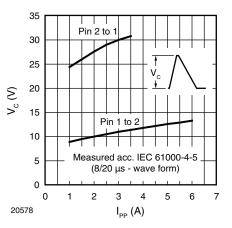
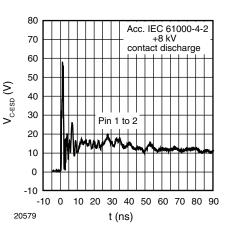
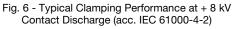


Fig. 5 - Typical Peak Clamping Voltage V_{C} vs. Peak Pulse Current I_{PP}



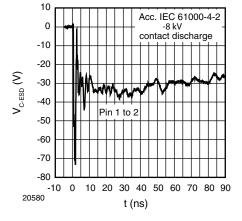


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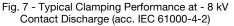
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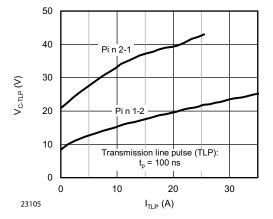
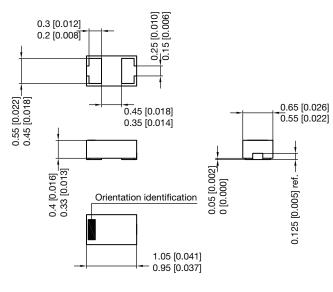
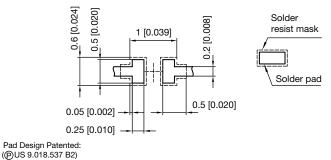


Fig. 8 - Typical Peak Clamping Voltage vs. Peak Pulse Current

PACKAGE DIMENSIONS in millimeters (inches): LLP1006-2L



Foot print recommendation:

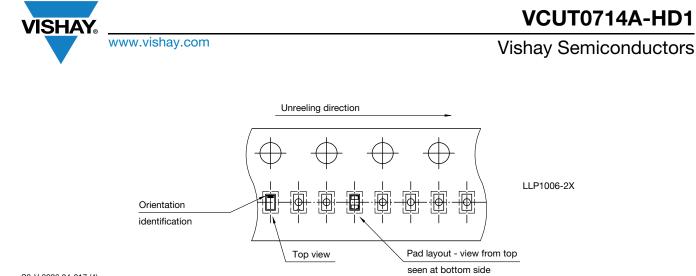


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