VFT2045CBP

Vishay General Semiconductor

# Trench MOS Barrier Schottky Rectifier for PV Solar Cell Bypass Protection

Ultra Low  $V_F = 0.33$  V at  $I_F = 5.0$  A



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PRIMARY CHARACTERISTICS					
I <sub>F(AV)</sub>	2 x 10 A				
V <sub>RRM</sub>	45 V				
I <sub>FSM</sub>	160 A				
$V_F$ at $I_F = 10 A$	0.41 V				
T <sub>OP</sub> max. (AC mode)	150 °C				
T <sub>J</sub> max. (DC forward current)	200 °C				
Package	ITO-220AB				
Circuit configuration	Common cathode				

## FEATURES

- Trench MOS Schottky technology
- · Low forward voltage drop, low power losses
- High efficiency operation
- Solder dip 275 °C max. 10 s, per JESD 22-B106
- T<sub>1</sub> 200 °C max. in solar bypass mode application
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### **TYPICAL APPLICATIONS**

For use in solar cell junction box as a bypass diode for protection, using DC forward current without reverse bias.

### **MECHANICAL DATA**

#### Case: ITO-220AB

Molding compound meets UL 94 V-0 flammability rating Base P/N-M3 - halogen-free, RoHS-compliant, and commercial grade

**Terminals:** matte tin plated leads, solderable per J-STD-002 and JESD 22-B102

M3 suffix meets JESD 201 class 1A whisker test

Polarity: as marked

Mounting Torque: 10 in-lbs maximum

<b>MAXIMUM RATINGS</b> (T <sub>A</sub> = 25 °C unless otherwise noted)						
PARAMETER			VFT2045CBP	UNIT		
Maximum repetitive peak reverse voltage		V <sub>RRM</sub>	45	V		
Maximum average forward rectified current (fig. 1)	per device	I (1)	20	А		
	per diode	I <sub>F(AV)</sub> <sup>(1)</sup>	10			
Peak forward surge current 8.3 ms single half sine-wave supe per diode	I <sub>FSM</sub>	160	А			
Isolation voltage from terminal to heatsink, t = 1 min	V <sub>AC</sub>	1500	V			
Operating junction and storage temperature range (AC mode)	T <sub>OP</sub> , T <sub>STG</sub>	-40 to +150	°C			
Junction temperature in DC forward current without reverse b	T <sub>J</sub> <sup>(2)</sup>	≤ 200	°C			

Notes

<sup>(1)</sup> With heatsink

<sup>(2)</sup> Meets the requirements of IEC 61215 ed. 2 bypass diode thermal test

<b>ELECTRICAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)							
PARAMETER	TEST CONDITIONS		SYMBOL	TYP.	MAX.	UNIT	
Instantaneous forward voltage per diode	$I_F = 5 A$	- T <sub>A</sub> = 25 °C	V <sub>F</sub> <sup>(1)</sup>	0.44	-	V	
	I <sub>F</sub> = 10 A			0.49	0.58		
	I <sub>F</sub> = 5 A	T <sub>A</sub> = 125 °C		0.33	-		
	I <sub>F</sub> = 10 A			0.41	0.52		
Reverse current per diode	$V_{-} = /(5)V_{-}$	T <sub>A</sub> = 25 °C	I <sub>R</sub> <sup>(2)</sup>	-	2000	μA	
		T <sub>A</sub> = 125 °C		10	30	mA	

#### Notes

<sup>(1)</sup> Pulse test: 300 µs pulse width, 1 % duty cycle

<sup>(2)</sup> Pulse test: Pulse width  $\leq$  40 ms

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ROHS COMPLIANT

HALOGEN



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<b>THERMAL CHARACTERISTICS</b> ( $T_A = 25 \text{ °C}$ unless otherwise noted)						
PARAMETER		SYMBOL	VFT2045CBP	UNIT		
Typical thermal resistance	per diode	$R_{ ext{ heta}JC}$	6.0	°C/W		
	per device		4.5	0/10		

ORDERING INFORMATION (Example)							
PACKAGE	PREFERRED P/N	UNIT WEIGHT (g)	PACKAGE CODE	BASE QUANTITY	DELIVERY MODE		
ITO-220AB	VFT2045CBP-M3/4W	1.76	4W	50/tube	Tube		

## RATINGS AND CHARACTERISTICS CURVES (T<sub>A</sub> = 25 °C unless otherwise noted)

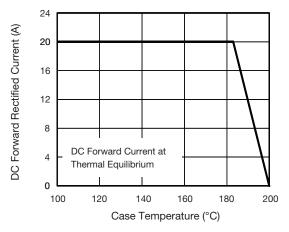


Fig. 1 - Maximum Forward Current Derating Curve

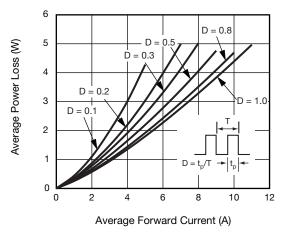
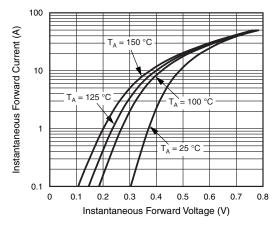
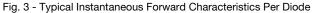


Fig. 2 - Forward Power Loss Characteristics Per Diode





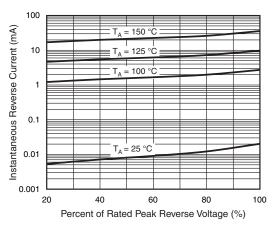
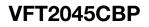


Fig. 4 - Typical Reverse Characteristics Per Diode

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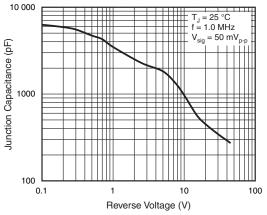


Fig. 5 - Typical Junction Capacitance Per Diode

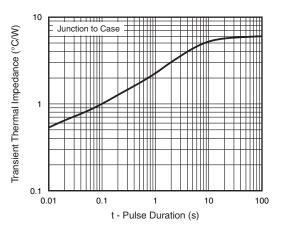
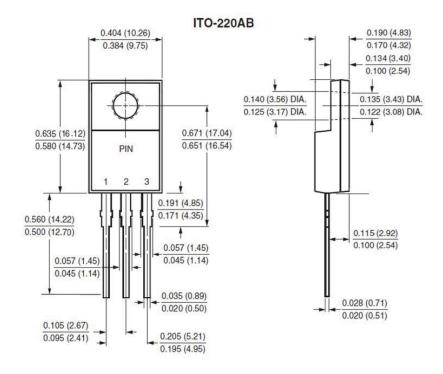


Fig. 6 - Typical Transient Thermal Impedance Per Diode

## **PACKAGE OUTLINE DIMENSIONS** in inches (millimeters)





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