



Power SMD LED PLCC-2



DESCRIPTION

The VLMK33.. series is an advanced modification of the Vishay VLMK31.. series. It is designed to incorporate larger chips, therefore, capable of withstanding a 50 mA drive current.

The package of the VLMK33.. is the PLCC-2 (equivalent to a size B tantalum capacitor).

It consists of a lead frame which is embedded in a white thermoplast. The reflector inside this package is filled up with clear epoxy.

PRODUCT GROUP AND PACKAGE DATA

- Product group: LED
- Package: SMD PLCC-2
- Product series: power
- Angle of half intensity: ± 60°

FEATURES

- Available in 8 mm tape
- ESD-withstand voltage: up to 2 kV according to JESD22-A114-B
- Compatible with IR reflow, vapor phase and wave solder processes according to CECC 00802 and J-STD-020
- Preconditioning according to JEDEC[®] level 2a
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

APPLICATIONS

- · Interior and exterior lighting
- Indicator and backlighting purposes for audio, video, LCDs, switches, symbols, illuminated advertising etc.
- Illumination purpose, alternative to incandescent lamps
- General use

PARTS TABLE														
PART	COLOR	LUMINOUS INTENSITY (mcd)		at I _F (mA)	WA	WAVELENGTH (nm)		at I _F	FORWARD VOLTAGE (V)		at I _F (mA)	TECHNOLOGY		
		MIN.	TYP.	MAX	(IIIA)	MIN.	TYP.	MAX	(mA)	MIN.	TYP.	MAX	(IIIA)	
VLMK33Q2T1-GS08	Amber	90	275	355	20	611	617	622	20	-	1.9	2.5	20	AllnGaP on GaAs
VLMK33Q2T1-GS18	Amber	90	275	355	20	611	617	622	20	-	1.9	2.5	20	AllnGaP on GaAs
VLMK33S1T1-GS08	Amber	180	275	355	20	611	617	622	20	-	1.9	2.5	20	AllnGaP on GaAs
VLMK33S1T1-GS18	Amber	180	275	355	20	611	617	622	20	-	1.9	2.5	20	AllnGaP on GaAs

ABSOLUTE MAXIMUM RATINGS (T_{amb} = 25 °C unless otherwise specified) **VLMK33..**

VEMIKOO				
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Reverse voltage ⁽¹⁾		V _R	5	V
DC forward current		I _F	50	mA
Power dissipation		Pv	130	mW
Junction temperature		Тj	125	°C
Operating temperature range		T _{amb}	-40 to +100	°C
Storage temperature range		T _{stg}	-40 to +100	°C
Soldering temperature	t ≤ 5 s	T _{sd}	260	°C
Thermal resistance junction-to-ambient	Mounted on PC board (pad size > 16 mm^2)	R _{thJA}	400	K/W

Note

⁽¹⁾ Driving LED in reverse direction is suitable for a short term application

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Pb-free (e3)

ROHS COMPLIANT HALOGEN FREE GREEN (5-2008) www.vishay.com

VLMK33..

Vishay Semiconductors

OPTICAL AND ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C unless otherwise specified) VLMK33, AMBER							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
Lumineus intereit.	L 00 A	VLMK33Q2T1	Ι _V	90	275	355	mcd
Luminous intensity	I _F = 20 mA	VLMK33S1T1	Ι _V	180	275	355	mcd
Luminous flux/luminous intensity			φ _V /I _V	-	3.14	-	mlm/mcd
Dominant wavelength	I _F = 20 mA	VLMK33Q2T1	λ _d	611	617	622	nm
		VLMK33S1T1	λ _d	611	617	622	nm
Peak wavelength	I _F = 20 mA		λρ	-	624	-	nm
Spectral bandwidth at 50 % I _{rel max.}	I _F = 20 mA		Δλ	-	18	-	nm
Angle of half intensity	I _F = 20 mA		φ	-	± 60		0
Forward voltage	l _F = 20 mA		V _F	-	1.9	2.5	V
Reverse current	V _R = 5 V		V _R	-	0.01	10	μA

LUMINOUS INTENSITY CLASSIFICATION						
GROUP	LUMINOUS INTENSITY (mcd)					
GROOP	MIN.	MAX.				
Q2	90	112				
R1	112	140				
R2	140	180				
S1	180	224				
S2	224	280				
T1	280	355				

Note

- Luminous intensity is tested at a current pulse duration of 25 ms and an accuracy of \pm 11 %.

The above type numbers represent the order groups which include only a few brightness groups. Only one group will be shipped on each reel (there will be no mixing of two groups on each reel). In order to ensure availability, single brightness groups will be not orderable.

In a similar manner for colors where wavelength groups are measured and binned, single wavelength groups will be shipped on any one reel. In order to ensure availability, single wavelength groups will be not orderable

COLOR CLASSIFICATION					
	DOMINANT WAVELENGTH (nm) AMBER				
GROUP					
	MIN.	MAX.			
1	611	618			
2	614	622			

Note

[·] Wavelength are tested at a current pulse duration of 25 ms

CROSSING TABLE					
VISHAY	OSRAM				
VLMK33Q2T1	LAT676-Q2T1				
VLMK33S1T1	LAT676-S1T1				

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TYPICAL CHARACTERISTICS ($T_{amb} = 25 \text{ °C}$ unless otherwise specified)

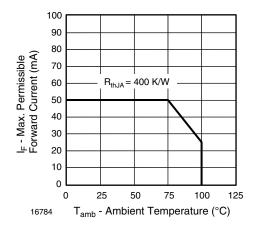


Fig. 1 - Forward Current vs. Ambient Temperature

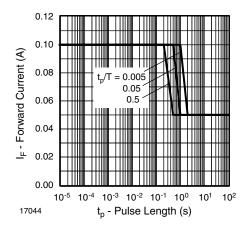


Fig. 2 - Forward Current vs. Pulse Length

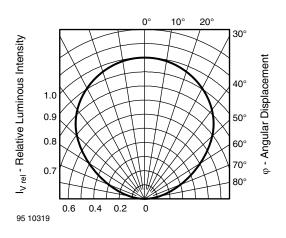


Fig. 3 - Relative Luminous Intensity vs. Angular Displacement

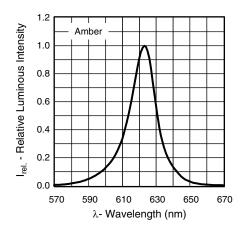


Fig. 4 - Relative Luminous Intensity vs. Angular Displacement

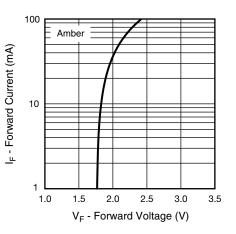


Fig. 5 - Forward Current vs. Forward Voltage

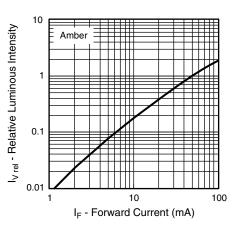


Fig. 6 - Change of Dominant Wavelength vs. Forward Current

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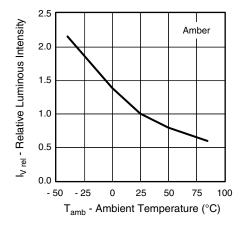


Fig. 7 - Relative Luminous Intensity vs. Ambient Temperature

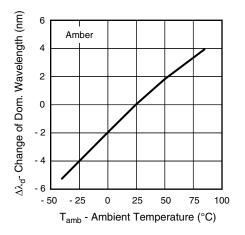


Fig. 8 - Change of Dominant Wavelength vs. Ambient Temperature

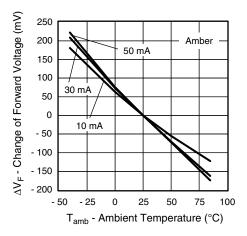
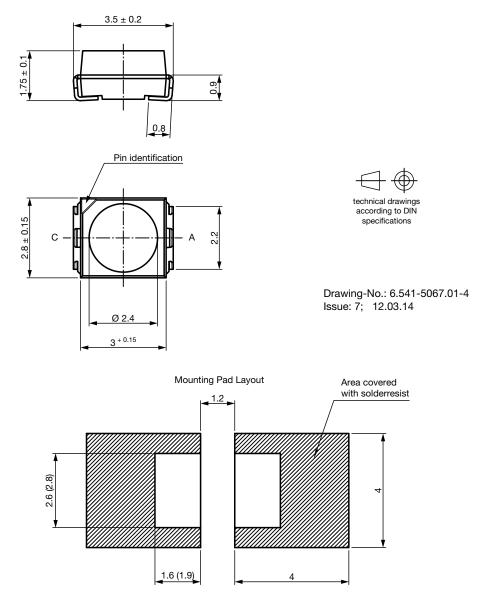


Fig. 9 - Change of Forward Voltage vs. Ambient Temperature



PACKAGE DIMENSIONS in millimeters



Dimensions: reflow and vapor phase (wave soldering)

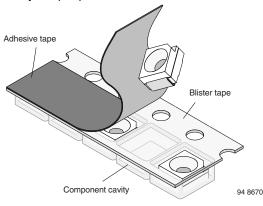
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METHOD OF TAPING / POLARITY AND TAPE AND REEL

SMD LED (VLM3-SERIES)

Vishay's LEDs in SMD packages are available in an antistatic 8 mm blister tape (in accordance with DIN IEC 40 (CO) 564) for automatic component insertion. The blister tape is a plastic strip with impressed component cavities, covered by a top tape.



TAPING OF VLM.3..

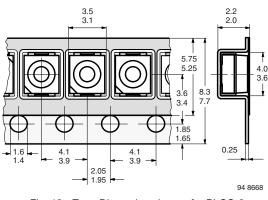
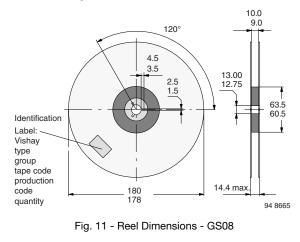


Fig. 10 - Tape Dimensions in mm for PLCC-2

REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS08 (= 1500 PCS)



REEL PACKAGE DIMENSION IN MILLIMETERS FOR SMD LEDS, TAPE OPTION GS18 (= 8000 PCS) PREFERRED

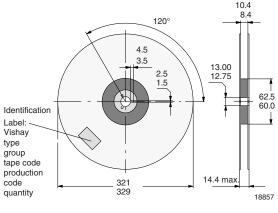


Fig. 12 - Reel Dimensions - GS18

SOLDERING PROFILE

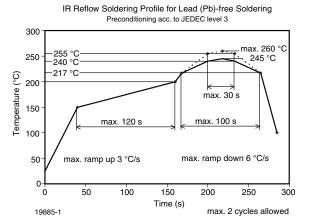


Fig. 13 - Vishay Lead (Pb)-free Reflow Soldering Profile (according to J-STD-020)

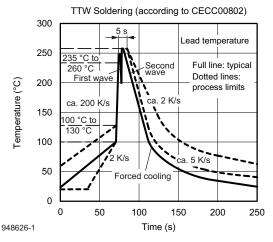


Fig. 14 - Double Wave Soldering of Opto Devices (all packages)

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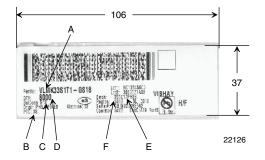
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BAR CODE PRODUCT LABEL (example)



- A) Type of component
- B) Manufacturing plant
- C) SEL selection code (bin):
 - e.g.: S1 = code for luminous intensity group 3 = code for color group
- D) Total quantity
- E) Batch = date code: year / week / manufacturing plant
- F) Region code



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