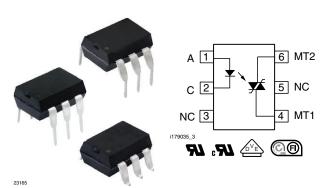


Optocoupler, Phototriac Output, High dV/dt, Low Input Current



LINKS TO ADDITIONAL RESOURCES













DESCRIPTION

The VO4254 and VO4256 phototriac consists of a GaAs IRLED optically coupled to a photosensitive non-zero crossing TRIAC packaged in a DIP-6 package.

High input sensitivity is achieved by using an emitter follower phototransistor and a cascaded SCR predriver resulting in an LED trigger current of 1.6 mA for bin D, 2 mA for bin H, and 3 mA for bin M.

The new non zero phototriac family use a proprietary dV/dt clamp resulting in a static dV/dt of greater than 5 kV/µs.

The VO4254 and VO4256 phototriac isolates low-voltage logic from 120 $V_{AC},\,240~V_{AC},\,$ and 380 V_{AC} lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

FEATURES

- High static dV/dt 5 kV/µs
- High input sensitivity 1.6 mA, 2 mA, and 3 mA
- 400 V and 600 V blocking voltage
- 300 mA on-state current
- Isolation rated voltage 4420 V_{RMS}
- Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

Pb-free





APPLICATIONS

- · Solid-state relays
- Industrial controls
- Office equipment
- Consumer appliances

AGENCY APPROVALS

- UL1577
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- FIMKO

ORDERING INFORMATION					
V O 4 2 5 # X - X O O # T DIP-6 Option 6 Option 7 PART NUMBER PACKAGE OPTION TAPE AND REEL 7.62 mm 10.16 mm 1>0.7 mm					
ACENOV CERTIFIED / DACKAGE	V _{DRM} 400 V _{DRM} 600				
		TRIGGER CURRENT, I _{FT} (mA)			
AGENCY CERTIFIED / PACKAGE		TRIGGER CUR	RENT, I _{FT} (mA)		
UL, cUL, FIMKO	2	TRIGGER CUR 1.6	RENT, I _{FT} (mA)	3	
	2 VO4254H		, ,	3 VO4256M	
UL, cUL, FIMKO	_	1.6	, ,	· ·	

Note

· Additional options may be possible, please contact sales office



ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT	
INPUT						
Reverse voltage			V_{R}	6	V	
Forward current			I _F	60	mA	
Power dissipation			P _{diss}	100	mW	
Derate from 25 °C				1.33	mW/°C	
OUTPUT						
Peak off-state voltage		VO4254H	V_{DRM}	400	V	
Feak oil-state voitage		VO4256D/H/M	V_{DRM}	600	V	
RMS on-state current			I _{TM}	300	mA	
Power dissipation			P _{diss}	500	mW	
Derate from 25 °C				6.6	mW/°C	
COUPLER						
Storage temperature range			T _{stg}	-55 to +150	°C	
Ambient temperature range			T _{amb}	-55 to +100	°C	
Soldering temperature	Max. ≤ 10 s dip soldering ≥ 0.5 mm from case bottom		T _{sld}	260	°C	

Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

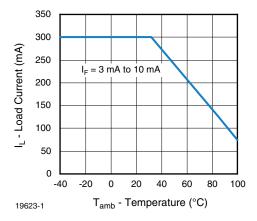


Fig. 1 - Recommended Operating Condition



THERMAL CHARACTERISTICS			
PARAMETER	SYMBOL	VALUE	UNIT
LED power dissipation	P _{diss}	100	mW
Output power dissipation	P _{diss}	500	mW
Maximum LED junction temperature	T _{jmax.}	125	°C
Maximum output die junction temperature	T _{jmax.}	125	°C
Thermal resistance, junction emitter to board	θ_{JEB}	150	°C/W
Thermal resistance, junction emitter to case	θ_{JEC}	139	°C/W
Thermal resistance, junction detector to board	θ_{JDB}	78	°C/W
Thermal resistance, junction detector to case	θ_{JDC}	103	°C/W
Thermal resistance, junction emitter to junction detector	θ_{JED}	496	°C/W
Thermal resistance, case to ambient	θ_{CA}	3563	°C/W

Note

The thermal characteristics table above were measured at 25 °C and the thermal model is represented in the thermal network below. Each
resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal
resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation
of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note

ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT								
Forward voltage	I _F = 10 mA		V_{F}	-	1.2	1.4	V	
Reverse current	V _R = 6 V		I _R	-	0.1	10	μΑ	
Input capacitance	V _F = 0 V, f = 1 MHz		C _I	-	40	-	pF	
OUTPUT								
Repetitive peak off-state voltage	I - 100 uA	VO4254H	V_{DRM}	400	-	-	V	
	$I_{DRM} = 100 \mu\text{A}$	VO4256D/H/M	V_{DRM}	600	-	-	V	
Off-state current	$V_D = V_{DRM}$		I _{DRM}	-	-	100	μA	
On-state voltage	$I_{T} = 300 \text{ mA}$		V_{TM}	-	-	3	V	
On-current	$PF = 1, V_{T(RMS)} = 1.7 V$		I _{TM}	-	-	300	mA	
Critical rate of rise of off-state voltage	$V_D = 0.67 \ V_{DRM}, T_J = 25 \ ^{\circ}C$		dV/dt _{cr}	5000	-	-	V/µs	
COUPLER								
LED trigger current, current required to latch output	V 0V	VO4254H	I _{FT}	-	-	2	mA	
		VO4256D	I _{FT}	-	-	1.6	mA	
	$V_D = 3 V$	VO4256H	I _{FT}	-	-	2	mA	
		VO4256M	I _{FT}	-	-	3	mA	
Capacitance (input to output)	f = 1 MHz, V _{IO} = 0 V		C _{IO}	-	0.8	-	pF	

Note

 Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements



SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 100 / 21				
Pollution degree	According to DIN VDE 0109		2				
Comparative tracking index	Insulation group IIIa	CTI	175				
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	4420	V _{RMS}			
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V_{IOTM}	8000	V _{peak}			
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	890	V _{peak}			
Isolation resistance	$T_{amb} = 25 ^{\circ}C, V_{IO} = 500 V$	R _{IO}	≥ 10 ¹²	Ω			
	$T_{amb} = 100 ^{\circ}C, V_{IO} = 500 V$	R _{IO}	≥ 10 ¹¹	Ω			
Output safety power		P _{SO}	500	mW			
Input safety current		I _{SI}	250	mA			
Input safety temperature		T _S	175	°C			
Creepage distance	DIP-6		≥ 7	mm			
Clearance distance	DIP-6		≥ 7	mm			
Creepage distance	DID 6 400 mill option 6		≥ 8	mm			
Clearance distance	DIP-6, 400 mil, option 6		≥ 8	mm			
Creepage distance	SMD 6 option 7		≥ 7	mm			
Clearance distance	SMD-6, option 7		≥ 7	mm			
Insulation thickness		DTI	≥ 0.4	mm			

Note

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

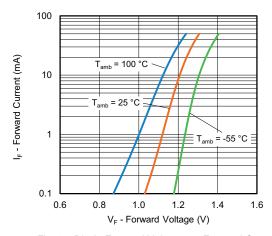


Fig. 2 - Diode Forward Voltage vs. Forward Current

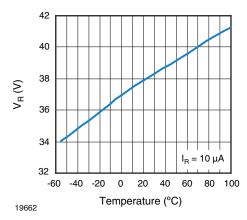


Fig. 3 - Diode Reverse Voltage vs. Temperature

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As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits

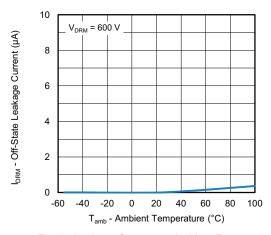


Fig. 4 - Leakage Current vs. Ambient Temperature

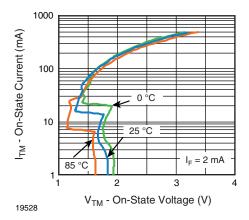


Fig. 5 - On-State Current vs. On-State Voltage

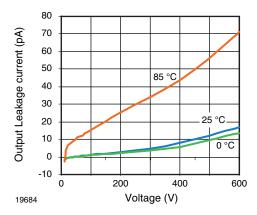


Fig. 6 - Output Off Current (Leakage) vs. Voltage

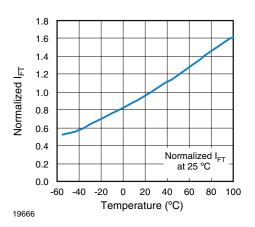


Fig. 7 - Normalized Trigger Input Current vs. Temperature

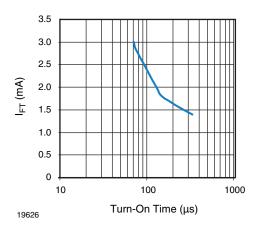


Fig. 8 - I_{FT} vs. Turn-On Time (µs)

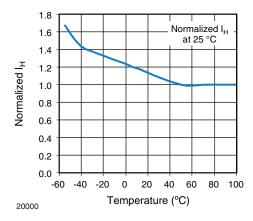
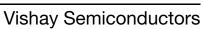


Fig. 9 - Normalized I_H vs. Temperature





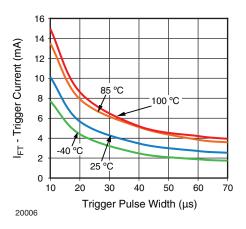
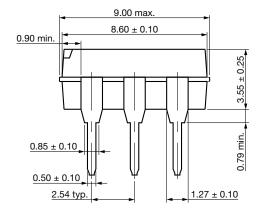
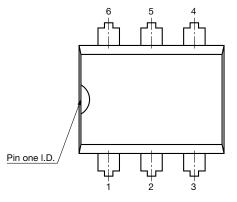


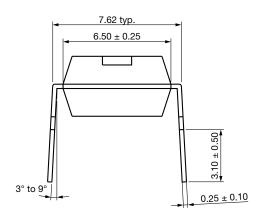
Fig. 10 - I_{FT} vs. LED Pulse Width

PACKAGE DIMENSIONS (in millimeters)

DIP-6





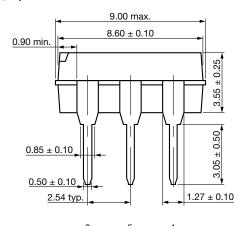


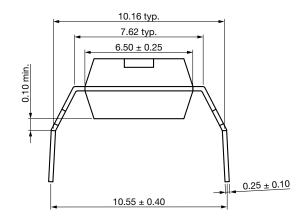


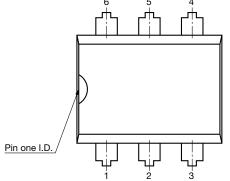
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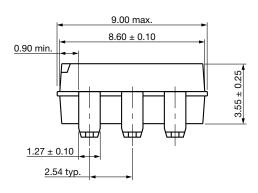
DIP-6, Option 6

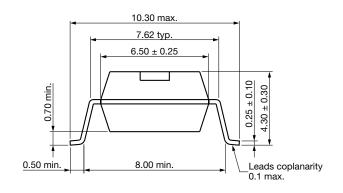


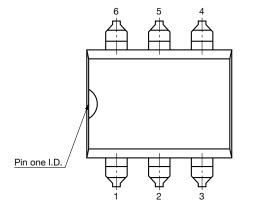


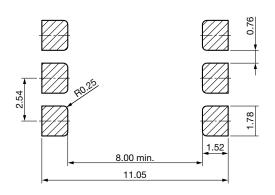


SMD-6, Option 7











PACKAGE MARKING



Fig. 11 - Example of VO4256D-X001



Fig. 12 - Example of VO4254H-X006

Notes

- "YWW" is the date code marking (Y = year code, WW = week code)
- The VDE logo is only marked on option 1 parts
- Tape and reel suffix (T) is not part of the package marking

PACKING INFORMATION (in millimeters)

Tube

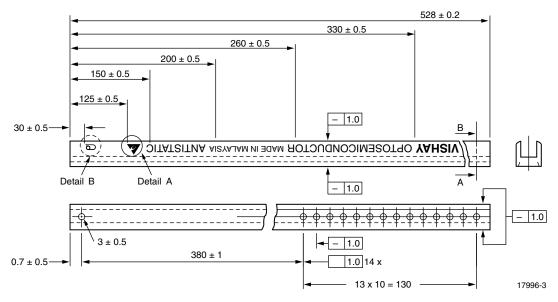


Fig. 13 - Shipping Tube Specifications for DIP-6 Packages

DEVICES PER TUBS					
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX		
DIP-6	50	40	2000		
DIP-6, option 6	50	40	2000		

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DIP-6

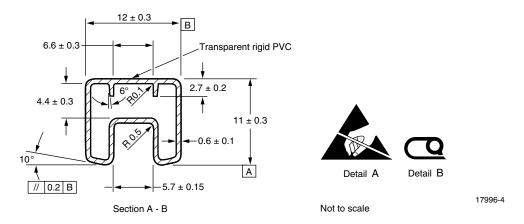


Fig. 14 - Tube Shipping Medium

DIP-6, Option 6

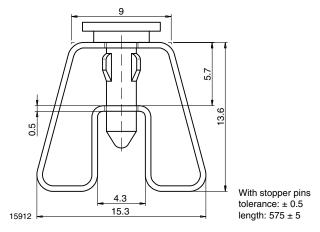


Fig. 15 - Tube Shipping Medium

Tape and Reel

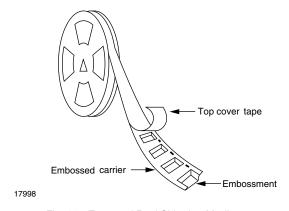


Fig. 16 - Tape and Reel Shipping Medium

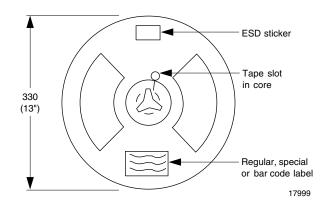


Fig. 17 - Tape and Reel Shipping Medium

SMD-6, Option 7

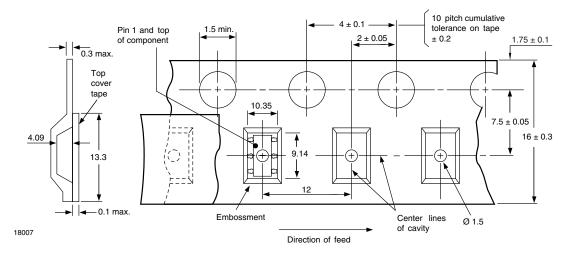


Fig. 18 - Tape and Reel Packing (1000 pieces on Reel)

SOLDER PROFILES

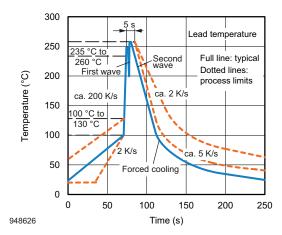


Fig. 19 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices

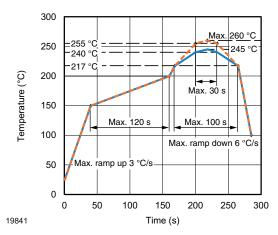


Fig. 20 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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