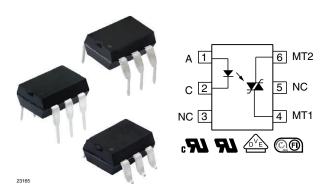


## Optocoupler, Phototriac Output, High dV/dt, Low Input Current



#### **LINKS TO ADDITIONAL RESOURCES**













#### **DESCRIPTION**

The VO4257 and VO4258 phototriac consists of a GaAs IRLED optically coupled to a photosensitive non-zero crossing TRIAC packaged in a DIP-6 package.

High input sensitivity is achieved by using an emitter follower phototransistor and a cascaded SCR predriver resulting in an LED trigger current of 1.6 mA for bin D, 2 mA for bin H, and 3 mA for bin M.

The new non zero phototriac family use a proprietary dV/dt clamp resulting in a static dV/dt of greater than 5 kV/µs.

The VO4257, VO4258 phototriac isolates low-voltage logic from 120 VAC, 240 VAC, and 380 VAC lines to control resistive, inductive, or capacitive loads including motors, solenoids, high current thyristors or TRIAC and relays.

### **FEATURES**

- High static dV/dt 5 kV/µs
- High input sensitivity I<sub>FT</sub> = 1.6 mA, 2 mA, and 3 mA
- 700 and 800 V blocking voltage
- 300 mA on-state current
- Isolation rated voltage 4420 V<sub>RMS</sub>



# Pb-free



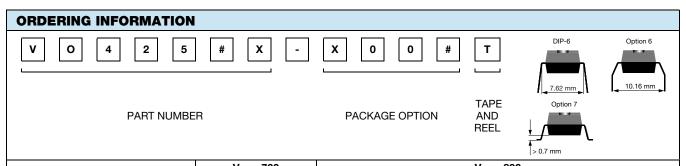
# COMPLIAN

### **APPLICATIONS**

- Solid-state relays
- · Industrial controls
- · Office equipment
- Consumer appliances

### **AGENCY APPROVALS**

- UL
- cUL
- DIN EN 60747-5-5 (VDE 0884-5), available with option 1
- FIMKO



AGENCY CERTIFIED / PACKAGE	V <sub>DRM</sub> 700	V <sub>DRM</sub> 800				
AGENCT CERTIFIED / FACKAGE	TRIGGER CURRENT, I <sub>FT</sub> (mA)					
UL, cUL, BSI, FIMKO	3	1.6	2	3		
DIP-6	VO4257M	VO4258D	VO4258H	VO4258M		
DIP-6, 400 mil, option 6	-	-	VO4258H-X006	-		
SMD-6, option 7	-	VO4258D-X007T	VO4258H-X007T	VO4258M-X007T		
VDE, UL, cUL, BSI, FIMKO	3	1.6	2	3		
SMD-6, option 7	-	_	-	-		



www.vishay.com

# Vishay Semiconductors

ABSOLUTE MAXIMUM RATINGS (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	VALUE	UNIT		
INPUT							
Reverse voltage			$V_{R}$	6	V		
Forward current			I <sub>F</sub>	60	mA		
Derate from 25 °C				1.33	mW/°C		
OUTPUT							
Deals off state wells as		VO4257D/H/M	$V_{DRM}$	700	V		
Peak off-state voltage		VO4258D/H/M	$V_{DRM}$	800	V		
RMS on-state current			I <sub>TM</sub>	300	mA		
Derate from 25 °C				6.6	mW/°C		
COUPLER							
Storage temperature range			T <sub>stg</sub>	-55 to +150	°C		
Ambient temperature range			T <sub>amb</sub>	-55 to +100	°C		
Soldering temperature	Max. ≤ 10 s dip soldering ≥ 0.5 mm from case bottom		T <sub>sld</sub>	260	°C		

#### Note

Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
maximum ratings for extended periods of the time can adversely affect reliability

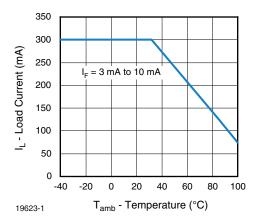


Fig. 1 - Recommended Operating Condition



THERMAL CHARACTERISTICS			
PARAMETER	SYMBOL	VALUE	UNIT
LED power dissipation	P <sub>diss</sub>	100	mW
Output power dissipation	P <sub>diss</sub>	500	mW
Total power dissipation	P <sub>tot</sub>	600	mW
Maximum LED junction temperature	T <sub>jmax.</sub>	125	°C
Maximum output die junction temperature	T <sub>jmax.</sub>	125	°C
Thermal resistance, junction emitter to board	$\theta_{JEB}$	150	°C/W
Thermal resistance, junction emitter to case	$\theta_{JEC}$	139	°C/W
Thermal resistance, junction detector to board	$\theta_{JDB}$	78	°C/W
Thermal resistance, junction detector to case	$\theta_{JDC}$	103	°C/W
Thermal resistance, junction emitter to junction detector	$\theta_{JED}$	496	°C/W
Thermal resistance, case to ambient	$\theta_{CA}$	3563	°C/W

#### Note

The thermal characteristics table above were measured at 25 °C and the thermal model is represented in the thermal network below. Each
resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal
resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation
of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note

<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT								
Forward voltage	I <sub>F</sub> = 10 mA		V <sub>F</sub>	-	1.2	1.4	V	
Reverse current	V <sub>R</sub> = 6 V		I <sub>R</sub>	-	0.1	10	μΑ	
Input capacitance	$V_F = 0 V, f = 1 MHz$		C <sub>I</sub>	-	40	-	pF	
OUTPUT								
Denotitive needs off state voltage	I <sub>DRM</sub> = 100 μA	VO4257D/H/M	$V_{DRM}$	700	-	-	V	
Repetitive peak off-state voltage		VO4258D/H/M	$V_{DRM}$	800	-	-	V	
Off-state current	$V_D = V_{DRM}$		I <sub>DRM</sub>	-	-	100	μΑ	
On-state voltage	I <sub>T</sub> = 300 mA		$V_{TM}$	-	-	3	V	
On-current	PF = 1, V <sub>T(RMS)</sub> = 1.7 V		I <sub>TM</sub>	-	-	300	mA	
Critical state of rise of off-state voltage	$V_D = 0.67 \ V_{DRM}, \ T_J = 25 \ ^{\circ}C$		dV/dt <sub>cr</sub>	5000	-		V/µs	
COUPLER								
LED trigger current, current required to latch output		VO4257D	I <sub>FT</sub>	-	-	1.6	mA	
		VO4257H I <sub>FT</sub> -	-	2	mA			
	V - 2 V	VO4257M	I <sub>FT</sub>	-	-	3	mA	
	V <sub>D</sub> = 3 V	VO4258D	I <sub>FT</sub>	-	-	1.6	mA	
		VO4258H	I <sub>FT</sub>	-	-	2	mA	
		VO4258M	I <sub>FT</sub>	-	-	3	mA	
Capacitance (input to output)	f = 1 MHz, V <sub>IO</sub> = 0 V		C <sub>IO</sub>	-	0.8	-	pF	

### Note

• Minimum and maximum values were tested requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements



SAFETY AND INSULATION RATINGS							
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT			
Climatic classification	According to IEC 68 part 1		55 / 100 / 21				
Comparative tracking index		CTI	175				
Maximum rated withstanding isolation voltage	t = 1 min	V <sub>ISO</sub>	4420	V <sub>RMS</sub>			
Maximum transient isolation voltage		V <sub>IOTM</sub>	8000	V <sub>peak</sub>			
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	890	V <sub>peak</sub>			
Leadath a seatalance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 25 °C	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω			
Isolation resistance	V <sub>IO</sub> = 500 V, T <sub>amb</sub> = 100 °C	R <sub>IO</sub>	≥ 10 <sup>11</sup>	Ω			
Output safety power		P <sub>SO</sub>	500	mW			
Input safety current		I <sub>SI</sub>	250	mA			
Safety temperature		T <sub>S</sub>	175	°C			
Creepage distance			≥ 7	mm			
Clearance distance			≥ 7	mm			
Insulation thickness		DTI	≥ 0.4	mm			
Pollution degree (DIN VDE 0109)			2				

#### Note

### TYPICAL CHARACTERISTICS (T<sub>amb</sub> = 25 °C, unless otherwise specified)

Fig. 3 - Diode Reverse Voltage vs. Temperature

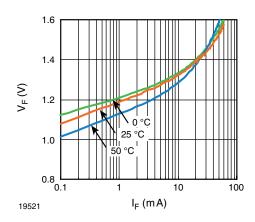


Fig. 2 - Diode Forward Voltage vs. Forward Current

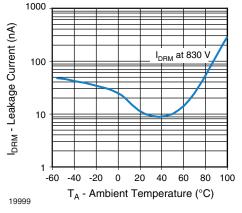
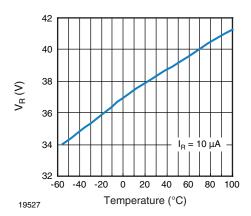


Fig. 4 - Leakage Current vs. Ambient Temperature



<sup>•</sup> As per IEC 60747-5-5, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits

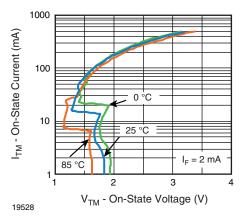


Fig. 5 - Output On Current (I<sub>TM</sub>) vs. Voltage

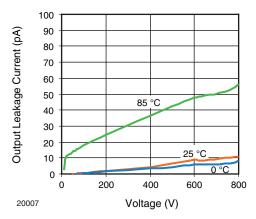


Fig. 6 - Output Off Current (Leakage) vs. Voltage

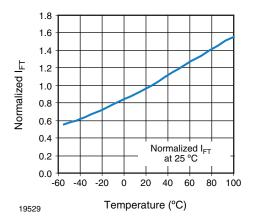


Fig. 7 - Normalized Trigger Input Current vs. Temperature

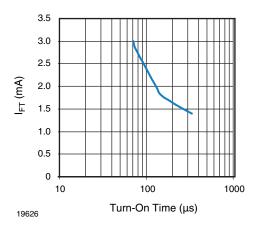


Fig. 8 - Trigger Current vs. Turn-On Time

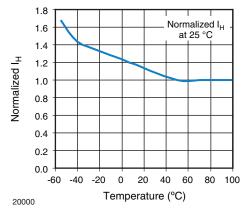


Fig. 9 - Normalized Holding Current vs. Temperature

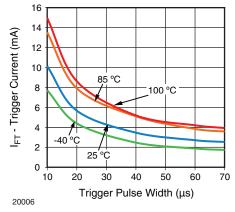
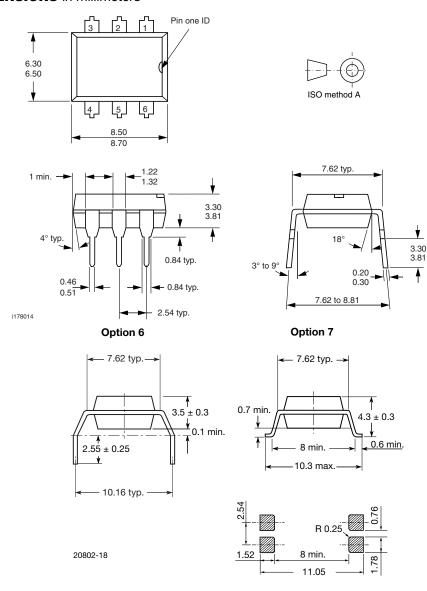


Fig. 10 - I<sub>FT</sub> vs. LED Pulse Width



### **PACKAGE DIMENSIONS** in millimeters



### PACKAGE MARKING (example)



### Note

• VDE logo is only marked on option 1 parts. Tape and reel suffix (T) is not part of the package marking



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Vishay

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