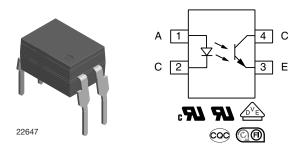


### Vishay Semiconductors

# Low Input Current Optocoupler, Phototransistor Output, High Reliability, 5300 V<sub>RMS</sub>



#### **DESCRIPTION**

The 110 °C rated VO615C series feature a high current transfer ratio, low coupling capacitance and high isolation voltage. These couplers have a GaAs infrared diode emitter, which is optically coupled to a silicon planar phototransistor detector, and is incorporated in a plastic DIP-4 package.

The coupling devices are designed for signal transmission between two electrically separated circuits.

#### **FEATURES**

- · Copper lead-frame
- Operating temperature from -55 °C to +110 °C
- Isolation test voltage, 5300 V<sub>RMS</sub>
- High collector emitter voltage, V<sub>CEO</sub> = 80 V
- · Low saturation voltage
- · Fast switching times
- Low CTR degradation
- · Low coupling capacitance
- End stackable, 0.100" (2.54 mm) spacing
- Material categorization: for definitions of compliance please see <a href="https://www.vishav.com/doc?99912">www.vishav.com/doc?99912</a>

# Phofree



### RoHS COMPLIANT

FREE GREEN

#### <u>GREEN</u> (5-2008)

#### **APPLICATIONS**

- AC adapters
- SMPS
- PLC
- · Factory automation
- · Game consoles

#### **AGENCY APPROVALS**

The safety application model number covering all products in this datasheet is VO615C. This model number should be used when consulting safety agency documents.

- UL1577, file no. E52744
- cUL tested to CSA 22.2 bulletin 5A
- DIN EN 60747-5-5 (VDE 0884), available with option 1
- FIMKO EN60065 and EN60950-1, file no. FI 27409
- CQC GB8898-2001

#### ORDERING INFORMATION DIP-4 Option 6 ٧ 0 6 1 5 С Χ 0 T 7.62 mm 10.16 mm PART NUMBER **CTR** PACKAGE OPTION TAPE RIN AND Option 9 REEL **CTR (%)** AGENCY CERTIFIED/PACKAGE 10 mA UL. cUL. FIMKO, CQC 40 to 80 63 to 125 100 to 200 160 to 320 VO615C-1 VDE, UL, cUL, FIMKO, CQC (option 1) 40 to 80 63 to 125 100 to 200 160 to 320 VO615C-3X016 VO615C-4X016 DIP-4, 400 mil, option 6 VO615C-2X016 VO615C-2X019T. SMD-4, option 9 VO615C-2X019T1 (1)

#### Notes

Rev. 1.3, 17-Sep-14

- Additional options may be available, please contact the sales office.
- (1) T1 rotation in tape and reel packaging.



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<b>ABSOLUTE MAXIMUM RATINGS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT		
INPUT						
Reverse voltage		$V_R$	6	V		
Forward current		I <sub>F</sub>	60	mA		
Forward surge current	t <sub>p</sub> ≤ 10 μs	I <sub>FSM</sub>	1.5	Α		
Power dissipation	at 25 °C	P <sub>diss</sub>	70	mW		
OUTPUT			·			
Collector emitter voltage		V <sub>CEO</sub>	80	V		
Emitter collector voltage		V <sub>ECO</sub>	7	V		
Collector current		· I <sub>C</sub>	50	mA		
	t <sub>p</sub> ≤ 1 ms		100	mA		
Output power dissipation	at 25 °C	P <sub>diss</sub>	150	mW		
COUPLER			·			
Isolation test voltage (RMS)	t = 1 min	V <sub>ISO</sub>	5300	$V_{RMS}$		
Total power dissipation		P <sub>tot</sub>	200	mW		
Operation temperature		T <sub>amb</sub>	-55 to +110	°C		
Storage temperature range		T <sub>stg</sub>	-55 to +150	°C		
Junction temperature		T <sub>j</sub>	125	°C		
Soldering temperature (1)	2 mm from case, ≤ 10 s	T <sub>sld</sub>	260	°C		

#### **Notes**

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not
  implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute
  maximum ratings for extended periods of the time can adversely affect reliability.
- (1) Refer to reflow profile for soldering conditions for surface mounted devices (SMD), and wave profile for soldering conditions for through hole devices (DIP), please go to "Assembly Instructions" (<a href="https://www.vishay.com/doc?80054">www.vishay.com/doc?80054</a>).

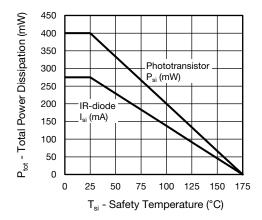


Fig. 1 - Total Power Dissipation vs. Safety Temperature

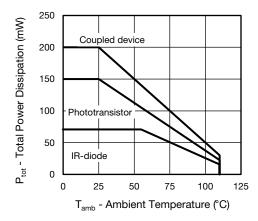


Fig. 2 - Total Power Dissipation vs. Ambient Temperature



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<b>ELECTRICAL CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)								
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT		
INPUT	INPUT							
Forward voltage	$I_F = 60 \text{ mA}$	$V_{F}$		1.35	1.6	V		
Reverse current	V <sub>R</sub> = 6 V	$I_{R}$		0.01	10	μΑ		
Junction capacitance $V_R = 0 \text{ V}, f = 1 \text{ MHz}$ $C_j$ 9			pF					
OUTPUT								
Collector emitter leakage current	V <sub>CE</sub> = 10 V	I <sub>CEO</sub>		0.3	100	nA		
Collector emitter capacitance	$V_{CE} = 5 \text{ V, f} = 1 \text{ MHz}$	$C_{CE}$		2.8		pF		
Collector emitter breakdown voltage	I <sub>C</sub> = 1 mA	$BV_{CEO}$	80			V		
Emitter collector breakdown voltage	I <sub>E</sub> = 100 μA	$BV_{ECO}$	7			V		
COUPLER								
Collector emitter saturation voltage	$I_F = 10 \text{ mA}, I_C = 2.5 \text{ mA}$	$V_{CEsat}$		0.25	0.4	V		
Coupling capacitance	f = 1 MHz	C <sub>C</sub>		0.4		pF		
Cut-off frequency	$I_F$ = 10 mA, $V_{CC}$ = 5 V, $R_L$ = 100 $\Omega$	f <sub>ctr</sub>		110		kHz		

#### Note

 Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering evaluation. Typical values are for information only and are not part of the testing requirements.

CURRENT TRANSFER RATIO (T <sub>amb</sub> = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	PART	SYMBOL	MIN.	TYP.	MAX.	UNIT
I <sub>C</sub> /I <sub>F</sub>		VO615C-1	CTR	40		80	%
	I10 m/ \/ 5 \/	VO615C-2	CTR 63	63		125	%
	$I_F = 10 \text{ mA}, V_{CE} = 5 \text{ V}$	VO615C-3	CTR	100		200	%
		VO615C-4	CTR	160		320	%

<b>SWITCHING CHARACTERISTICS</b> (T <sub>amb</sub> = 25 °C, unless otherwise specified)						
PARAMETER TEST CONDITION SYMBOL MIN. TYP. MAX.						UNIT
NON-SATURATED						
Rise time	L 2 A V	t <sub>r</sub>		3		μs
Fall time		t <sub>f</sub>		3		μs
Turn-on time	$I_C = 2 \text{ mA}, V_{CC} = 5 \text{ V}, R_L = 100 \Omega$	t <sub>on</sub>		6		μs
Turn-off time		t <sub>off</sub>		4		μs

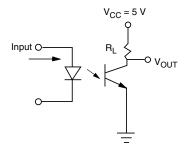


Fig. 3 - Test Circuit

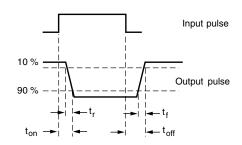


Fig. 4 - Test Circuit and Waveforms



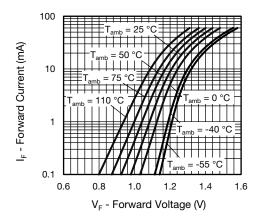
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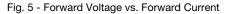
### Vishay Semiconductors

SAFETY AND INSULATION RATINGS					
PARAMETER	SYMBOL	VALUE	UNIT		
MAXIMUM SAFETY RATINGS					
Output safety power		P <sub>SO</sub>	700	mW	
Input safety current		I <sub>si</sub>	400	mW	
Safety temperature		T <sub>S</sub>	175	°C	
Comparative tracking index	CTI	175			
INSULATION RATED PARAMETERS					
Maximum withstanding isolation voltage	V <sub>ISO</sub>	5300	V <sub>RMS</sub>		
Maximum transient isolation voltage		V <sub>IOTM</sub>	8000	V <sub>peak</sub>	
Maximum repetitive peak isolation voltage		V <sub>IORM</sub>	565	V <sub>peak</sub>	
		V <sub>IORM</sub> (1)	1140	V <sub>peak</sub>	
Insulation resistance	$T_{amb} = 25  ^{\circ}C,  V_{DC} = 500  V$	R <sub>IO</sub>	≥ 10 <sup>12</sup>	Ω	
Isolation resistance	Isolation resistance $T_{amb} = 100  ^{\circ}\text{C},  V_{DC} = 500  \text{V}$		≥ 10 <sup>11</sup>	Ω	
Climatic classification (according to IEC 68		55/110/21			
Environment (pollution degree in accordance		2			
Internal and external creepage	Standard DIP-4		≥ 7	mm	
internal and external creepage	400 mil DIP-4		≥ 8	mm	
Clearance	Standard DIP-4		≥ 7	mm	
Olearance	400 mil DIP-4		≥ 8	mm	
Insulation thickness		0.4	mm		

#### **Notes**

#### **TYPICAL CHARACTERISTICS** (T<sub>amb</sub> = 25 °C, unless otherwise specified)





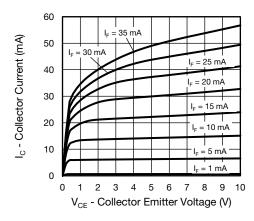


Fig. 6 - Collector Current vs. Collector Emitter Voltage (NS)

<sup>•</sup> As per DIN EN 60747-5-5, 2, § 7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with the safety ratings shall be ensured by means of protective circuits.

<sup>(1)</sup> Only for option 6.





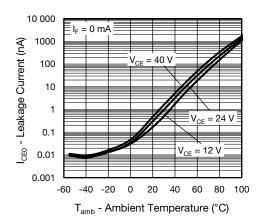


Fig. 7 - Leakage Current vs. Ambient Temperature

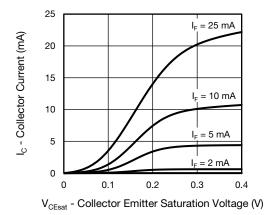


Fig. 8 - Collector Current vs. Collector Emitter Voltage (saturated)

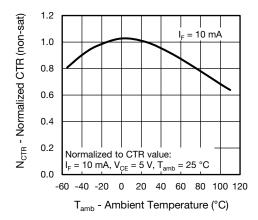


Fig. 9 - Normalized Current Transfer Ratio (non-saturated) vs.

Ambient Temperature

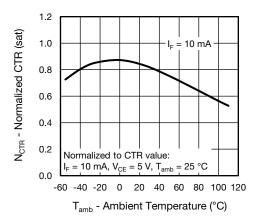


Fig. 10 - Normalized Current Transfer Ratio (saturated) vs.
Ambient Temperature

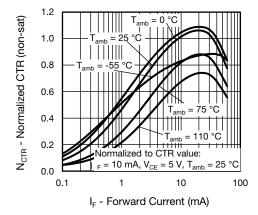


Fig. 11 - Normalized CTR (non-saturated) vs. Forward Current

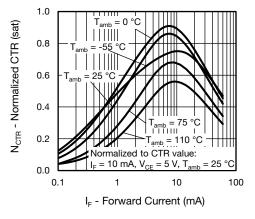


Fig. 12 - Normalized CTR (saturated) vs. Forward Current



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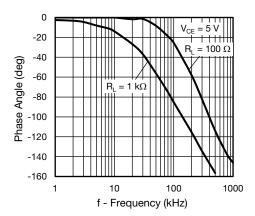


Fig. 13 - Phase Angle vs. Frequency

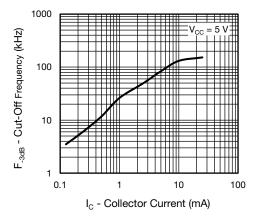


Fig. 14 - Frequency vs. Collector Current

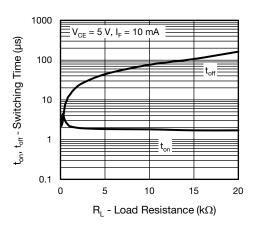


Fig. 15 - Switching Time vs. Load Resistance

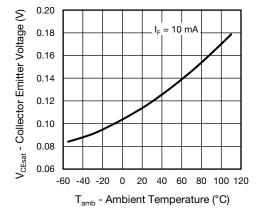
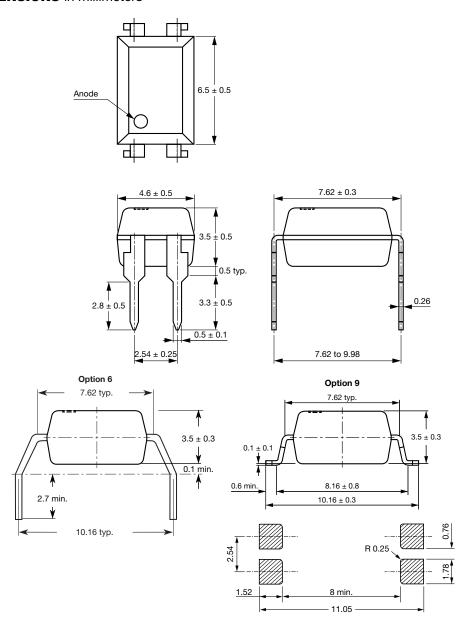


Fig. 16 - Collector Emitter Voltage vs. Ambient Temperature (saturated)





#### **PACKAGE DIMENSIONS** in millimeters



### PACKAGE MARKING (Example of VO615C-3X016)



#### Note

• Option information is not marked.



# Vishay Semiconductors

#### **PACKING INFORMATION**

DEVICE PER TUBE						
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX			
DIP-4	100	40	4000			

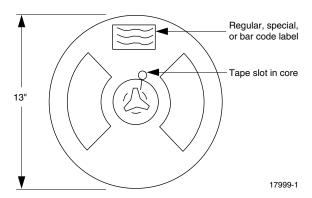


Fig. 17 - Tape and Reel Shipping Medium

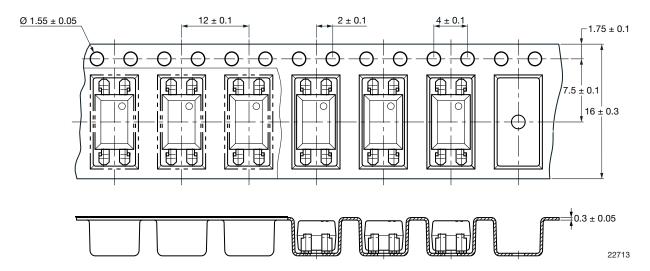


Fig. 18 - Tape Packing for Option 7 and 9, T1 rotation (2000 units per reel)



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Revision: 02-Oct-12 Document Number: 91000

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