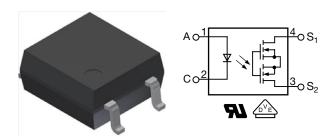
RoHS

COMPLIANT



Vishay Semiconductors

1 Form A Solid-State Relay

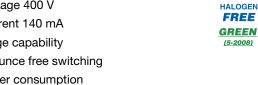


DESCRIPTION

The VOR1142 is an SPST normally open switch (1 form A) that can replace electromechanical relays in many applications. It is constructed using a GaAlAs IRED actuation control and MOSFETs for the switch output.

FEATURES

- · Current limit protection
- Isolation test voltage 3750 V_{RMS}
- Typical R_{ON} 22 Ω
- Load voltage 400 V
- Load current 140 mA
- · High surge capability
- · Clean bounce free switching
- Low power consumption
- High temperature range
- · Material categorization: for definitions of compliance please see www.vishav.com/doc?99912

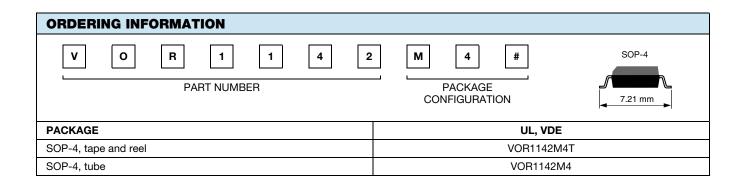


APPLICATIONS

- · General telecom switching
- Metering
- Security equipment
- Instrumentation
- · Industrial controls
- Battery management systems
- Automatic measurement equipment

AGENCY APPROVALS

- UL1577, file no. E52744
- DIN EN 60747-5-5 (VDE0884-5)





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ABSOLUTE MAXIMUM RATINGS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	CONDITIONS	SYMBOL	VALUE	UNIT			
INPUT							
IRED continuous forward current		I _F	50	mA			
IRED reverse voltage		V _R	5	V			
Input power dissipation		P _{diss}	80	mW			
OUTPUT							
DC or peak AC load voltage		V _L	400	V			
Continuous DC load current		ΙL	140	mA			
SSR output power dissipation		P _{diss}	550	mW			
SSR							
Ambient temperature range (1)		T _{amb}	-40 to +100	°C			
Storage temperature range		T _{stg}	-40 to +150	°C			
Soldering temperature	t = 10 s max.	T _{sld}	260	°C			

Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
- (1) For continuous negative potential from output side to input side only 85 °C is allowed.

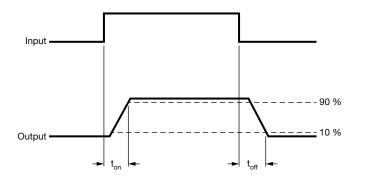
ELECTRICAL CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)							
PARAMETER	TEST CONDITION	SYMBOL	MIN.	TYP.	MAX.	UNIT	
INPUT							
IRED forward current, switch turn-on	I _L = 100 mA, t = 10 ms	I _{Fon}	-	0.25	2	mA	
IRED forward current, switch turn-off	$V_L = \pm 350 \text{ V}, I_L < 1 \mu\text{A}$	I _{Foff}	0.05	0.15	-	mA	
IRED forward voltage	I _F = 10 mA	V _F	-	1.36	1.5	V	
IRED reverse current	V _R = 5 V I _R -		-	-	10	μΑ	
OUTPUT							
On-resistance	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	R _{ON}	-	22	27	Ω	
Off-resistance	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	R _{OFF}	0.5	850	-	GΩ	
Off-state leakage current	$I_F = 0 \text{ mA}, V_L = \pm 100 \text{ V}$	I _{leak}	ı	< 1	100	nA	
	$I_F = 0 \text{ mA}, V_L = \pm 400 \text{ V}$	I _{leak}	-	6	500	nA	
Output capacitance	I _F = 0 mA, V _L = 1 V, 1 MHz	Co	-	39	-	pF	
	$I_F = 0 \text{ mA}, V_L = 50 \text{ V}, 1 \text{ MHz}$	Co	-	6	-	pF	
Current limit AC/DC	$I_F = 5 \text{ mA}, t = 5 \text{ ms}, V_L = \pm 6 \text{ V}$	I _{limit}	170	300	450	mA	
COUPLER							
Capacitance (input to output)	V _{IO} = 1 V	C _{IO}	-	0.4	-	pF	

Note

Minimum and maximum values are testing requirements. Typical values are characteristics of the device and are the result of engineering
evaluations. Typical values are for information only and are not part of the testing requirements.



SWITCHING CHARACTERISTICS (T _{amb} = 25 °C, unless otherwise specified)						
PARAMETER	TEST CONDITION SYMBOL MIN.		TYP.	MAX.	UNIT	
Turn-on time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t _{on}	-	0.2	0.5	ms
Turn-off time	$I_F = 5 \text{ mA}, I_L = 50 \text{ mA}$	t _{off}	-	0.05	0.2	ms



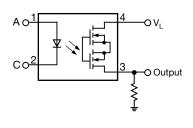


Fig. 1 - Timing Schematic

PARAMETER	TEST CONDITION	SYMBOL	VALUE	UNIT
Climatic classification	According to IEC 68 part 1		40/100/21	
Pollution degree	According to DIN VDE 0109		2	
Comparative tracking index		CTI	175	
Maximum rated withstanding isolation voltage	According to UL1577, t = 1 min	V _{ISO}	3750	V_{RMS}
Maximum transient isolation voltage	According to DIN EN 60747-5-5	V_{IOTM}	6000	V _{peak}
Maximum repetitive peak isolation voltage	According to DIN EN 60747-5-5	V _{IORM}	707	V _{peak}
Isolation resistance	$T_{amb} = 25 ^{\circ}\text{C}, V_{IO} = 500 \text{V}$	R _{IO}	≥ 10 ¹²	Ω
	$T_{amb} = 100 ^{\circ}\text{C}, V_{IO} = 500 \text{V}$	R _{IO}	≥ 10 ¹¹	Ω
Output safety power		P _{SO}	550	mW
Input safety current		I _{SI}	180	mA
Input safety temperature		T _S	175	°C
Clearance distance	SOP-4		≥ 5	mm
Creepage distance	SOP-4		≥ 5	mm
Insulation thickness		DTI	≥ 0.3	mm
Input to output test voltage, method B	V_{IORM} x 1.875 = V_{PR} , 100 % production test with t_M = 1 s, partial discharge < 5 pC		1326	V _{peak}
Input to output test voltage, method A	$V_{IORM} \times 1.6 = V_{PR}$, sample test with $t_M = 10 \text{ s}$, partial discharge $< 5 \text{ pC}$	V _{PR}	1131	V _{peak}

Note

As per IEC 60747-5-5, §7.4.3.8.2, this optocoupler is suitable for "safe electrical insulation" only within the safety ratings. Compliance with
the safety ratings shall be ensured by means of protective circuits.

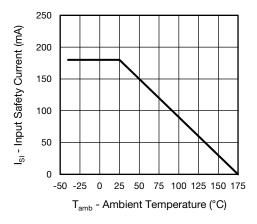


Fig. 2 - Safety Input Current vs. Ambient Temperature

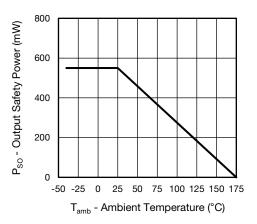


Fig. 3 - Safety Power Dissipation vs. Ambient Temperature

TYPICAL CHARACTERISTICS (T_{amb} = 25 °C, unless otherwise specified)

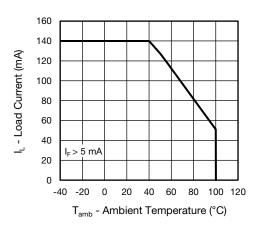


Fig. 4 - Maximum Load Current vs. Ambient Temperature

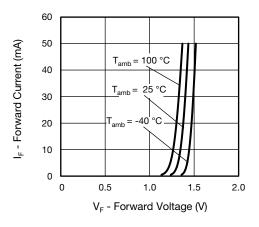


Fig. 6 - Forward Current vs. Forward Voltage

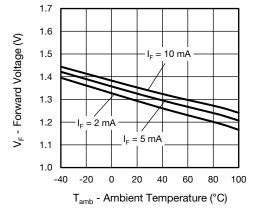


Fig. 5 - Forward Voltage vs. Ambient Temperature

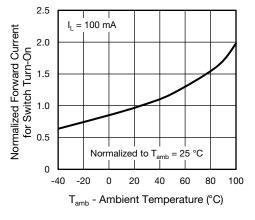
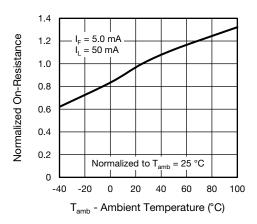


Fig. 7 - Normalized Forward Current for Switch Turn-On vs.

Ambient Temperature



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Fig. 8 - Normalized On-Resistance vs. Ambient Temperature

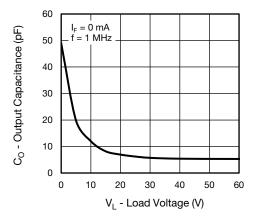


Fig. 9 - Output Capacitance vs. Load Voltage

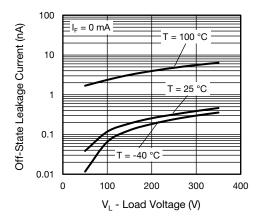


Fig. 10 - Off-State Leakage Current vs. Load Voltage

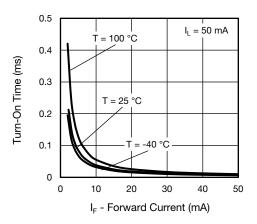


Fig. 11 - Turn-On Time vs. Forward Current

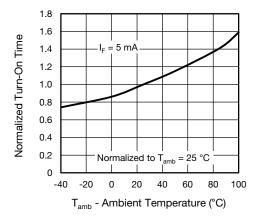


Fig. 12 - Normalized Turn-On Time vs. Ambient Temperature

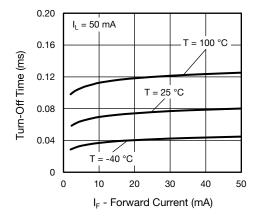


Fig. 13 - Turn-Off Time vs. Forward Current



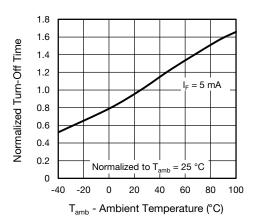
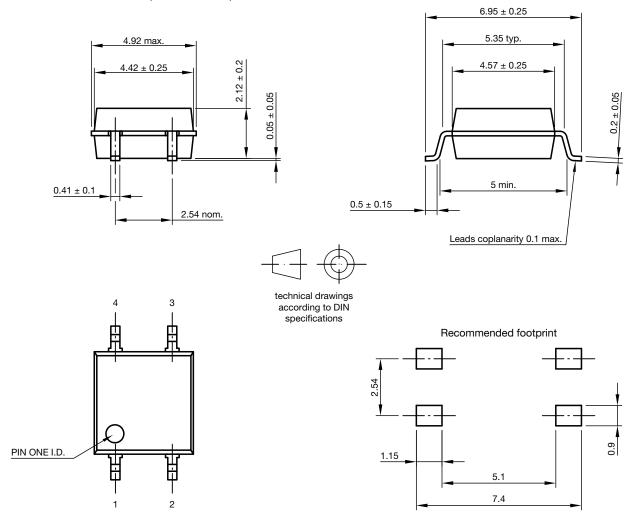


Fig. 14 - Normalized Turn-Off Time vs. Ambient Temperature

PACKAGE DIMENSIONS (in millimeters)



Drawing-No.: 6.544-5415.01-4

Issue: 2; 23.07.12

Fig. 15 - Package Drawing

PACKAGE MARKING

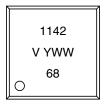
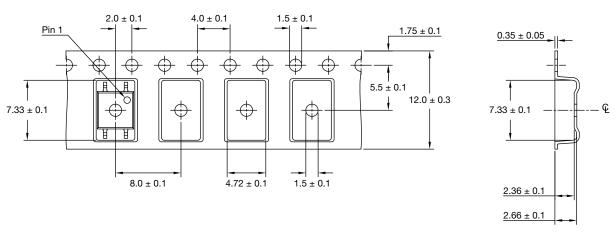


Fig. 16 - VOR1142M4

Note

· Package configuration (T, M) are not part of the package marking.

PACKAGING INFORMATION (in millimeters)



Note:

• Cummulative tolerance of 10 spocket holes is 0.20 mm

Fig. 17 - Tape and Reel Packing (2000 pieces on reel)

DEVICE PER TUBE						
TYPE	UNITS/TUBE	TUBES/BOX	UNITS/BOX			
SOP-4	100	40	4000			

SOLDER PROFILES

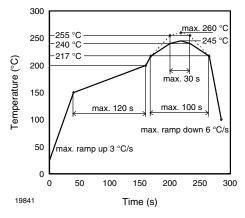


Fig. 18 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2 Floor life: unlimited

Conditions: T_{amb} < 30 °C, RH < 85 %

Moisture sensitivity level 1, according to J-STD-020



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Revision: 02-Oct-12 Document Number: 91000

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AQV238AD01 AQW414TS AQY221N2SYD01 AQY221R2VJ AQY275AXJ AQY414SXE01 G2-1A02-ST G2-1A03-ST G2-1A03-TT

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