## Widebody 2.5 A IGBT and MOSFET Driver



## DESCRIPTION

The VOW3120 consists of an infrared light emitting diode optically coupled to an integrated circuit with a power output stage. This optocoupler is ideally suited for driving power IGBTs and MOSFETs used in motor control and inverter applications. The high operating voltage range of the output stage provides the drive voltages required by gate controlled devices. The voltage and current supplied by this optocoupler makes it ideally suited for directly driving IGBTs with ratings up to $1200 \mathrm{~V} / 100 \mathrm{~A}$. For IGBTs with higher ratings, the VOW3120 can be used to drive a discrete power stage which drives the IGBT gate.
The VOW3120 provides higher isolation for applications operating at higher working voltages, and or higher pollution degree criteria. Higher $\mathrm{V}_{\text {IORM }}$, $\mathrm{V}_{\text {IOTM }}$, creepage and clearance distances, make the VOW3120 ideal for many industrial control and power conversion applications.

## FEATURES

- 2.5 A minimum peak output current
- 10 mm minimum external creepage distance
- $50 \mathrm{kV} / \mu \mathrm{s}$ (typ.) common mode rejection
- $I_{\mathrm{CC}}=2.5 \mathrm{~mA}$ maximum supply current
- Under voltage lock-out (UVLO) with hysteresis
- Wide operating $\mathrm{V}_{\mathrm{Cc}}$ range: 15 V to 32 V
- Industrial temperature range: $-40^{\circ} \mathrm{C}$ to $+100^{\circ} \mathrm{C}$
- Material categorization:
 for definitions of compliance please see www.vishay.com/doc?99912


## APPLICATIONS

- Industrial welding equipment
- Motor drives
- Industrial inverters
- Commercial and residential solar inverters
- Wind generator inverters
- EV and plug-in HEV chargers


## AGENCY APPROVALS

All parts are certified under base model VOW3120. This model number should be used when consulting safety agency documents.

- UL1577
- cUL
- CQC
- DIN EN 60747-5-5 (VDE 0884-5)


VOW3120

| ABSOLUTE MAXIMUM RATINGS ( $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$, unless otherwise specified) |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: |
| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT |
| INPUT |  |  |  |  |
| Input forward current |  | $\mathrm{I}_{\mathrm{F}}$ | 25 | mA |
| Peak transient input current | $<1 \mu \mathrm{~s}$ pulse width, 300 pps | $\mathrm{I}_{\text {F(TRAN })}$ | 1 | A |
| Reverse input voltage |  | $\mathrm{V}_{\mathrm{R}}$ | 5 | V |
| Input power dissipation |  | $\mathrm{P}_{\text {diss }}$ | 40 | mW |
| LED junction temperature |  | $\mathrm{T}_{\mathrm{j}}$ | 125 | ${ }^{\circ} \mathrm{C}$ |
| OUTPUT |  |  |  |  |
| High peak output current ${ }^{(1)}$ |  | $\mathrm{I}_{\text {OH(PEAK }}$ | 2.5 | A |
| Low peak output current ${ }^{(1)}$ |  | 1 OLPEAK ) | 2.5 | A |
| Supply voltage |  | $\left(\mathrm{V}_{\text {CC }}-\mathrm{V}_{\text {EE }}\right)$ | 0 to +35 | V |
| Output voltage |  | $\mathrm{V}_{\text {O(PEAK) }}$ | 0 to $+\mathrm{V}_{\mathrm{Cc}}$ | V |
| Output power dissipation |  | $\mathrm{P}_{\text {diss }}$ | 220 | mW |
| Output junction temperature |  | $\mathrm{T}_{\mathrm{j}}$ | 125 | ${ }^{\circ} \mathrm{C}$ |
| OPTOCOUPLER |  |  |  |  |
| Storage temperature range |  | $\mathrm{T}_{\text {stg }}$ | -55 to +150 | ${ }^{\circ} \mathrm{C}$ |
| Ambient operating temperature range |  | $\mathrm{T}_{\text {amb }}$ | -40 to +100 | ${ }^{\circ} \mathrm{C}$ |
| Total power dissipation |  | $\mathrm{P}_{\text {tot }}$ | 260 | mW |
| Lead solder temperature | For $10 \mathrm{~s}, 1.6 \mathrm{~mm}$ below seating plane | $\mathrm{T}_{\text {sld }}$ | 260 | ${ }^{\circ} \mathrm{C}$ |

## Notes

- Stresses in excess of the absolute maximum ratings can cause permanent damage to the device. Functional operation of the device is not implied at these or any other conditions in excess of those given in the operational sections of this document. Exposure to absolute maximum ratings for extended periods of the time can adversely affect reliability.
(1) Maximum pulse width $=10 \mu \mathrm{~s}$, maximum duty cycle $=0.2 \%$. This value is intended to allow for component tolerances for designs with $l_{0}$ peak minimum $=2.5 \mathrm{~A}$. See applications section for additional details on limiting $\mathrm{l}_{\mathrm{OH}}$ peak.

|  |  |  |  |  |  |
| :--- | :---: | :---: | :---: | :---: | :---: |
| RECOMMENDED OPERATING CONDITION |  |  |  |  |  |
| PARAMETER | SYMBOL | MIN. | MAX. | UNIT |  |
| Power supply voltage | $\mathrm{V}_{\mathrm{CC}}-\mathrm{V}_{\text {EE }}$ | 15 | 32 | V |  |
| Input LED current (on) | $\mathrm{I}_{\mathrm{F}}$ | 10 | - | mA |  |
| Input voltage (off) | $\mathrm{V}_{\text {F(OFF) }}$ | -3 | 0.8 | V |  |
| Operating temperature | $\mathrm{T}_{\mathrm{amb}}$ | -40 | +100 | ${ }^{\circ} \mathrm{C}$ |  |



Fig. 1 - Dissipated Operating Power vs. Operating Temperature

VOW3120
Vishay Semiconductors

## THERMAL CHARACTERISTICS

| PARAMETER | SYMBOL | VALUE | UNIT |  |
| :---: | :---: | :---: | :---: | :---: |
| LED power dissipation | $\mathrm{P}_{\text {Led }}$ | 40 | mW | $\mathrm{T}_{\mathrm{JE}}$ |
| Output power dissipation | Pout | 220 | mW | Mor |
| Total power dissipation | $\mathrm{P}_{\text {TOT }}$ | 260 | mW | N |
| Maximum LED junction temperature | $\mathrm{T}_{\mathrm{j} \text { max. }}$ | 125 | ${ }^{\circ} \mathrm{C}$ | - |
| Maximum output die junction temperature | $\mathrm{T}_{\mathrm{j} \text { max. }}$. | 125 | ${ }^{\circ} \mathrm{C}$ |  |
| Thermal resistance, LED to output | $\theta_{\text {ED }}$ | 315 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |
| Thermal resistance, LED to board | $\theta_{\text {EB }}$ | 300 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |
| Thermal resistance, output to board | $\theta_{\text {DB }}$ | 80 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ |  |
| Thermal resistance, board to ambient | $\theta_{\text {BA }}$ | 50 | ${ }^{\circ} \mathrm{C} / \mathrm{W}$ | $\mathrm{T}_{\text {amb }}$ |

## Note

- The thermal characteristics table above were measured at $25^{\circ} \mathrm{C}$ and the thermal model is represented in the thermal network below. Each resistance value given in this model can be used to calculate the temperatures at each node for a given operating condition. The thermal resistance from board to ambient will be dependent on the type of PCB, layout and thickness of copper traces. For a detailed explanation of the thermal model, please reference Vishay's Thermal Characteristics of Optocouplers application note.


## ELECTRICAL CHARACTERISTICS

| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| High level output current | $\mathrm{V}_{\mathrm{O}}=\left(\mathrm{V}_{\mathrm{CC}}-4 \mathrm{~V}\right)$ | $\mathrm{IOH}^{\text {l }}$ | 0.5 | - | - | A |
|  | $\mathrm{V}_{\mathrm{O}}=\left(\mathrm{V}_{\mathrm{CC}}-15 \mathrm{~V}\right)$ | $\mathrm{IOH}^{\text {O }}$ | 2.5 | - | - | A |
| Low level output current | $\mathrm{V}_{\mathrm{O}}=\left(\mathrm{V}_{\mathrm{EE}}+2.5 \mathrm{~V}\right)$ | lOL | 0.5 | - | - | A |
|  | $\mathrm{V}_{\mathrm{O}}=\left(\mathrm{V}_{\mathrm{EE}}+15 \mathrm{~V}\right)$ | $\mathrm{l}_{\mathrm{OL}}$ | 2.5 | - | - | A |
| High level output voltage | $\mathrm{l}_{\mathrm{O}}=-100 \mathrm{~mA}$ | $\mathrm{V}_{\mathrm{OH}}$ | $\mathrm{V}_{C C}-4$ | - | - | V |
| Low level output voltage | $\mathrm{I}_{\mathrm{O}}=100 \mathrm{~mA}$ | $\mathrm{V}_{\text {OL }}$ | - | 0.2 | 0.5 | V |
| High level supply current | Output open, $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ to 16 mA | $\mathrm{I}_{\mathrm{COH}}$ | - | - | 2.5 | mA |
| Low level supply current | Output open, $\mathrm{V}_{\mathrm{F}}=-3 \mathrm{~V}$ to +0.8 V | $\mathrm{I}_{\mathrm{CCL}}$ | - | - | 2.5 | mA |
| Threshold input current low to high | $\mathrm{I}_{\mathrm{O}}=0 \mathrm{~mA}, \mathrm{~V}_{\mathrm{O}}>5 \mathrm{~V}$ | $\mathrm{IFLH}^{\text {F }}$ | - | 3.4 | 8 | mA |
| Threshold input voltage high to low |  | $\mathrm{V}_{\text {FHL }}$ | 0.8 | - | - | V |
| Input forward voltage | $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | $\mathrm{V}_{\mathrm{F}}$ | 1 | 1.36 | 1.6 | V |
| Temperature coefficient of forward voltage | $\mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | $\Delta \mathrm{V}_{\mathrm{F}} / \Delta \mathrm{T}_{\text {amb }}$ | - | -1.4 | - | $\mathrm{mV} /{ }^{\circ} \mathrm{C}$ |
| Input reverse breakdown voltage | $\mathrm{I}_{\mathrm{R}}=10 \mu \mathrm{~A}$ | $\mathrm{V}_{(\mathrm{BR})}$ | 5 | - | - | V |
| Input capacitance | $\mathrm{f}=1 \mathrm{MHz}, \mathrm{V}_{\mathrm{F}}=0 \mathrm{~V}$ | $\mathrm{C}_{\text {IN }}$ | - | 45 | - | pF |
| UVLO threshold | $\mathrm{V}_{\mathrm{O}} \geq 5 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | $\mathrm{V}_{\text {UVLO }}+$ | 11 | - | 13.5 | V |
|  |  | VUVLO - | 9.5 | - | 12 | V |
| UVLO hysteresis |  | UVLOHYS | - | 1.6 | - | V |
| Capacitance (input to output) | $\mathrm{f}=1 \mathrm{MHz}, \mathrm{V}_{\mathrm{F}}=0 \mathrm{~V}$ | $\mathrm{C}_{10}$ | - | 0.9 | - | pF |

## Note

- Minimum and maximum values were tested over recommended operating conditions $\left(T_{a m b}=-40^{\circ} \mathrm{C}\right.$ to $+100{ }^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{F}(\mathrm{ON})}=10 \mathrm{~mA}$ to 16 mA , $\mathrm{V}_{\mathrm{F}(\mathrm{OFF})}=-3 \mathrm{~V}$ to $0.8 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}=15 \mathrm{~V}$ to $32 \mathrm{~V}, \mathrm{~V}_{\mathrm{EE}}=$ ground) unless otherwise specified. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements. All typical values were measured at $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$ and with $\mathrm{V}_{\mathrm{CC}}-\mathrm{V}_{\mathrm{EE}}=32 \mathrm{~V}$.

VOW3120

| SWITCHING CHARACTERISTICS |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Propagation delay time to logic low output | $\mathrm{R}_{\mathrm{g}}=10 \Omega, \mathrm{C}_{\mathrm{g}}=10 \mathrm{nF}, \mathrm{f}=10 \mathrm{kHz}$, duty cycle $=50 \%$ | $\mathrm{t}_{\text {PHL }}$ | 0.1 | 0.25 | 0.5 | $\mu \mathrm{s}$ |
| Propagation delay time to logic high output | $\mathrm{R}_{\mathrm{g}}=10 \Omega, \mathrm{C}_{\mathrm{g}}=10 \mathrm{nF}, \mathrm{f}=10 \mathrm{kHz}$, duty cycle $=50 \%$ | $\mathrm{t}_{\text {PLH }}$ | 0.1 | 0.25 | 0.5 | $\mu \mathrm{s}$ |
| Pulse width distortion | $\mathrm{R}_{\mathrm{g}}=10 \Omega, \mathrm{C}_{\mathrm{g}}=10 \mathrm{nF}, \mathrm{f}=10 \mathrm{kHz}$, duty cycle $=50 \%$ | PWD | - | - | 0.3 | $\mu \mathrm{s}$ |
| Rise time | $\mathrm{R}_{\mathrm{g}}=10 \Omega, \mathrm{C}_{\mathrm{g}}=10 \mathrm{nF}, \mathrm{f}=10 \mathrm{kHz}$, duty cycle $=50 \%$ | $\mathrm{t}_{\mathrm{r}}$ | - | 0.1 | - | $\mu \mathrm{s}$ |
| Fall time | $\mathrm{R}_{\mathrm{g}}=10 \Omega, \mathrm{C}_{\mathrm{g}}=10 \mathrm{nF}, \mathrm{f}=10 \mathrm{kHz}$, duty cycle $=50 \%$ | $\mathrm{t}_{\mathrm{f}}$ | - | 0.1 | - | $\mu \mathrm{s}$ |
| UVLO turn on delay | $\mathrm{V}_{\mathrm{O}}>5 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | TuvLo-on | - | 0.8 | - | $\mu \mathrm{s}$ |
| UVLO turn off delay | $\mathrm{V}_{\mathrm{O}}<5 \mathrm{~V}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA}$ | TuvLO-OFF | - | 0.6 | - | $\mu \mathrm{s}$ |

## Note

- Minimum and maximum values were tested over recommended operating conditions $\left(T_{a m b}=-40^{\circ} \mathrm{C}\right.$ to $+100^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{F}(\mathrm{ON})}=10 \mathrm{~mA}$ to 16 mA , $\mathrm{V}_{\mathrm{F}(\mathrm{OFF})}=-3 \mathrm{~V}$ to $0.8 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}=15 \mathrm{~V}$ to $32 \mathrm{~V}, \mathrm{~V}_{\mathrm{EE}}=$ ground) unless otherwise specified. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements. All typical values were measured at $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$ and with $\mathrm{V}_{\mathrm{CC}}-\mathrm{V}_{\mathrm{EE}}=32 \mathrm{~V}$.


Fig. $2-t_{\text {PLH }}, t_{\text {PHL }}, t_{r}$ and $t_{f}$ Test Circuit and Waveforms

| COMMON MODE TRANSIENT IMMUNITY |  |  |  |  |  |  |
| :---: | :---: | :---: | :---: | :---: | :---: | :---: |
| PARAMETER | TEST CONDITION | SYMBOL | MIN. | TYP. | MAX. | UNIT |
| Common mode transient immunity at logic high output | $\begin{gathered} \mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{F}}=10 \mathrm{~mA} \text { to } 16 \mathrm{~mA}, \\ \mathrm{~V}_{\mathrm{CM}}=1500 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}=32 \mathrm{~V} \end{gathered}$ |  | 25 | 50 | - | kV/ $/ \mathrm{s}$ |
| Common mode transient immunity at logic low output | $\begin{gathered} \mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}, \mathrm{~V}_{\mathrm{CM}}=1500 \mathrm{~V}, \\ \mathrm{~V}_{\mathrm{CC}}=32 \mathrm{~V}, \mathrm{~V}_{\mathrm{F}}=0 \mathrm{~V} \end{gathered}$ | \|CM ${ }_{\text {L }}$ | 25 | 45 | - | kV/ $/$ s |

## Note

- Minimum and maximum values were tested over recommended operating conditions $\left(T_{a m b}=-40^{\circ} \mathrm{C}\right.$ to $+100^{\circ} \mathrm{C}, \mathrm{I}_{\mathrm{F}(\mathrm{ON})}=10 \mathrm{~mA}$ to 16 mA , $\mathrm{V}_{\mathrm{F}(\mathrm{OFF})}=-3 \mathrm{~V}$ to $0.8 \mathrm{~V}, \mathrm{~V}_{\mathrm{CC}}=15 \mathrm{~V}$ to $32 \mathrm{~V}, \mathrm{~V}_{\mathrm{EE}}=$ ground) unless otherwise specified. Typical values are characteristics of the device and are the result of engineering evaluations. Typical values are for information only and are not part of the testing requirements. All typical values were measured at $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}$ and with $\mathrm{V}_{\mathrm{CC}}-\mathrm{V}_{\mathrm{EE}}=32 \mathrm{~V}$.


Switch at $A: I_{F}=10 \mathrm{~mA}$


Fig. 3-CMR Test Circuit and Waveforms

| PARAMETER | TEST CONDITION | SYMBOL | VALUE | UNIT |
| :---: | :---: | :---: | :---: | :---: |
| Climatic classification | According to IEC 68 part 1 |  | 40/100 / 21 |  |
| Pollution degree | According to DIN VDE 0109 |  | 2 |  |
| Comparative tracking index | Insulation group IIIa | CTI | 250 |  |
| Maximum rated withstanding isolation voltage | According to UL1577, $\mathrm{t}=1 \mathrm{~min}$ | $\mathrm{V}_{\text {ISO }}$ | 5300 | $\mathrm{V}_{\text {RMS }}$ |
| Maximum transient isolation voltage | According to DIN EN 60747-5-5 | $\mathrm{V}_{\text {IOTM }}$ | 8000 | $V_{\text {peak }}$ |
| Maximum repetitive peak isolation voltage | According to DIN EN 60747-5-5 | V ${ }_{\text {IORM }}$ | 1414 | $\mathrm{V}_{\text {peak }}$ |
| Isolation resistance | $\mathrm{T}_{\mathrm{amb}}=25^{\circ} \mathrm{C}, \mathrm{V}_{10}=500 \mathrm{~V}$ | $\mathrm{R}_{\mathrm{IO}}$ | $\geq 10^{12}$ | $\Omega$ |
|  | $\mathrm{T}_{\text {amb }}=10{ }^{\circ} \mathrm{C}, \mathrm{V}_{\mathrm{IO}}=500 \mathrm{~V}$ | $\mathrm{R}_{\mathrm{IO}}$ | $\geq 10^{11}$ | $\Omega$ |
| Output safety power |  | Pso | 800 | mW |
| Input safety current |  | $\mathrm{I}_{\mathrm{S}}$ | 350 | mA |
| Input safety temperature |  | TS | 175 | ${ }^{\circ} \mathrm{C}$ |
| Creepage distance | DIP-8, widebody, 400 mil |  | $\geq 10$ | mm |
| Clearance distance |  |  | $\geq 10$ | mm |
| Creepage distance | SMD-8, widebody, 400 mil (option 7) |  | $\geq 10$ | mm |
| Clearance distance |  |  | $\geq 10$ | mm |
| Insulation thickness |  | DTI | $\geq 0.4$ | mm |
| Input to output test voltage, method B | $\mathrm{V}_{\text {IORM }} \times 1.875=\mathrm{V}_{\mathrm{PR}}, 100 \%$ production test with $\mathrm{t}_{\mathrm{M}}=1 \mathrm{~s}$, partial discharge $<5 \mathrm{pC}$ | $V_{\text {PR }}$ | 2651 | $V_{\text {peak }}$ |
| Input to output test voltage, method A | $\mathrm{V}_{\text {IORM }} \times 1.6=\mathrm{V}_{\mathrm{PR}}, 100 \%$ sample test with $\mathrm{t}_{\mathrm{M}}=10 \mathrm{~s}$, partial discharge $<5 \mathrm{pC}$ | $V_{\text {PR }}$ | 2262 | $V_{\text {peak }}$ |

## Note

- According to DIN EN60747-5-5 (see figure 4). This optocoupler is suitable for safe electrical isolation only within the safety ratings. Compliance with the safety ratings shall be ensured by means of suitable protective circuits.


Fig. 4 - Safety Power Dissipation vs. Ambient Temperature


Fig. 5 - Safety Input Current vs. Ambient Temperature

TYPICAL CHARACTERISTICS $\left(T_{\text {amb }}=25^{\circ} \mathrm{C}\right.$, unless otherwise specified)


Fig. 6 - High Output Voltage Drop vs. Ambient Temperature


Fig. 7 - Output High Current vs. Ambient Temperature


Fig. 8 - Low Level Output Voltage vs. Ambient Temperature


Fig. 9 - Output Low Current vs. Ambient Temperature


Fig. 10 - Output Low Voltage vs. Output Low Current


Fig. 11 - Output High Voltage vs. Output High Current


Fig. 12 - Supply Current vs. Ambient Temperature


Fig. 13 - Supply Current vs. Supply Voltage


Fig. 14 - threshold Current vs. Ambient Temperature


Fig. 15 - Output Voltage vs. Forward Current


Fig. 16 - Propagation Delay vs. Supply Voltage


Fig. 17 - Propagation Delay vs. Ambient Temperature


Fig. 18 - Propagation Delay vs. Forward Current


Fig. 19 - Propagation Delay vs. Series Load Resistance


Fig. 20 - Propagation Delay vs. Series Load Capacitance


Fig. 21 - Forward Current vs. Forward Voltage

## PACKAGE DIMENSIONS (in millimeters)

DIP-8, Widebody, 400 mil


SMD-8, Widebody, 400 mil (option 7)


## PACKAGE MARKING



Fig. 22 - Example of VOW3120-X017T

## Notes

- The VDE logo is only marked on option 1 parts.
- Tape and reel (T) and package option (option 7) is not part of the package markings.

PACKING INFORMATION (in millimeters)

## Tubes



| DEVICE PER TUBE |  |  |  |
| :--- | :---: | :---: | :---: |
| TYPE | UNITS/TUBE | TUBE/BOX | UNITS/BOX |
| DIP-8, widebody, 400 mil | 40 | 30 | 1200 |

## DIP-8, Widebody, 400 mil



1. ALL TUBE TOLERANCES TO BE $\pm 0.25$


ONLY, UNLESS OTHERWISE SPECIFIED.
Fig. 23 - Tape and Reel Shipping Medium

## Tape and Reel



Fig. 24 - Tape and Reel Shipping Medium


Fig. 25 - Tape and Reel Shipping Medium

## SMD-8, Widebody, 400 mil (option 7)



Fig. 26 - Tape and Reel Packing (750 parts per reel)

## SOLDER PROFILES



Fig. 27 - Wave Soldering Double Wave Profile According to J-STD-020 for DIP Devices


Fig. 28 - Lead (Pb)-free Reflow Solder Profile According to J-STD-020 for SMD Devices

## HANDLING AND STORAGE CONDITIONS

ESD level: HBM class 2
Floor life: unlimited
Conditions: $\mathrm{T}_{\text {amb }}<30^{\circ} \mathrm{C}, \mathrm{RH}<85 \%$
Moisture sensitivity level 1 , according to J-STD-020

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