Vishay Semiconductors

High Performance Schottky Rectifier, 1.0 A

FEATURES

Low forward voltage drop



COMPLIANT

HALOGEN

FREE

- · Guard ring for enhanced ruggedness and long term reliability
- · Small foot print, surface mountable
- · High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Material categorization: for definitions of compliance please see www.vishay.com/doc?99912

DESCRIPTION

The VS-10BQ040-M3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS					
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UNITS			
I _{F(AV)}	Rectangular waveform	1.0	A		
V _{RRM}		40	V		
I _{FSM}	t _p = 5 μs sine	430	A		
V _F	1.0 A _{pk} , T _J = 125 °C	0.38	V		
TJ	Range	-55 to +150	°C		

VOLTAGE RATINGS					
PARAMETER	SYMBOL	VS-10BQ040-M3	UNITS		
Maximum DC reverse voltage	V _R	40	V		
Maximum working peak reverse voltage	V _{RWM}	40	v		

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
Maximum average forward current	I _{F(AV)}	50 % duty cycle at T_L = 122 °C, rectangular waveform		1.0	А
Maximum peak one cycle non-repetitive surge current	I _{FSM}	5 μs sine or 3 μs rect. pulse	Following any rated load condition and with	430	A
		10 ms sine or 6 ms rect. pulse	rated V_{RRM} applied	40	
Non-repetitive avalanche energy	E _{AS}	T _J = 25 °C, I _{AS} = 1 A, L = 6 mH		3.0	mJ
Repetitive avalanche current	I _{AR}			1.0	А

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Document Number: 93335





PRODUCT SUMMARY

Package

 $I_{F(AV)}$

 V_R

V_F at I_F

 I_{RM}

T_J max.

Diode variation

E_{AS}

Cathode

0

SMB

1.0 A

40 V

0.38 V

9 mA at 125 °C

150 °C

Single die

3.0 mJ

Anode

-0



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ELECTRICAL SPECIFICATIONS					
PARAMETER	SYMBOL	TEST CONDITIONS		VALUES	UNITS
	V _{FM} ⁽¹⁾	1 A	- T _J = 25 °C	0.45	V
Maximum forward voltage drop		2 A		0.52	
See fig. 1		1 A	T _J = 125 °C	0.38	
		2 A		0.50	
Maximum reverse leakage current	I _{RM}	T _J = 25 °C		0.1	mA
See fig. 2		T _J = 125 °C	V _R = Rated V _R	9.0	
Typical junction capacitance	CT	$V_{\rm R}$ = 5 $V_{\rm DC}$, (test signal range 100 kHz to 1 MHz), 25 °C		115	pF
Typical series inductance	L _S	Measured lead to lead 5 mm from package body		2.0	nH
Maximum voltage rate of charge	dV/dt	Rated V _R		10 000	V/µs

Note

 $^{(1)}\,$ Pulse width = 300 $\mu s,$ duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS				
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS
Maximum junction and storage temperature range	T _J ⁽¹⁾ , T _{Stg}		-55 to +150	°C
Maximum thermal resistance, junction to lead	R _{thJL} ⁽²⁾	DC operation	36	°C/W
Maximum thermal resistance, junction to ambient	R _{thJA}		80	C/W
			0.10	g
Approximate weight			0.003	oz.
Marking device	Case style SMB (similar DO-214AA) 1F		F	

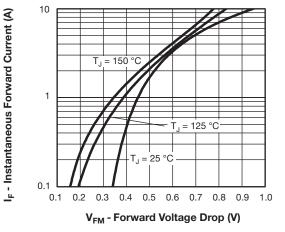
Notes

(1) $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$ thermal runaway condition for a diode on its own heatsink

⁽²⁾ Mounted 1" square PCB

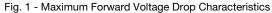


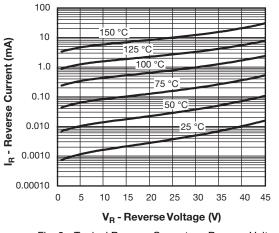
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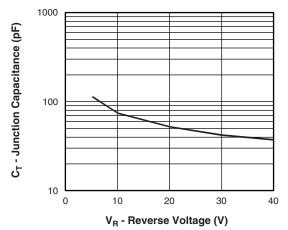
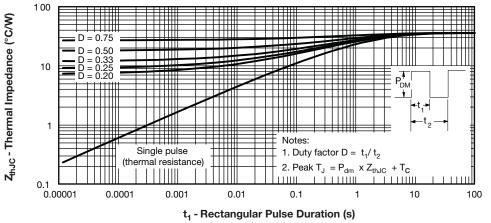
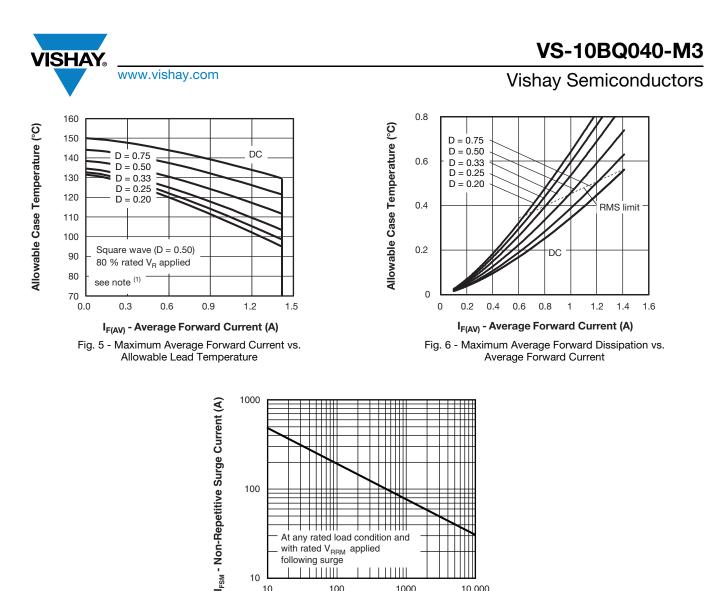


Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage







At any rated load condition and with rated V_{RRM} applied following surge

100

t_p - Square Wave Pulse Duration (μs) Fig. 7 - Maximum Peak Surge Forward Current vs. Pulse Duration

1000

10 000

Note

(1) Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; $\begin{array}{l} \mathsf{Pd} = \mathsf{Forward} \ \mathsf{power} \ \mathsf{loss} = \mathsf{I}_{\mathsf{F}(\mathsf{AV})} \times \mathsf{V}_{\mathsf{FM}} \ \mathsf{at} \ (\mathsf{I}_{\mathsf{F}(\mathsf{AV})}/\mathsf{D}) \ (\mathsf{see fig. 6}); \\ \mathsf{Pd}_{\mathsf{REV}} = \mathsf{Inverse} \ \mathsf{power} \ \mathsf{loss} = \mathsf{V}_{\mathsf{R1}} \times \mathsf{I}_{\mathsf{R}} \ (\mathsf{1} - \mathsf{D}); \ \mathsf{I}_{\mathsf{R}} \ \mathsf{at} \ \mathsf{V}_{\mathsf{R1}} = \mathsf{80} \ \% \ \mathsf{rated} \ \mathsf{V}_{\mathsf{R}} \end{array}$

100

10

10

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VISHA

Device code	VS-	10	в	Q	040	-M3
	1	2	3	4	5	6
	 Vishay Semiconductors productors Current rating B = SMB 			oduct		
	4 - Q = Schottky "Q" series					
	5 -	Vol	tage rati	ng (040	= 40 V))
	6 -	- Environmental digit:				
						P -

-M3 = Halogen-free, RoHS compliant and terminations lead (Pb)-free

ORDERING INFORMATION (Example)					
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION				
VS-10BQ040-M3/5BT	5BT	3200	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions www.vishay.com/doc?95401				
Part marking information	www.vishay.com/doc?95403			
Packaging information	www.vishay.com/doc?95404			



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