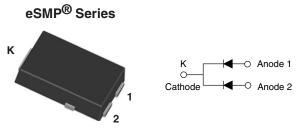
Vishay Semiconductors

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Hyperfast Rectifier, 2 x 5 A FRED Pt[®]



SMPC (TO-277A)

LINKS TO ADDITIONAL RESOURCES



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PRIMARY CHARACTERISTICS				
I _{F(AV)}	2 x 5 A			
V _R	100 V			
V _F at I _F	0.75 V			
t _{rr (typ.)}	25 ns			
T _J max.	175 °C			
Package	SMPC (TO-277A)			
Circuit configuration	Common cathode			

FEATURES

- Hyperfast recovery time, reduced Q_{rr}, and soft recovery
- 175 °C maximum operating junction temperature
- Specified for output and snubber operation
- Low forward voltage drop
- · Low leakage current
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 $^{\circ}\mathrm{C}$
- Meets JESD 201 class 2 whisker test
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

DESCRIPTION / APPLICATIONS

State of the art hyperfast recovery rectifiers specifically designed with optimized performance of forward voltage drop and hyperfast recovery time.

The planar structure and the platinum doped life time control guarantee the best overall performance, ruggedness, and reliability characteristics.

These devices are intended for use in snubber, boost, as high frequency rectifiers and freewheeling diodes.

The extremely optimized stored charge and low recovery current minimize the switching losses and reduce power dissipation in the switching element.

MECHANICAL DATA

Case: SMPC (TO-277A)

Molding compound meets UL 94 V-0 flammability rating Halogen-free, RoHS-compliant

Terminals: matte tin plated leads, solderable per J-STD-002

ABSOLUTE MAXIMUM RATINGS					
PARAMETER		SYMBOL	TEST CONDITIONS	VALUES	UNITS
Peak repetitive reverse voltage		V _{RRM}		100	V
Average rectified forward current	per device	I _{F(AV)}	T _{Sp} = 155 °C	10	•
Average rectilied forward current	per diode			5	
Non ropotitivo poak aurra aurrant	per device	I _{FSM}	T _J = 25 °C	130	A
Non-repetitive peak surge current	per diode			70	1
Operating junction and storage temperatures		T _J , T _{Stg}		-55 to +175	°C

ELECTRICAL SPECIFICATIONS (T _J = 25 °C unless otherwise specified)						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Breakdown voltage, blocking voltage	V_{BR}, V_{R}	I _R = 100 μA	100	-	-	
Forward voltage, per diode	V _F	I _F = 5 A	-	0.92	0.98	V
		I _F = 5 A, T _J = 150 °C	-	0.75	0.82	
Devenue la classe commente a cu dia da	I _R	$V_{R} = V_{R}$ rated	-	-	2	
Reverse leakage current, per diode		$T_J = 150 \text{ °C}, V_R = V_R \text{ rated}$	-	4	80	μA
Junction capacitance	CT	V _R = 100 V	-	18	-	pF

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COMPLIANT

HALOGEN

FREE



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DYNAMIC RECOVERY CHARACTERISTICS ($T_J = 25 \text{ °C}$ unless otherwise specified)							
PARAMETER	SYMBOL	TEST CONDITIONS		MIN.	TYP.	MAX.	UNITS
		$I_F = 1 \text{ A}, \text{ d}I_F/\text{d}t = 50 \text{ A}/\mu\text{s}, \text{ V}_R = 30 \text{ V}$		-	25	-	
Reverse recovery time t,	+	I _F = 0.5 A, I _R = 1 A, I _{rr} = 0.25 A		-	-	25	
	t _{rr}	T _J = 25 °C		-	18	-	ns
		T _J = 125 °C		-	28	-	
Peak recovery current I _{RRM}	T _J = 25 °C	$I_F = 5 A$	-	2	-	Α	
	IRRM	T _J = 125 °C	dI _F /dt = 200 A/µs V _R = 160 V	-	3.8	-	A
Reverse recovery charge Q _{rr}	0	T _J = 25 °C		-	18	-	nC
	Q _{rr}	T _J = 125 °C		-	53	-	no

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	MIN.	TYP.	MAX.	UNITS
Maximum junction and storage temperature range	T _J , T _{Stg}		-55	-	175	°C
Thermal resistance, junction to mount, per leg	R _{thJM}		-	2.5	3.5	°C/W
Thermal resistance, junction to ambient, per leg	R _{thJA}		-	80	-	°C/W
Approximate weight				0.1		g
Approximate weight				0.0035		oz.
Marking device		Case style SMPC (TO-277A)		SC	CH1	

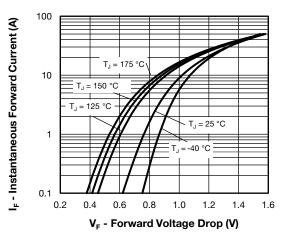


Fig. 1 - Typical Forward Voltage Drop Characteristics

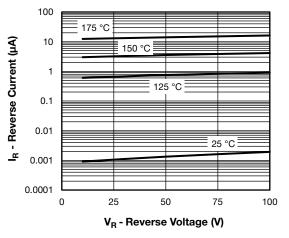
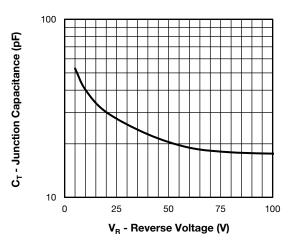


Fig. 2 - Typical Values of Reverse Current vs. Reverse Voltage

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Fig. 3 - Typical Junction Capacitance vs. Reverse Voltage

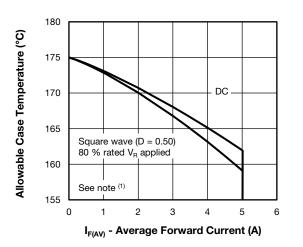


Fig. 4 - Maximum Allowable Case Temperature vs. Average Forward Current

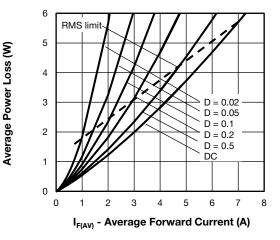


Fig. 5 - Forward Power Loss Characteristics

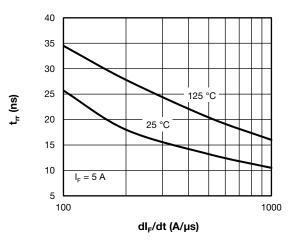


Fig. 6 - Typical Reverse Recovery Time vs. dI_F/dt

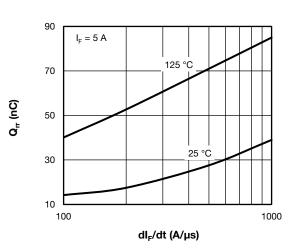


Fig. 7 - Typical Stored Charge vs. dl_F/dt

Note (1)

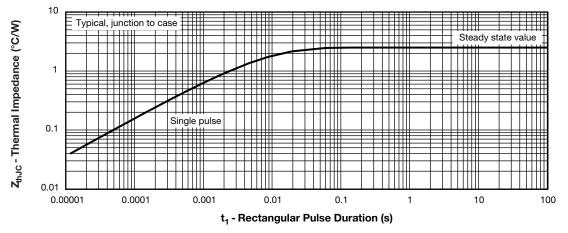
Formula used: $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$; Pd = forward power loss = $I_{F(AV)} \times V_{FM}$ at $(I_{F(AV)}/D)$ (see fig. 5); Pd_{REV} = inverse power loss = $V_{R1} \times I_R$ (1 - D); I_R at V_{R1} = rated V_R

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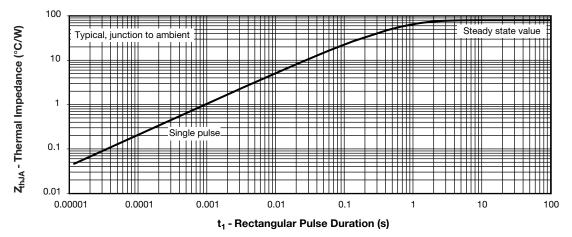
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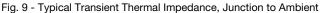


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Fig. 8 - Typical Transient Thermal Impedance, Junction to Case





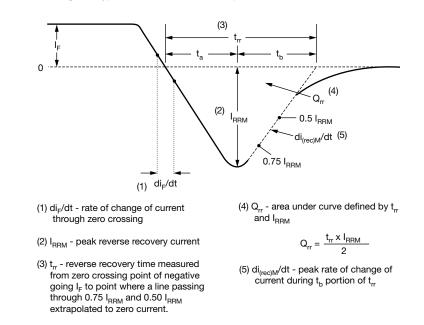


Fig. 10 - Reverse Recovery Waveform and Definitions

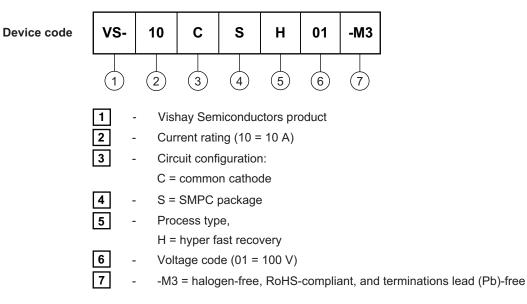
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ORDERING INFORMATION TABLE

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ORDERING INFORMATION (Example)					
PREFERRED P/N	QUANTITY PER REEL	MINIMUM ORDER QUANTITY	PACKAGING DESCRIPTION		
VS-10CSH01-M3/86A	1500	1500	7" diameter plastic tape and reel		
VS-10CSH01-M3/87A	6500	6500	13" diameter plastic tape and reel		

LINKS TO RELATED DOCUMENTS				
Dimensions	www.vishay.com/doc?95570			
Part marking information	www.vishay.com/doc?95565			
Packaging information	www.vishay.com/doc?88869			
SPICE model	www.vishay.com/doc?96095			

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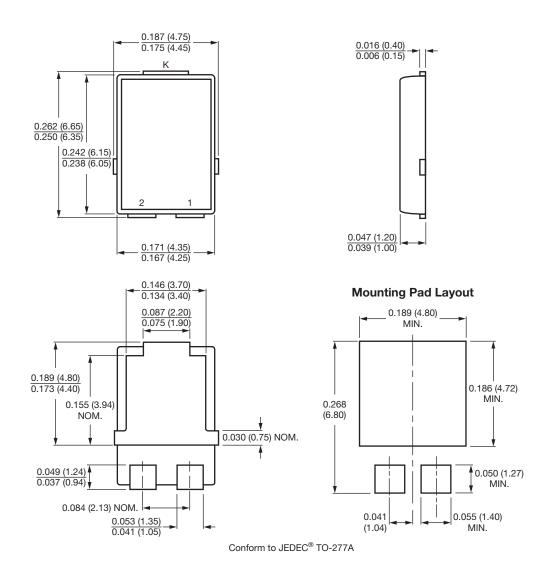
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TO-277A (SMPC)

DIMENSIONS in inches (millimeters)





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