**Vishay Semiconductors** 

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# High Performance Schottky Rectifier, 1 A



Cathode	Anode
0	O

DO-214AC (SMA)

PRODUCT SUMMARY					
Package	DO-214AC (SMA)				
I <sub>F(AV)</sub>	1 A				
V <sub>R</sub>	60 V				
V <sub>F</sub> at I <sub>F</sub>	0.57 V				
I <sub>RM</sub>	7.5 mA at 125 °C				
T <sub>J</sub> max.	150 °C				
Diode variation	Single die				
E <sub>AS</sub>	2.0 mJ				

### **FEATURES**

- Low forward voltage drop
- Guard ring for enhanced ruggedness and long term reliability
- Small footprint, surface mountable
- High frequency operation
- Meets MSL level 1, per J-STD-020, LF maximum peak of 260 °C
- Meets JESD 201 class 2 whisker test
- AEC-Q101 qualified
- Material categorization: for definitions of compliance please see <u>www.vishay.com/doc?99912</u>

### DESCRIPTION

The VS-10MQ060HM3 surface mount Schottky rectifier has been designed for applications requiring low forward drop and very small foot prints on PC boards. Typical applications are in disk drives, switching power supplies, converters, freewheeling diodes, battery charging, and reverse battery protection.

MAJOR RATINGS AND CHARACTERISTICS						
SYMBOL	CHARACTERISTICS	CHARACTERISTICS VALUES UN				
I <sub>F(AV)</sub>	Rectangular waveform	1	А			
V <sub>RRM</sub>		60	V			
I <sub>FSM</sub>	t <sub>p</sub> = 5 μs sine	40	А			
V <sub>F</sub>	1.5 A <sub>pk</sub> , T <sub>J</sub> = 125 °C	0.63	V			
TJ	Range	-55 to +150	°C			

VOLTAGE RATINGS						
PARAMETER	SYMBOL	VS-10MQ060HM3	UNITS			
Maximum DC reverse voltage	V <sub>R</sub>	60	V			
Maximum working peak reverse voltage	V <sub>RWM</sub>	00	v			

ABSOLUTE MAXIMUM RATINGS					
PARAMETER	SYMBOL	TEST CONDI	TIONS	VALUES	UNITS
Maximum average forward current		50 % duty cycle at $T_L = 120$ °C, rectangular waveform On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad area)		1.5	A
See fig. 4	IF(AV)	50 % duty cycle at $T_L = 129$ °C On PC board 9 mm <sup>2</sup> island (0.013 mm thick copper pad are	1		
Maximum peak one cycle		5 µs sine or 3 µs rect. pulse	Following any rated load condition and with	40	А
non-repetitive surge current I <sub>FSM</sub> See fig. 6		10 ms sine or 6 ms rect. pulse	rated $V_{RRM}$ applied	10	A
Non-repetitive avalanche energy	E <sub>AS</sub>	T <sub>J</sub> = 25 °C, I <sub>AS</sub> = 1 A, L = 4 mH		2.0	mJ
Repetitive avalanche current	I <sub>AR</sub>	Current decaying linearly to zero in 1 $\mu$ s Frequency limited by T <sub>J</sub> maximum V <sub>A</sub> = 1.5 x V <sub>R</sub> typical 1.0		1.0	А

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ELECTRICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CO	ONDITIONS	VALUES	UNITS	
		1 A	T <sub>.1</sub> = 25 °C	0.63	V	
Maximum forward voltage drop	V <sub>EM</sub> <sup>(1)</sup>	1.5 A	1j=23 C	0.71		
See fig. 1	VFM (*)	1 A	T <sub>.1</sub> = 125 °C	0.57		
		1.5 A	1j = 125 C	0.63		
Maximum reverse leakage current	le	T <sub>J</sub> = 25 °C		0.5	mA	
See fig. 2	I <sub>RM</sub>	T <sub>J</sub> = 125 °C	V <sub>R</sub> = Rated V <sub>R</sub>	7.5		
Threshold voltage	V <sub>F(TO)</sub>	$T_J = T_J$ maximum		0.45	V	
Forward slope resistance	r <sub>t</sub>			86.8	mΩ	
Typical junction capacitance	CT	$V_R = 10 V_{DC}, T_J = 25 \text{ °C}, \text{ test signal} = 1 \text{ MHz}$ 3		31	pF	
Typical series inductance	L <sub>S</sub>	Measured lead to lead 5 mm from package body 2.0		nH		
Maximum voltage rate of change	dV/dt	Rated V <sub>R</sub> 10 000		V/µs		

#### Note

 $^{(1)}$  Pulse width = 300  $\mu s,$  duty cycle = 2 %

THERMAL - MECHANICAL SPECIFICATIONS						
PARAMETER	SYMBOL	TEST CONDITIONS	VALUES	UNITS		
Maximum junction and storage temperature range	T <sub>J</sub> <sup>(1)</sup> , T <sub>Stg</sub>		-55 to +150	°C		
Maximum thermal resistance, junction to ambient	R <sub>thJA</sub>	DC operation	80	°C/W		
Approximate weight			0.07	g		
Approximate weight			0.002	oz.		
Marking device		Case style SMA (similar D-64)	11	Η		

#### Note

(1)  $\frac{dP_{tot}}{dT_J} < \frac{1}{R_{thJA}}$  thermal runaway condition for a diode on its own heatsink



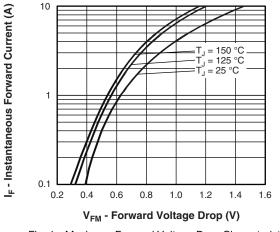
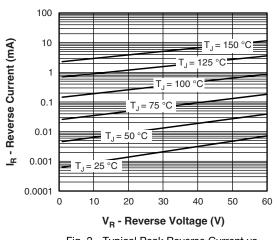
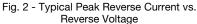
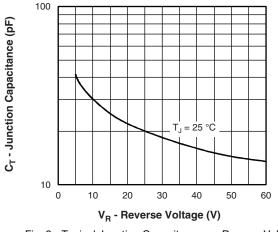
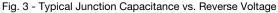


Fig. 1 - Maximum Forward Voltage Drop Characteristics









## VS-10MQ060HM3

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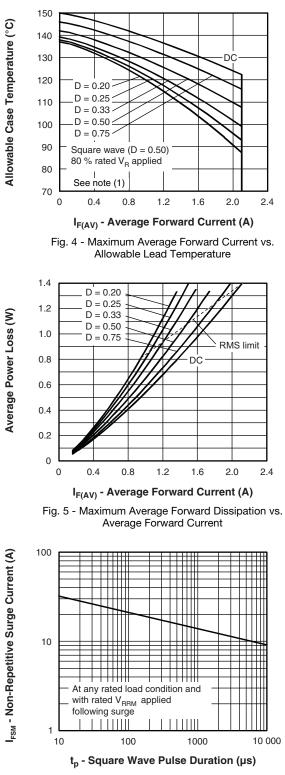


Fig. 6 - Maximum Peak Surge Forward Current vs. Pulse Duration

#### Note

<sup>(1)</sup> Formula used:  $T_C = T_J - (Pd + Pd_{REV}) \times R_{thJC}$ ;

Pd = Forward power loss = I<sub>F(AV)</sub> x V<sub>FM</sub> at (I<sub>F(AV)</sub>/D) (see fig. 6); Pd<sub>REV</sub> = Inverse power loss = V<sub>R1</sub> x I<sub>R</sub> (1 - D); I<sub>R</sub> at V<sub>R1</sub> = 80 % rated V<sub>R</sub>

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### **ORDERING INFORMATION TABLE**

Device code	VS-	10	м	Q	060	н	М3
		2	3	4	5	6	7
	1	- Visl	nay Sen	niconduo	ctors pro	oduct	
	2	- Cur	rent rati	ng			
	3	- M =	SMA				
	4	- Q =	Schottk	ky "Q" se	eries		
	5	- Vol	tage rati	ng (060	= 60 V)		
	6	. н=	AEC-Q	101 qua	lified		
	7	- Env	vironmer	ntal digit	:		
		М3	= Halog	en-free,	RoHS-	complia	int and t

ORDERING INFORMATION (Example)						
PREFERRED P/N	PREFERRED PACKAGE CODE MINIMUM ORDER QUANTITY PACKAGING DESCRIPTION					
VS-10MQ060HM3/5AT	5AT	7500	13" diameter plastic tape and reel			

LINKS TO RELATED DOCUMENTS			
Dimensions	www.vishay.com/doc?95400		
Part marking information	www.vishay.com/doc?95403		
Packaging information	www.vishay.com/doc?95404		



## **Outline Dimensions**

### **Vishay Semiconductors**

SMA

### **DIMENSIONS** in inches (millimeters)

DO-214AC (SMA)





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